

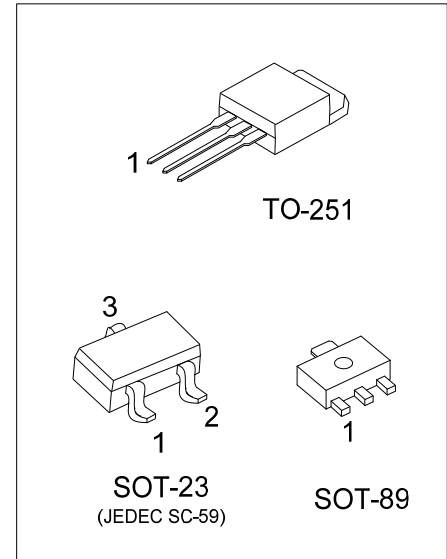
**08N50-CB****Power MOSFET****0.8A, 500V N-CHANNEL
POWER MOSFET****DESCRIPTION**

The UTC **08N50-CB** is a high voltage MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

FEATURES

* $R_{DS(on)} \leq 12 \Omega$ @ $V_{GS}=10V$, $I_D=0.4A$

* High breakdown voltage

**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
08N50L-AB3-R	08N50G-AB3-R	SOT-89	G	D	S	Tape Reel
08N50L-AE3-R	08N50G-AE3-R	SOT-23	G	S	D	Tape Reel
08N50L-TM3-T	08N50G-TM3-T	TO-251	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

08N50G-AB3-R (1) Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel, T: Tube (2) AB3: SOT-89, AE3: SOT-23, TM3: TO-251 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

PACKAGE	MARKING
SOT-89	Lot Code Date Code L: Lead Free G: Halogen Free
SOT-23	L: Lead Free G: Halogen Free
TO-251	Lot Code 08N50 L: Lead Free G: Halogen Free Date Code

■ **ABSOLUTE MAXIMUM RATINGS** ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	0.8	A
	Pulsed (Note 2)	I_{DM}	3.2	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	6.5	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.6	V/ns
Power Dissipation	SOT-89	P_D	3.3	W
	SOT-23($T_A = 25^\circ\text{C}$)		0.3	W
	TO-251		25	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature Range		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 1.14\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 1.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ **THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-89	θ_{JA}	160	$^\circ\text{C}/\text{W}$
	SOT-23		416	$^\circ\text{C}/\text{W}$
	TO-251		110	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-89	θ_{JC}	38	$^\circ\text{C}/\text{W}$
	TO-251		5	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

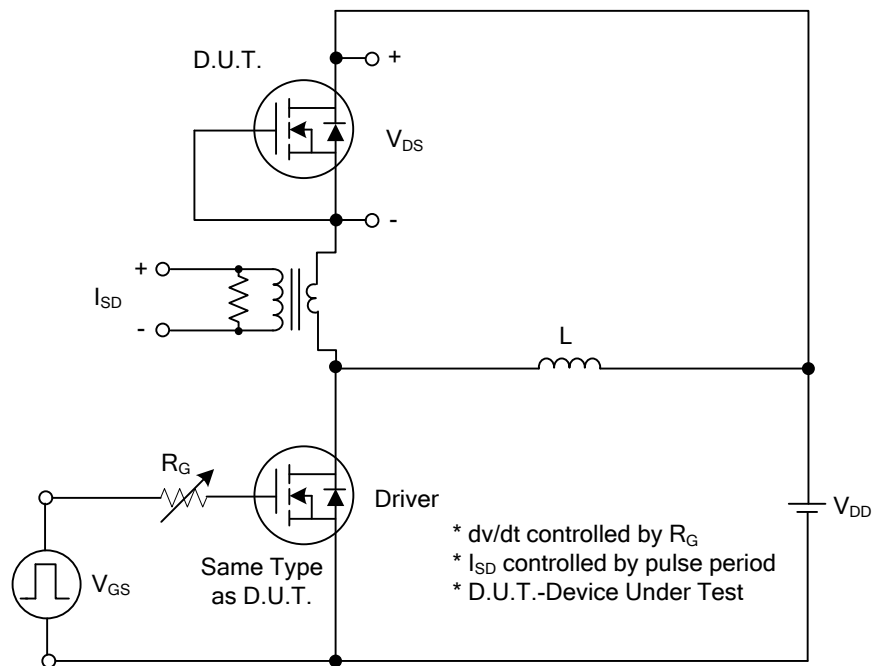
■ **ELECTRICAL CHARACTERISTICS** ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	500			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =500V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.4A			12	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		80		pF
Output Capacitance		C _{OSS}			15		pF
Reverse Transfer Capacitance		C _{RSS}			2		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DS} =100V, V _{GS} =10V, I _D =0.8A, I _D =1mA (Note 1, 2)		6		nC
Gate to Source Charge		Q _{GS}			3.2		nC
Gate to Drain Charge		Q _{GD}			0.35		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =0.8A, R _G =6Ω (Note 1, 2)		2.8		ns
Rise Time		t _R			16.7		ns
Turn-OFF Delay Time		t _{D(OFF)}			18.4		ns
Fall-Time		t _F			34		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				0.8	A
Maximum Body-Diode Pulsed Current		I _{SM}				3.2	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =0.8A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)		t _{rr}	I _S =1.0A, V _{GS} =0V,		146		ns
Body Diode Reverse Recovery Charge		Q _{rr}	dI _F /dt =100A/μs		265		nC

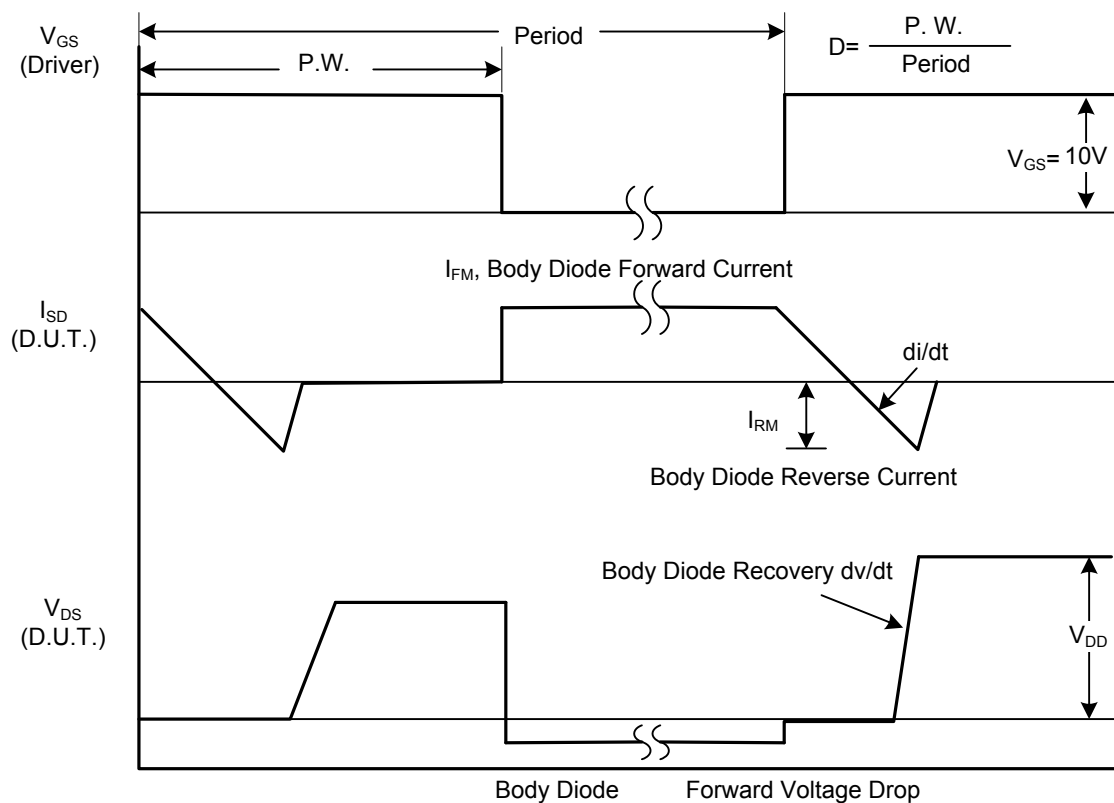
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycles $\leq 2\%$.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS

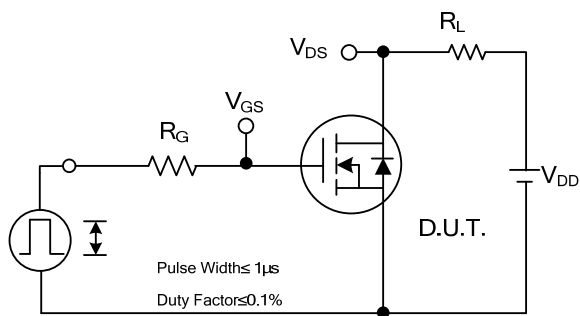


Peak Diode Recovery dv/dt Test Circuit

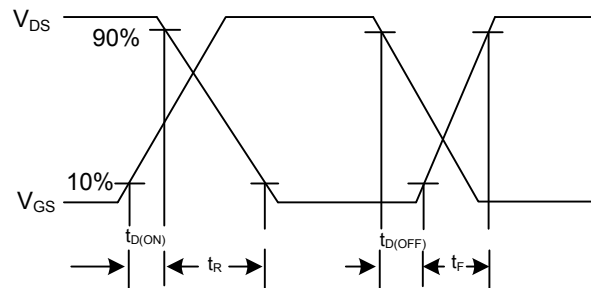


Peak Diode Recovery dv/dt Waveforms

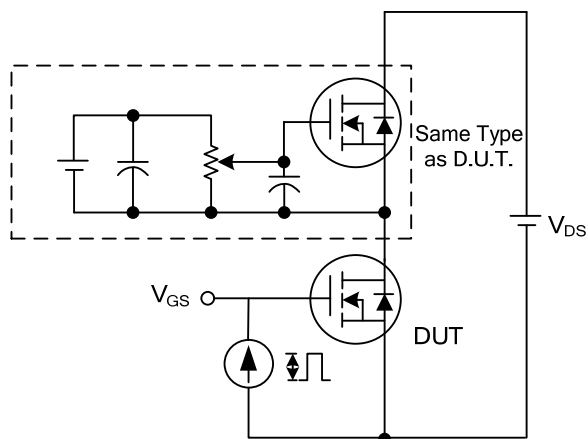
■ TEST CIRCUITS AND WAVEFORMS



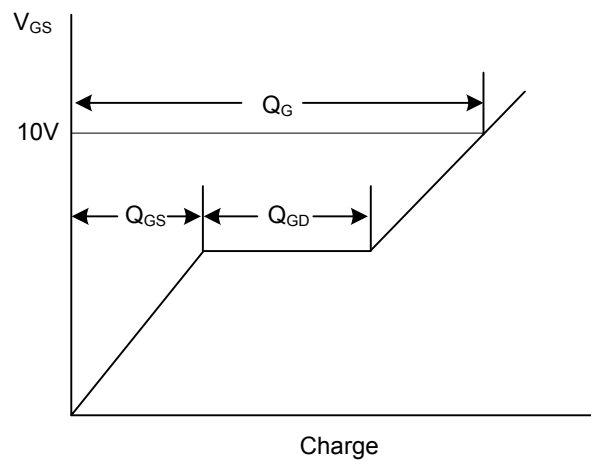
Switching Test Circuit



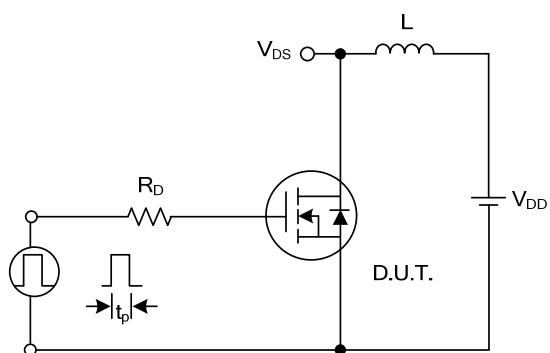
Switching Waveforms



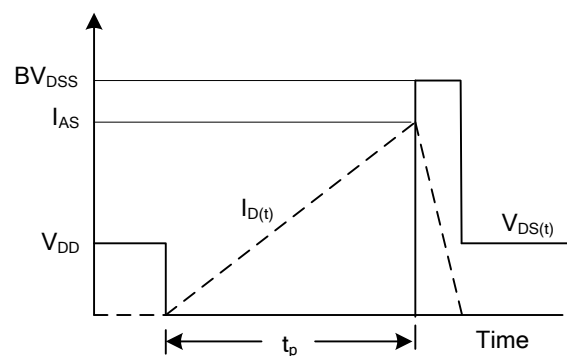
Gate Charge Test Circuit



Gate Charge Waveform

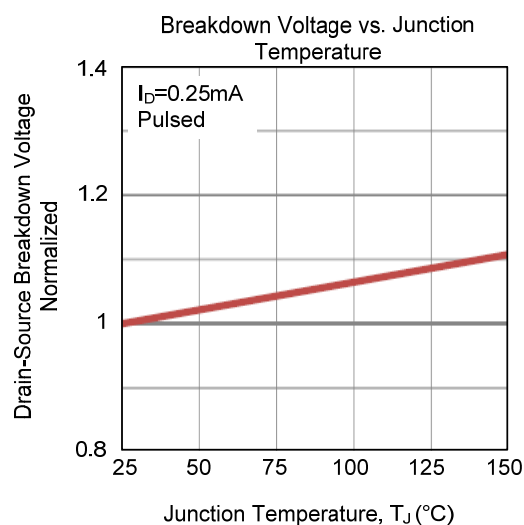
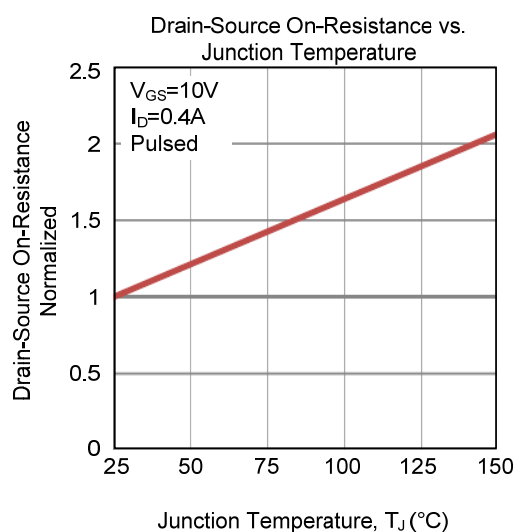
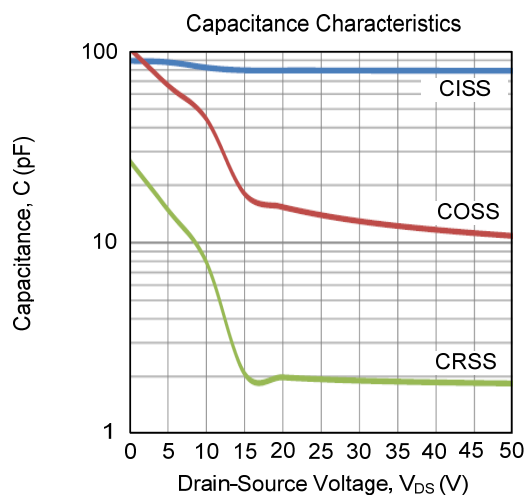
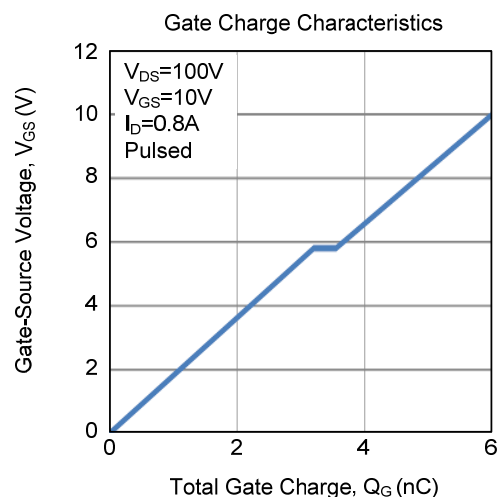
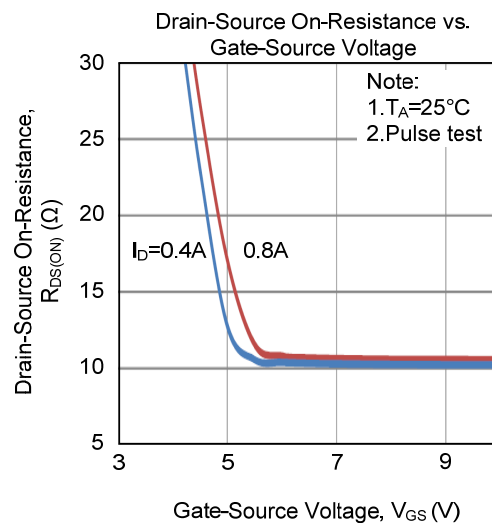
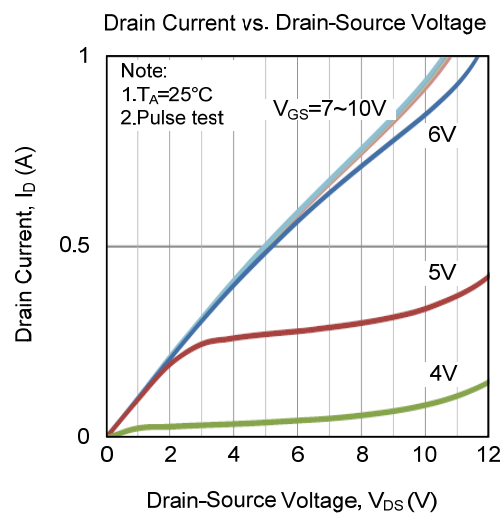


Unclamped Inductive Switching Test Circuit

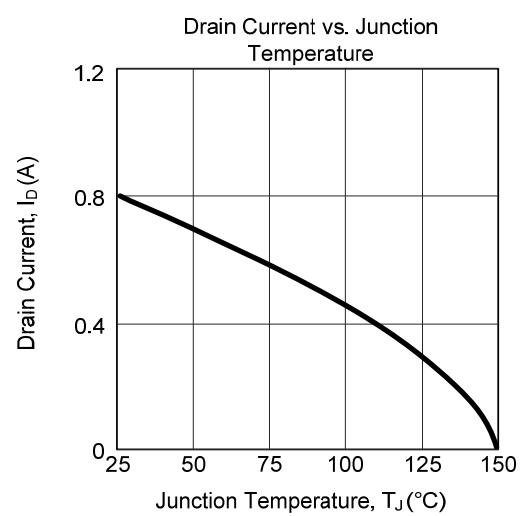
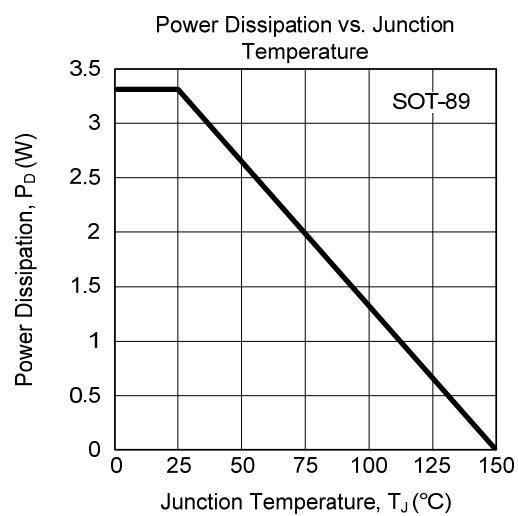
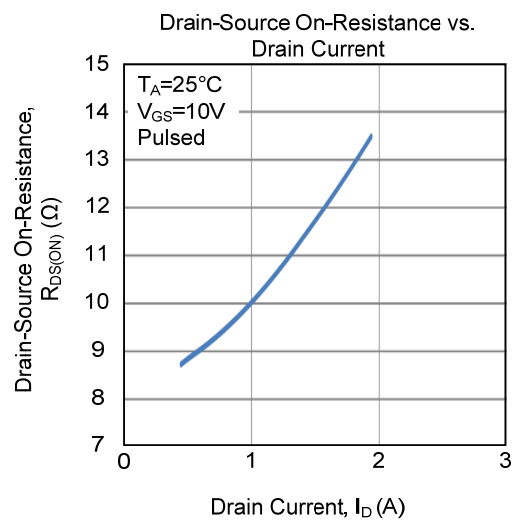
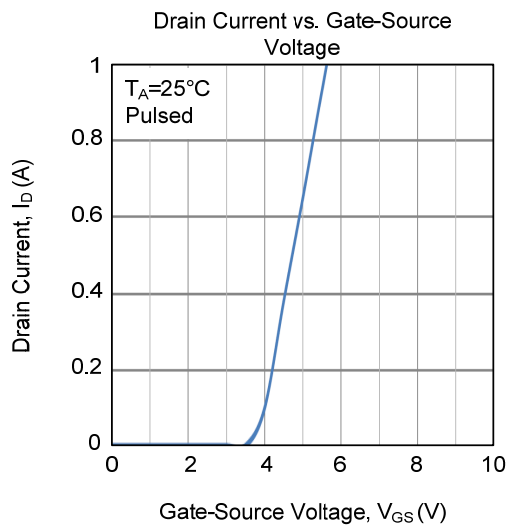
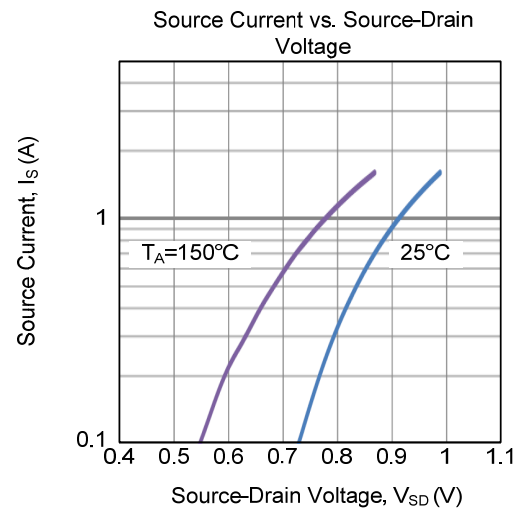
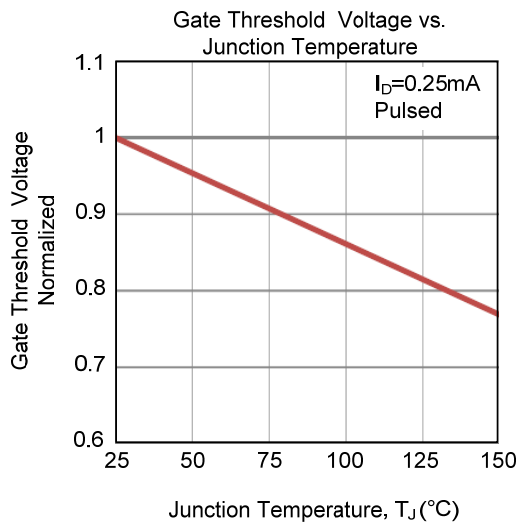


Unclamped Inductive Switching Waveforms

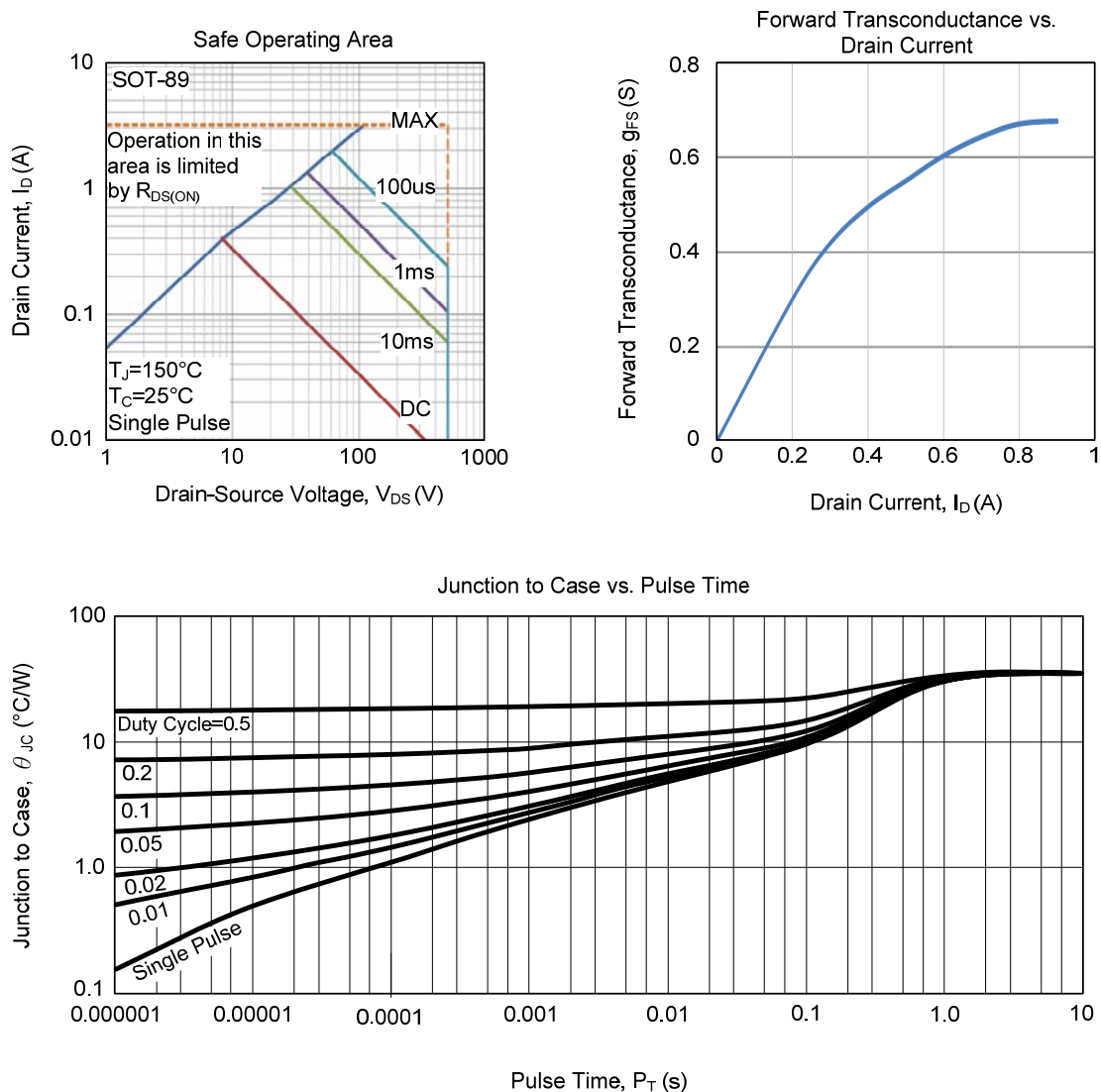
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



TYPICAL CHARACTERISTICS (Cont.)



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