

12NM120

Power MOSFET

12A, 1200V N-CHANNEL
SUPER-JUNCTION MOSFET

■ DESCRIPTION

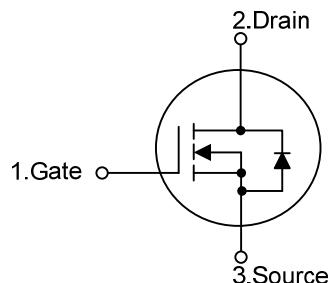
The UTC **12NM120** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

* $R_{DS(ON)} \leq 0.69 \Omega$ @ $V_{GS}=10V$, $I_D=6.0A$

* High Switching Speed

■ SYMBOL



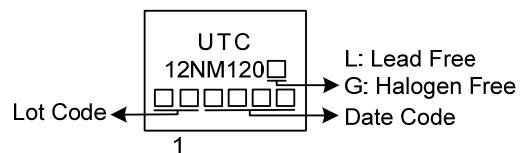
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12NM120L-TA3-T	12NM120G-TA3-T	TO-220	G	D	S	Tube
12NM120L-TF1-T	12NM120G-TF1-T	TO-220F1	G	D	S	Tube
12NM120L-TF2-T	12NM120G-TF2-T	TO-220F2	G	D	S	Tube
12NM120L-TQ2-T	12NM120G-TQ2-T	TO-263	G	D	S	Tube
12NM120L-TQ2-R	12NM120G-TQ2-R	TO-263	G	D	S	Tape Reel
12NM120L-T47-T	12NM120G-T47-T	TO-247	G	D	S	Tube
12NM120L-T47S-T	12NM120G-T47S-T	TO-247S	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

12NM120G-TA3-T 	(1)T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TQ2: TO-247S, T47: TO-247, T47S: TO-247S (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	1200	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	Continuous	I_D	12	A
	Pulsed	I_{DM}	24	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	420	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	0.97	V/ns
Power Dissipation	TO-220/TO-263	P_D	85	W
	TO-220F1/TO-220F2		35	W
	TO-247/TO-247S		130	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100\text{mH}$, $I_{AS} = 2.9\text{A}$, $V_{DD} = 90\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 12\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-263	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		62.5	$^\circ\text{C/W}$
	TO-247/TO-247S		40	$^\circ\text{C/W}$
Junction to Case	TO-220/TO-263	θ_{JC}	1.47	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		3.57	$^\circ\text{C/W}$
	TO-247/TO-247S		0.96	$^\circ\text{C/W}$

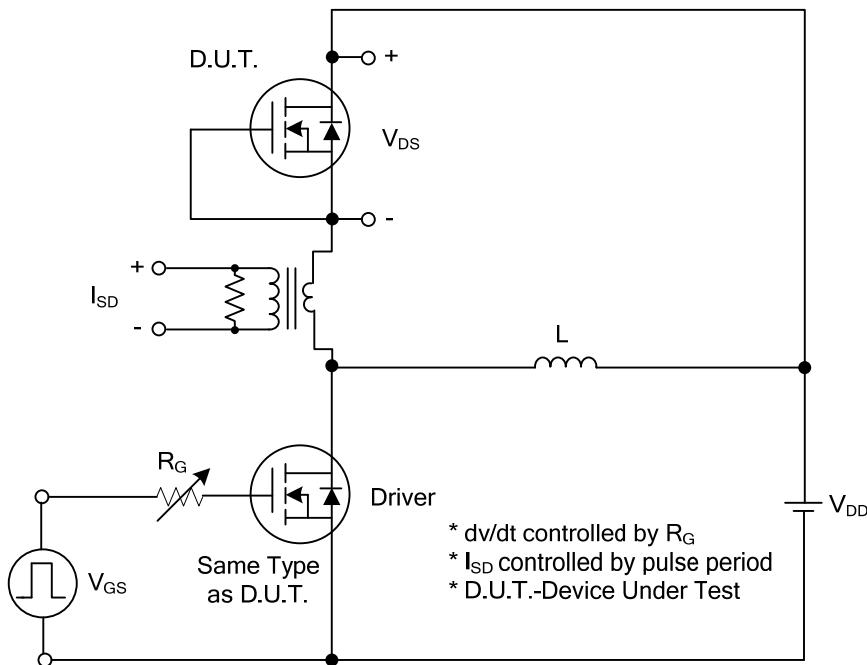
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	1200			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=1200\text{V}, V_{GS}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{GS}=+30\text{V}, V_{DS}=0\text{V}$		+100		nA
	Reverse	$V_{GS}=-30\text{V}, V_{DS}=0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=6.0\text{A}$			0.69	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}, V_{DS}=50\text{V}, f=1.0\text{MHz}$		1680		pF
Output Capacitance	C_{OSS}			95		pF
Reverse Transfer Capacitance	C_{RSS}			5		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DS}=960\text{V}, V_{GS}=10\text{V}, I_D=12\text{A}$ (Note 1, 2)		70		nC
Gate to Source Charge	Q_{GS}			16		nC
Gate to Drain Charge	Q_{GD}			26		nC
Turn-ON Delay Time	$t_{D(\text{ON})}$	$V_{DD}=100\text{V}, V_{GS}=10\text{V}, I_D=12\text{A}, R_G=25\Omega$ (Note 1, 2)		20		ns
Rise Time	t_R			24		ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			210		ns
Fall-Time	t_F			70		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				12	A
Maximum Body-Diode Pulsed Current	I_{SM}				24	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=12\text{A}, V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=12\text{A}, V_{GS}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		850		ns
Reverse Recovery Charge	Q_{rr}	(Note 1)			14	μC

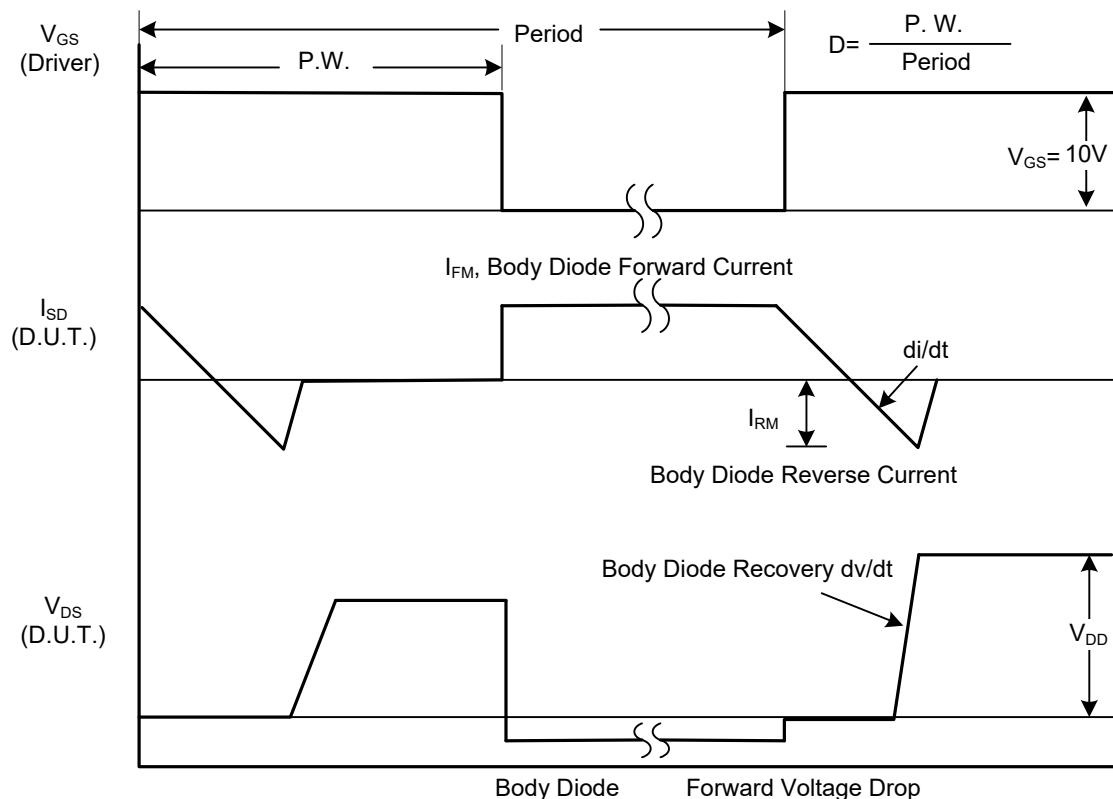
Notes: 1. Pulse Test: Pulse width $\leq 1200\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

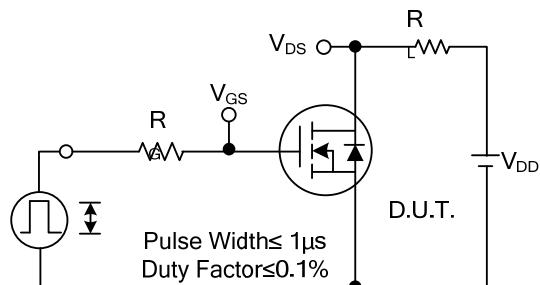


Peak Diode Recovery dv/dt Test Circuit

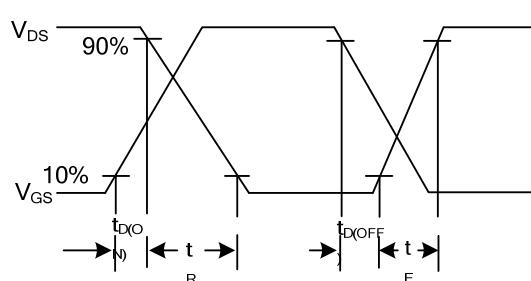


Peak Diode Recovery dv/dt Waveforms

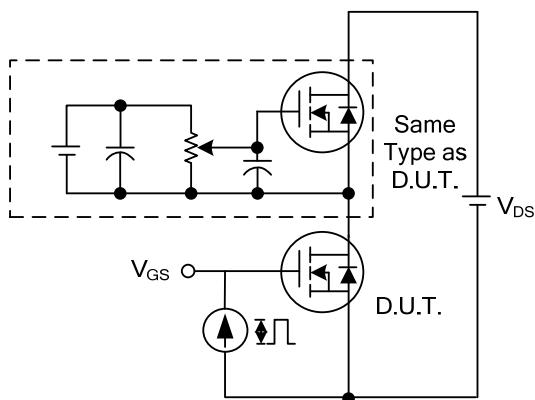
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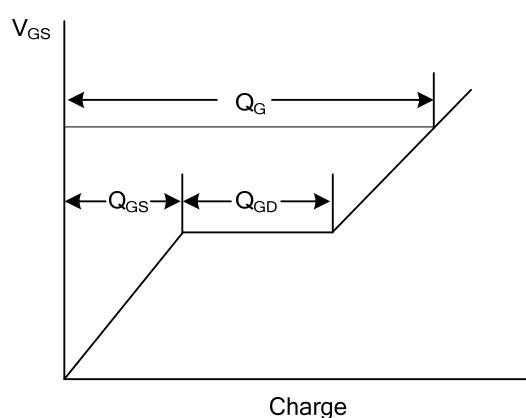
Switching Test Circuit



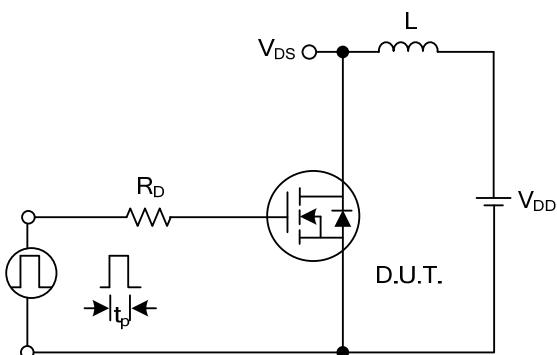
Switching Waveforms



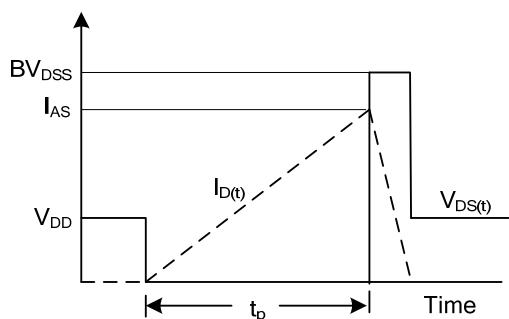
Gate Charge Test Circuit



Gate Charge Waveform

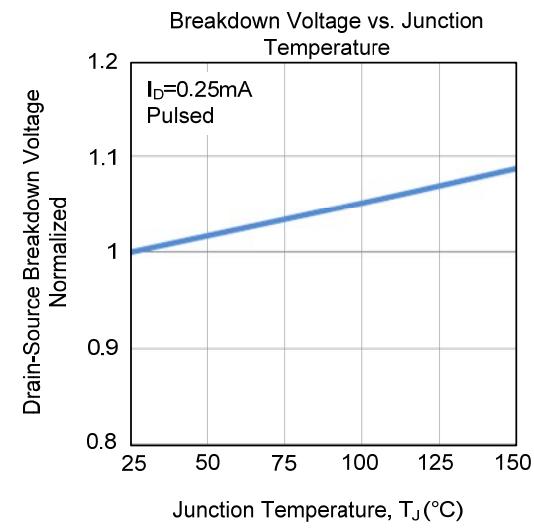
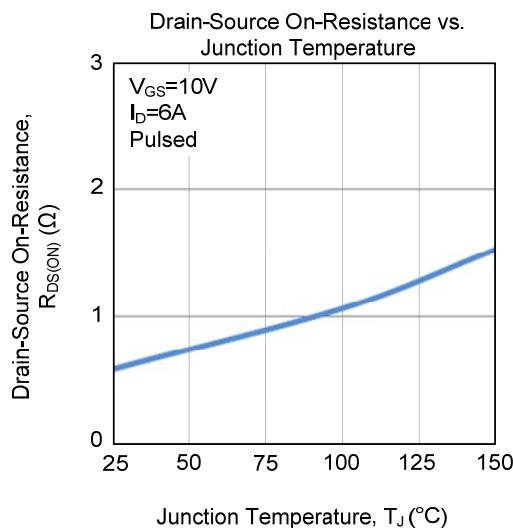
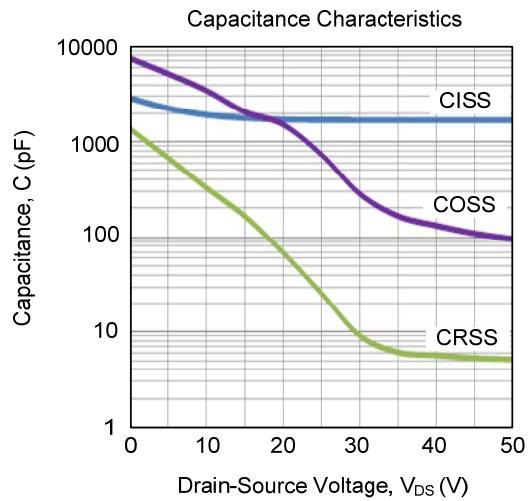
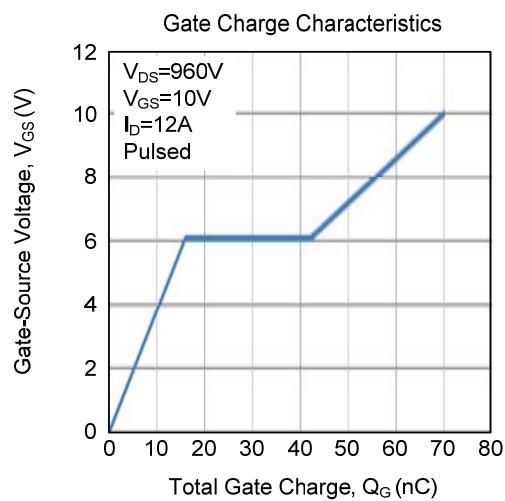
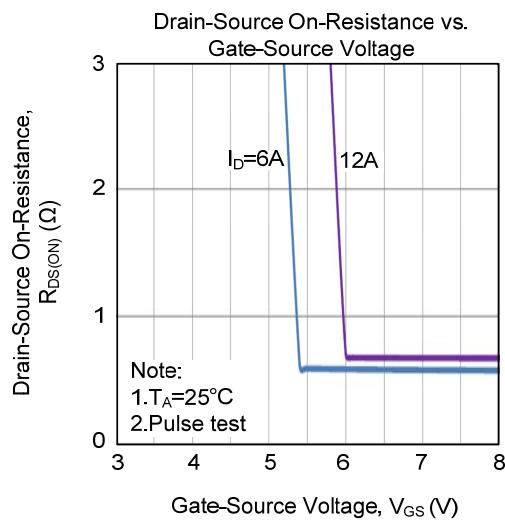
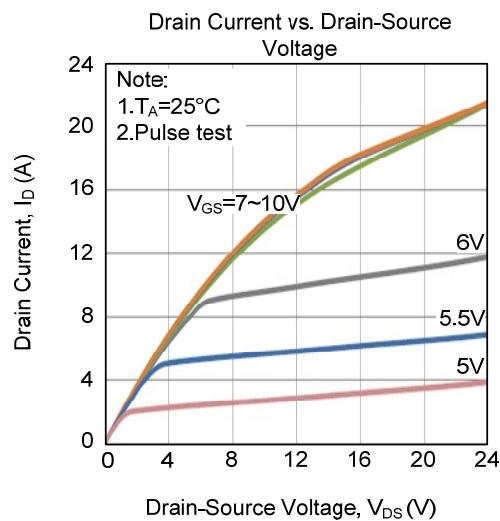


Unclamped Inductive Switching Test Circuit

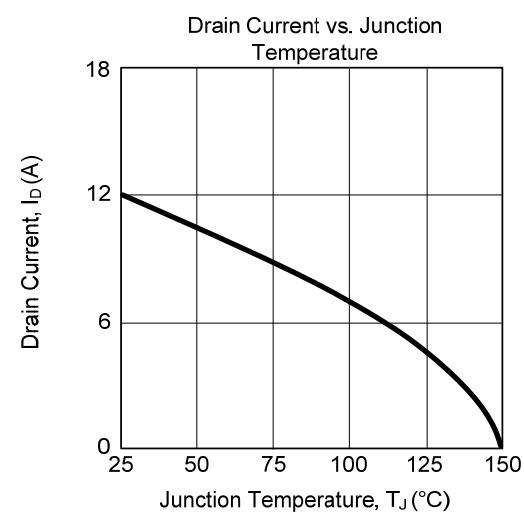
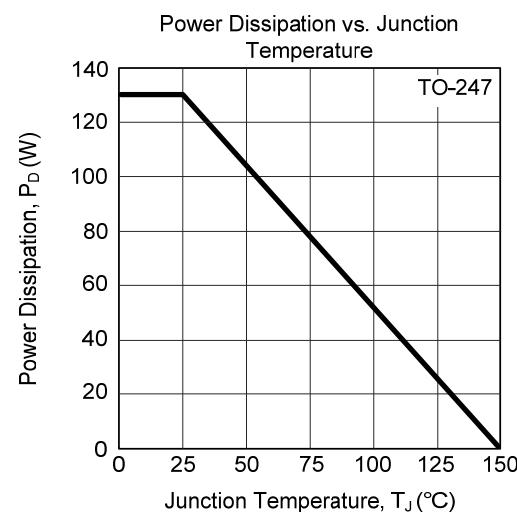
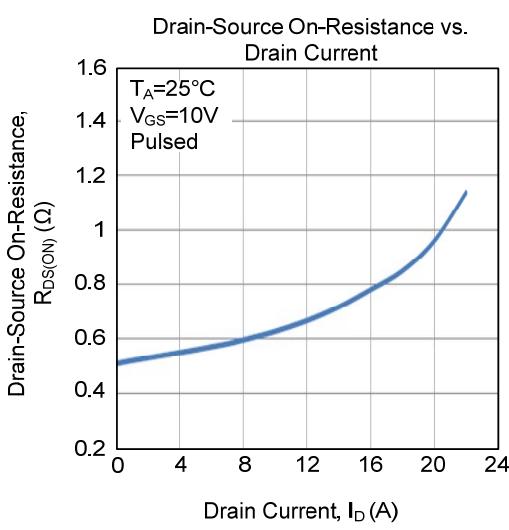
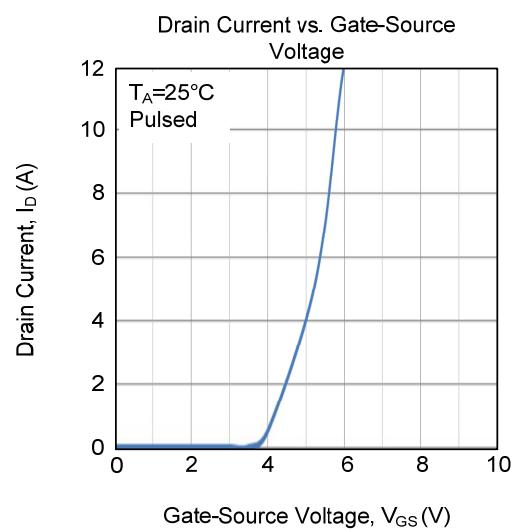
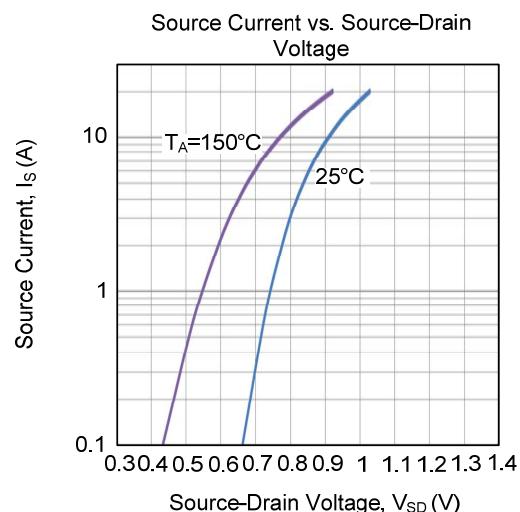
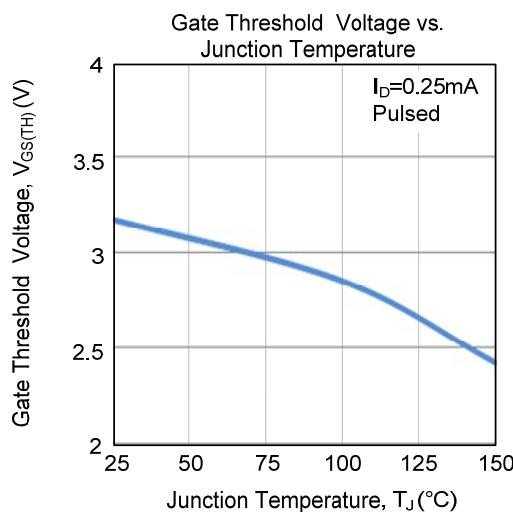


Unclamped Inductive Switching Waveforms

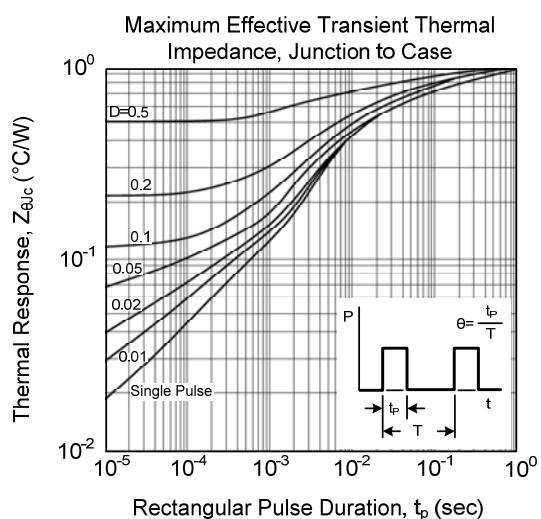
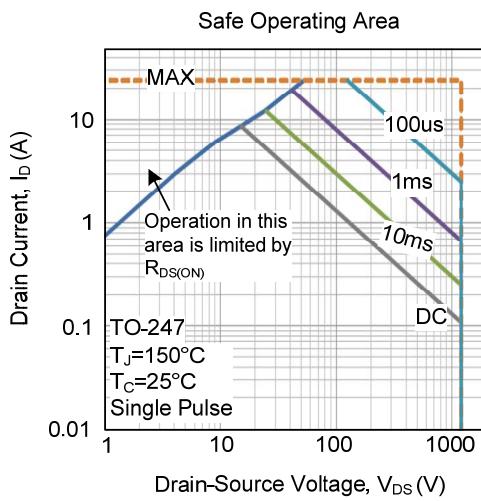
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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