



13NM80M1

Power MOSFET

13A, 800V N-CHANNEL SUPER-JUNCTION MOSFET

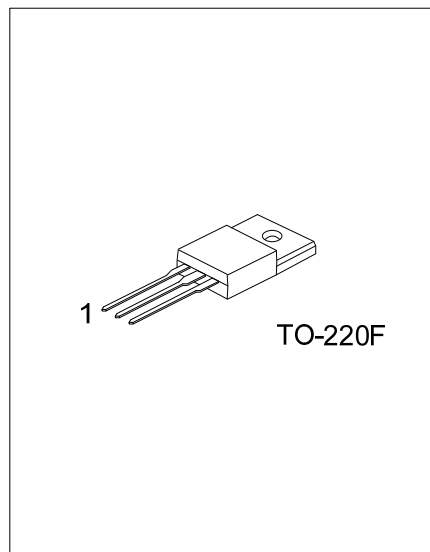
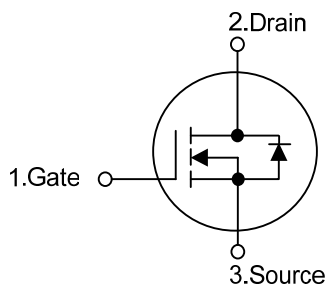
DESCRIPTION

The **UTC 13NM80M1** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

- * $R_{DS(ON)} \leq 0.35 \Omega$ @ $V_{GS}=10V$, $I_D=6.5A$
- * MSL1 Robust Package Design
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness
- * Green & Pb free

SYMBOL



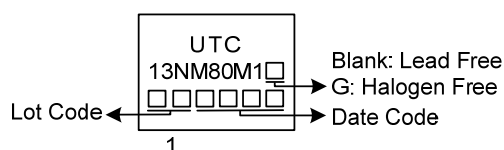
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Pb Free	Halogen Free		1	2	3	
13NM80M1-TF3-T	13NM80M1G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>13NM80M1G-TF3-T</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) T: Tube</p> <p>(2) TF3: TO-220F</p> <p>(3) G: Halogen Free and Lead Free, Blank: Lead Free</p>
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	800	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current	Continuous	I_D	13	A
	Pulsed (Note 2)	I_{DM}	39	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	286	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3	V/ns
Power Dissipation		P_D	31	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 17\text{mH}$, $I_{AS} = 5.8\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD} \leq 13\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
Junction to Case	θ_{JC}	4.03	$^{\circ}\text{C}/\text{W}$

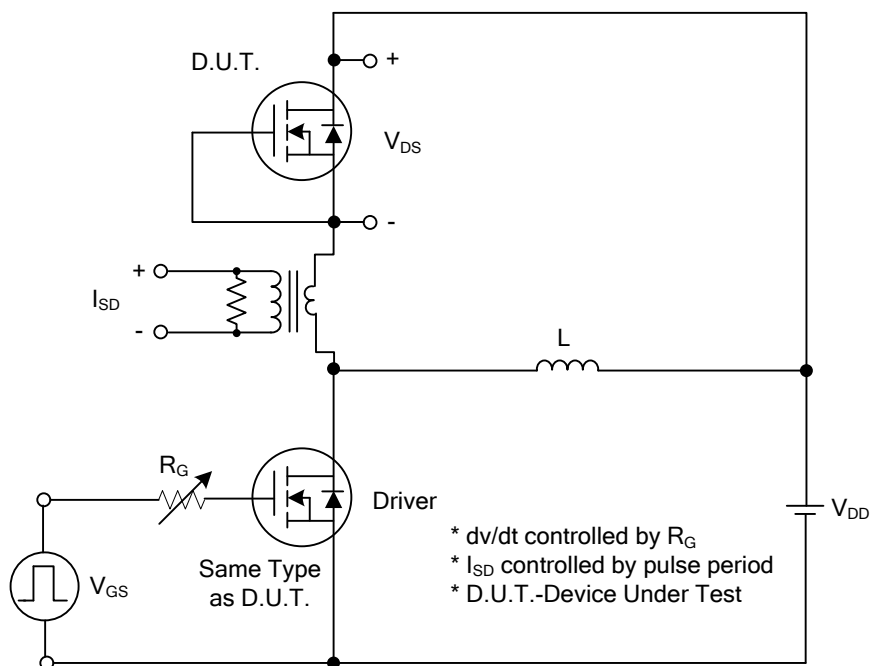
■ ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250μA	800			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = 800V, V _{GS} = 0V			10	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = 30V, V _{DS} = 0V			100	nA
		V _{GS} = -30V, V _{DS} = 0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	2.5		4.5	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 6.5A			0.35	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		1600		pF
Output Capacitance	C _{OSS}			850		pF
Reverse Transfer Capacitance	C _{RSS}			60		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =640V, V _{GS} =10V I _D =13A (Note 1,2)		63		nC
Gate to Source Charge	Q _{GS}			9		nC
Gate to Drain Charge	Q _{GD}			26		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DD} =400V, I _D =13A, R _G =25Ω, V _{GS} =10V (Note 1,2)		26		nS
Rise Time	t _R			28		nS
Turn-OFF Delay Time	t _{D(OFF)}			190		nS
Fall-Time	t _F			54		nS
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				13	A
Maximum Body-Diode Pulsed Current	I _{SM}				39	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =13A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =13A, V _{GS} =0V		500		nS
Body Diode Reverse Recovery Charge	Q _{rr}	dl _F /dt=100A/μs		9.3		μC

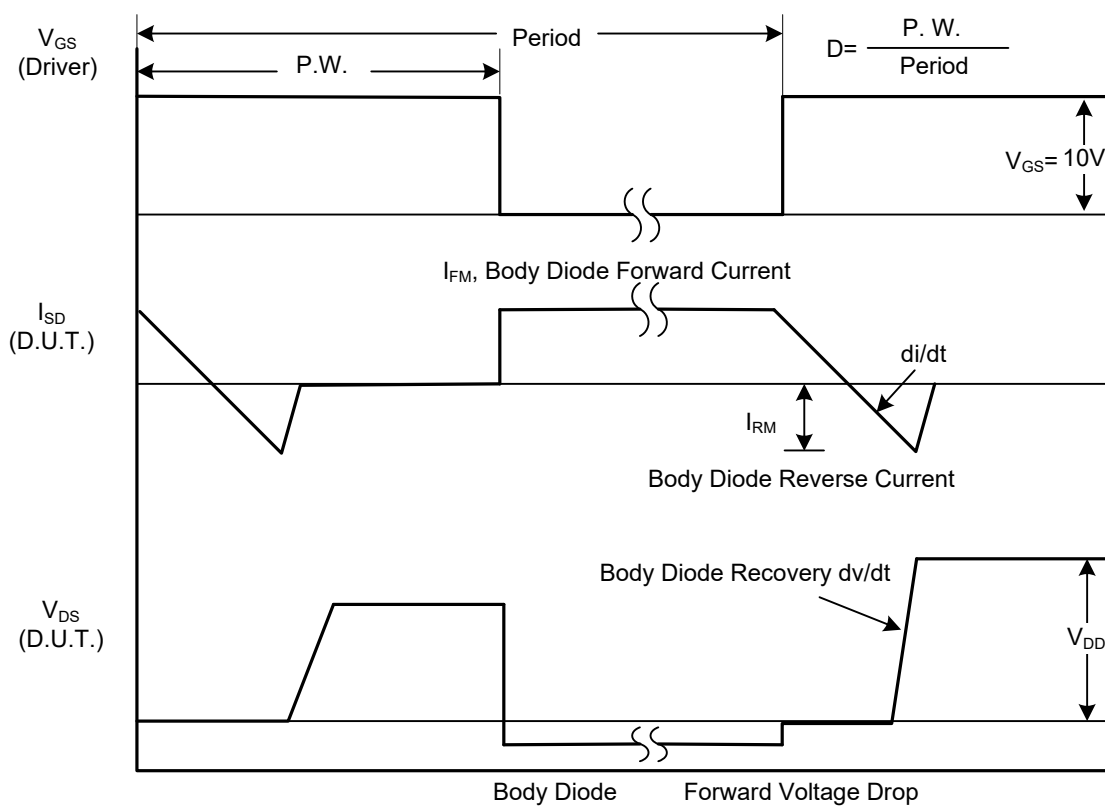
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

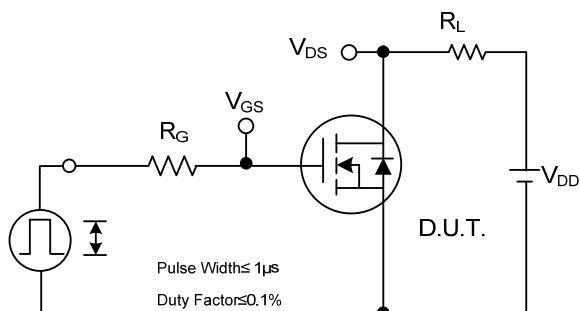


Peak Diode Recovery dv/dt Test Circuit

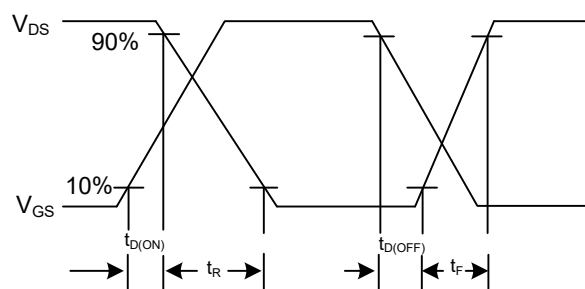


Peak Diode Recovery dv/dt Waveforms

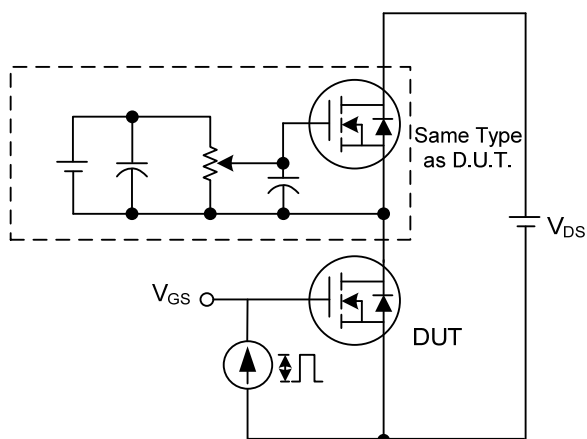
TEST CIRCUITS AND WAVEFORMS



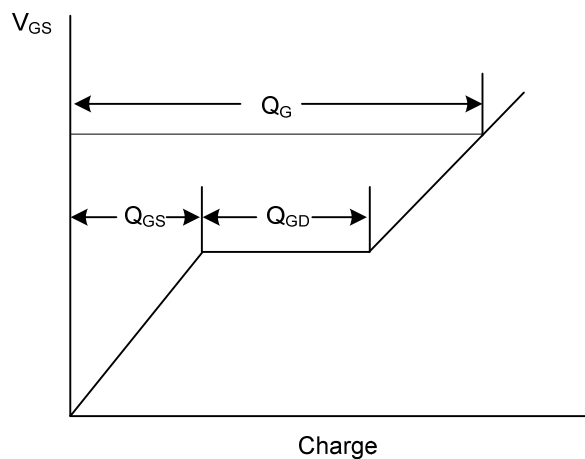
Switching Test Circuit



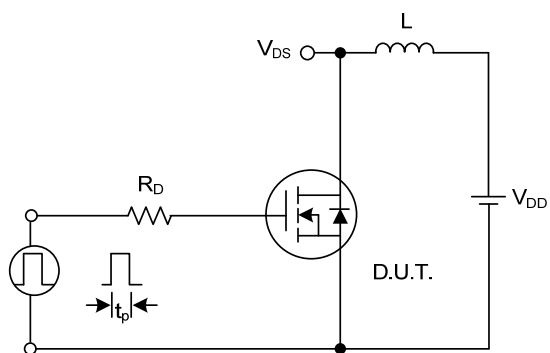
Switching Waveforms



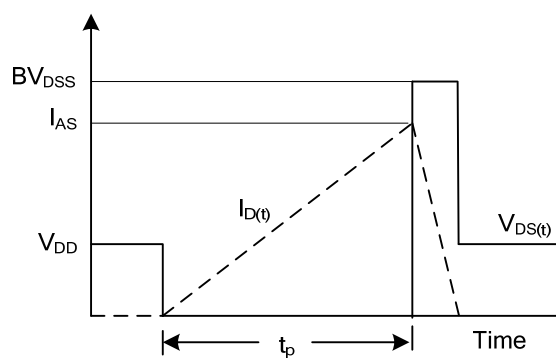
Gate Charge Test Circuit



Gate Charge Waveform

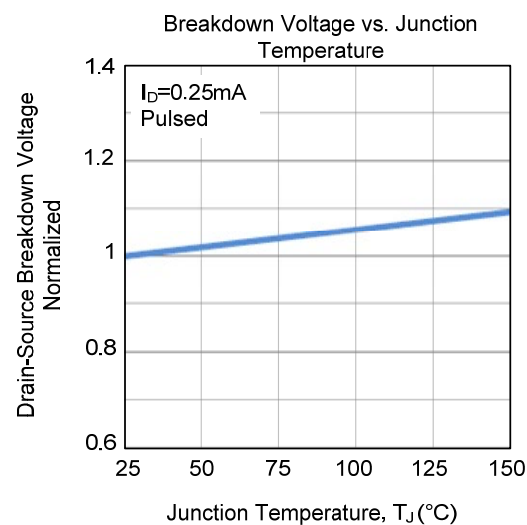
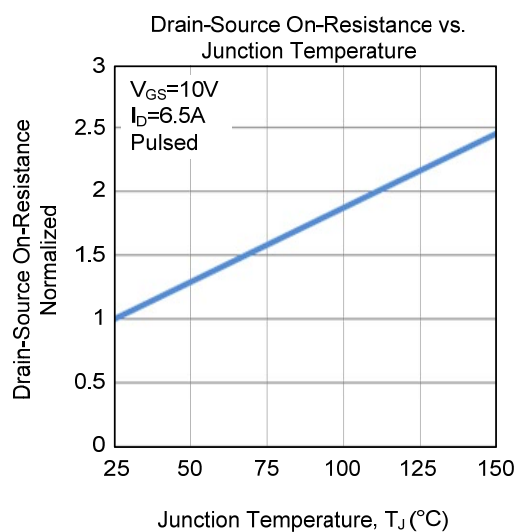
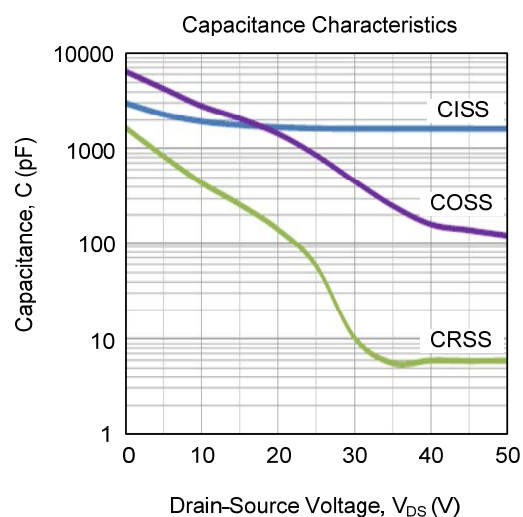
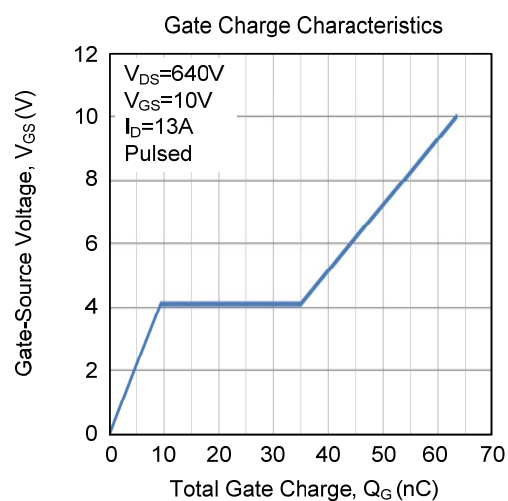
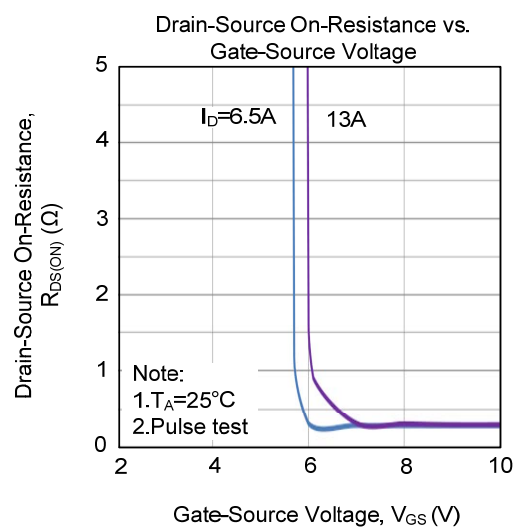
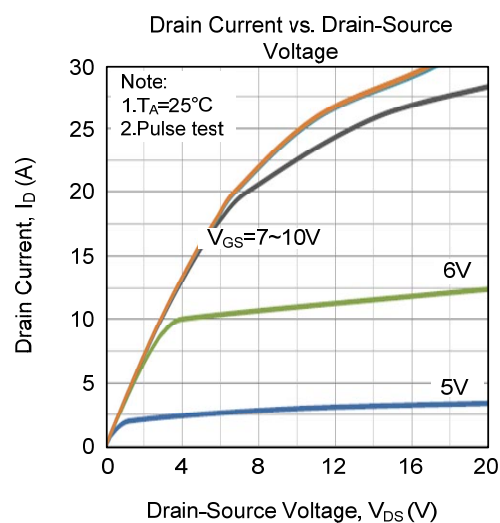


Unclamped Inductive Switching Test Circuit

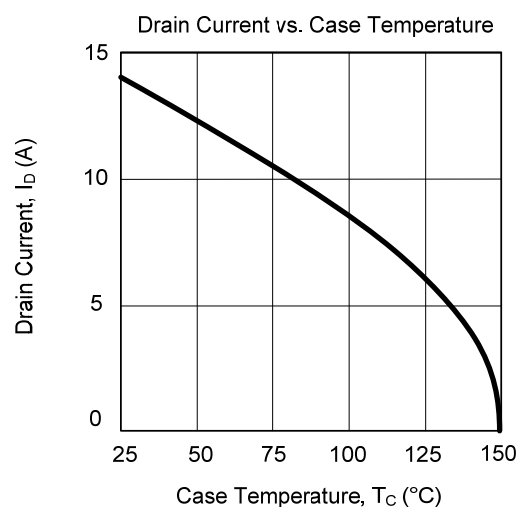
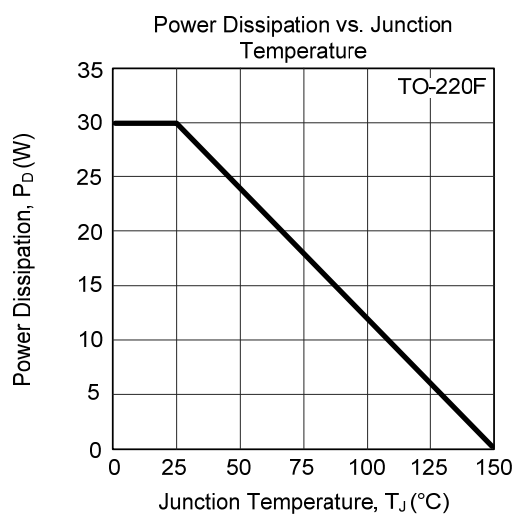
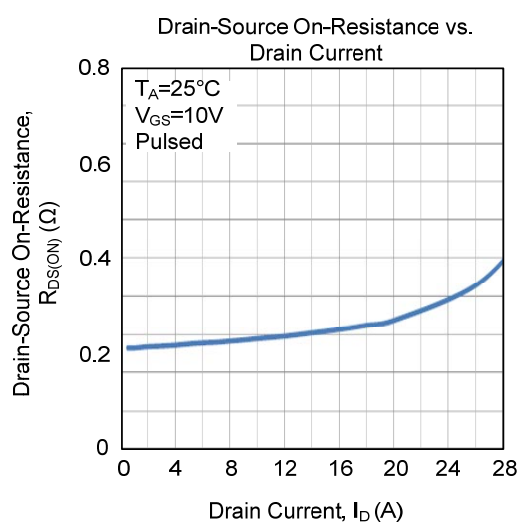
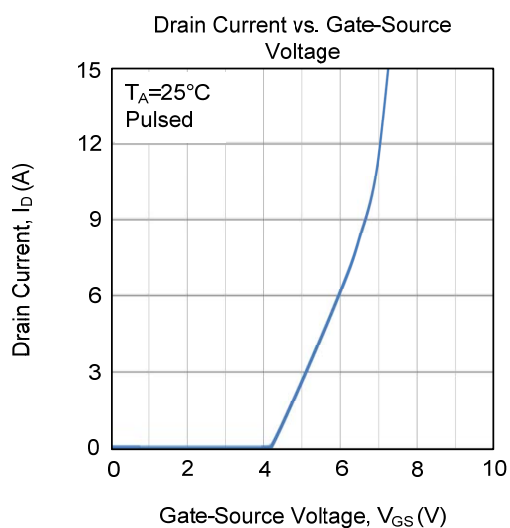
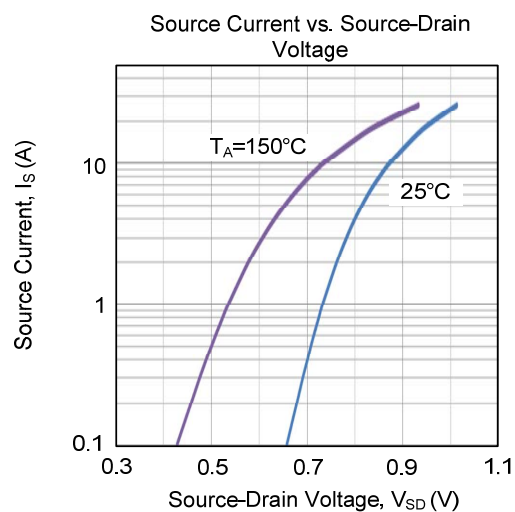
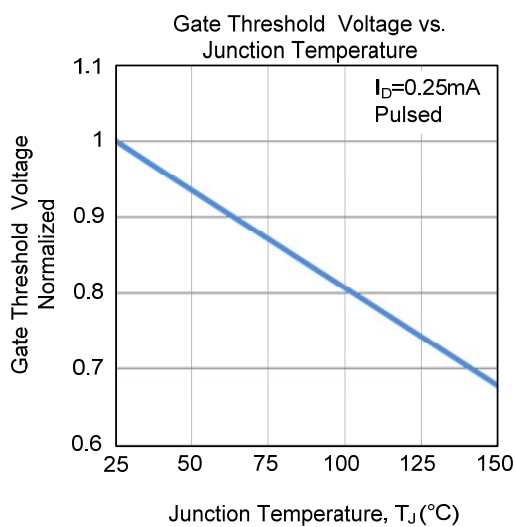


Unclamped Inductive Switching Waveforms

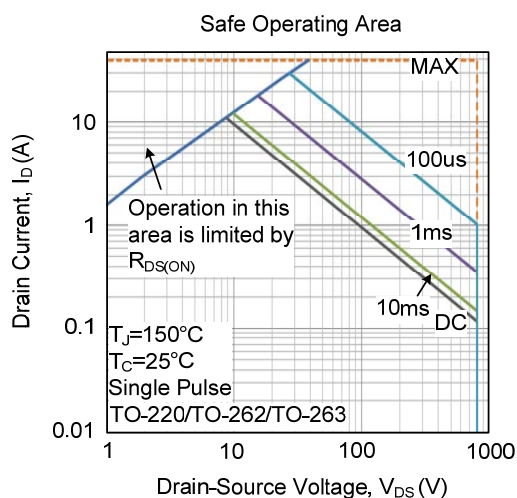
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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