



16NM60

Power MOSFET

16A, 600V N-CHANNEL SUPER-JUNCTION MOSFET

DESCRIPTION

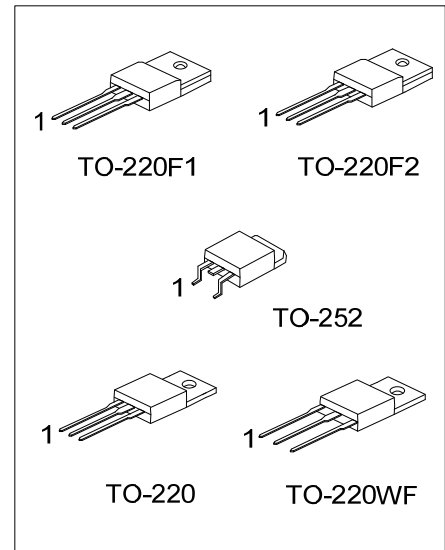
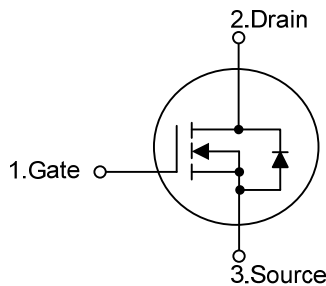
The UTC **16NM60** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

* $R_{DS(ON)} \leq 0.3 \Omega$ @ $V_{GS}=10V$, $I_D=8.0A$

* High Switching Speed

SYMBOL



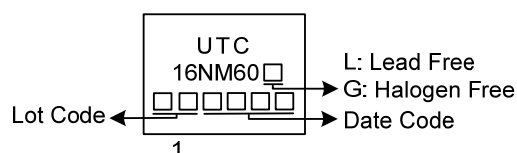
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
16NM60L-TA3-T	16NM60G-TA3-T	TO-220	G	D	S	Tube
16NM60L-TF1-T	16NM60G-TF1-T	TO-220F1	G	D	S	Tube
16NM60L-TF2-T	16NM60G-TF2-T	TO-220F2	G	D	S	Tube
16NM60L-TW1-T	16NM60G-TW1-T	TO-220WF	G	D	S	Tube
16NM60L-TN3-R	16NM60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

16NM60G-TA3-T (1) Packing Type (2) Package Type (3) Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TW1: TO-220WF, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	600	V
Gate-Source Voltage		V _{GSS}	±30	V
Continuous Drain Current	Continuous	I _D	16	A
	Pulsed	I _{DM}	48	A
Single Pulsed Avalanche Energy		E _{AS}	480	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.7	V/ns
Power Dissipation	TO-220	P _D	100	W
	TO-220F1/TO-220F2		30	W
	TO-220WF			
	TO-252		62	W
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=100mH, I_{AS}=3.1A, V_{DD}=50V, R_G=25Ω, Starting T_J = 25°C

4. I_{SD} ≤ 16A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F1	θ _{JA}	62.5	°C/W
	TO-220F2/TO-220WF			
	TO-252		110	°C/W
Junction to Case	TO-220	θ _{JC}	1.25	°C/W
	TO-220F1/TO-220F2		4.16	°C/W
	TO-220WF			
	TO-252		2.02(Note)	°C/W

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

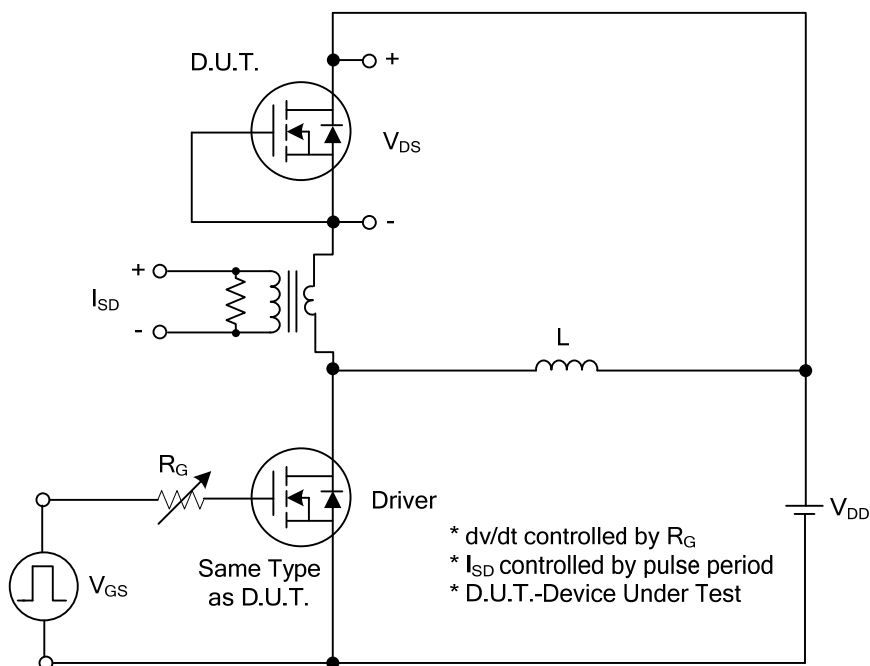
■ **ELECTRICAL CHARACTERISTICS** ($T_J=25^\circ\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	600			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =8.0A		0.25	0.3	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =50V, f=1.0MHz		975		pF
Output Capacitance		C _{OSS}			120		pF
Reverse Transfer Capacitance		C _{RSS}			5.2		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G	V _{DS} =480V, V _{GS} =10V, I _D =16A (Note 1, 2)		48		nC
Gate to Source Charge		Q _{GS}			13.2		nC
Gate to Drain Charge		Q _{GD}			20.4		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DD} =100V, V _{GS} =10V, I _D =16A, R _G =25Ω (Note 1, 2)		12		ns
Rise Time		t _R			26		ns
Turn-OFF Delay Time		t _{D(OFF)}			120		ns
Fall-Time		t _F			50		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				16	A
Maximum Body-Diode Pulsed Current		I _{SM}				48	A
Drain-Source Diode Forward Voltage		V _{SD}	I _S =16A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	I _S =16A, V _{GS} =0V, dI _F /dt=100A/μs		380		ns
Reverse Recovery Charge		Q _{rr}	(Note 1)		5.7		μC

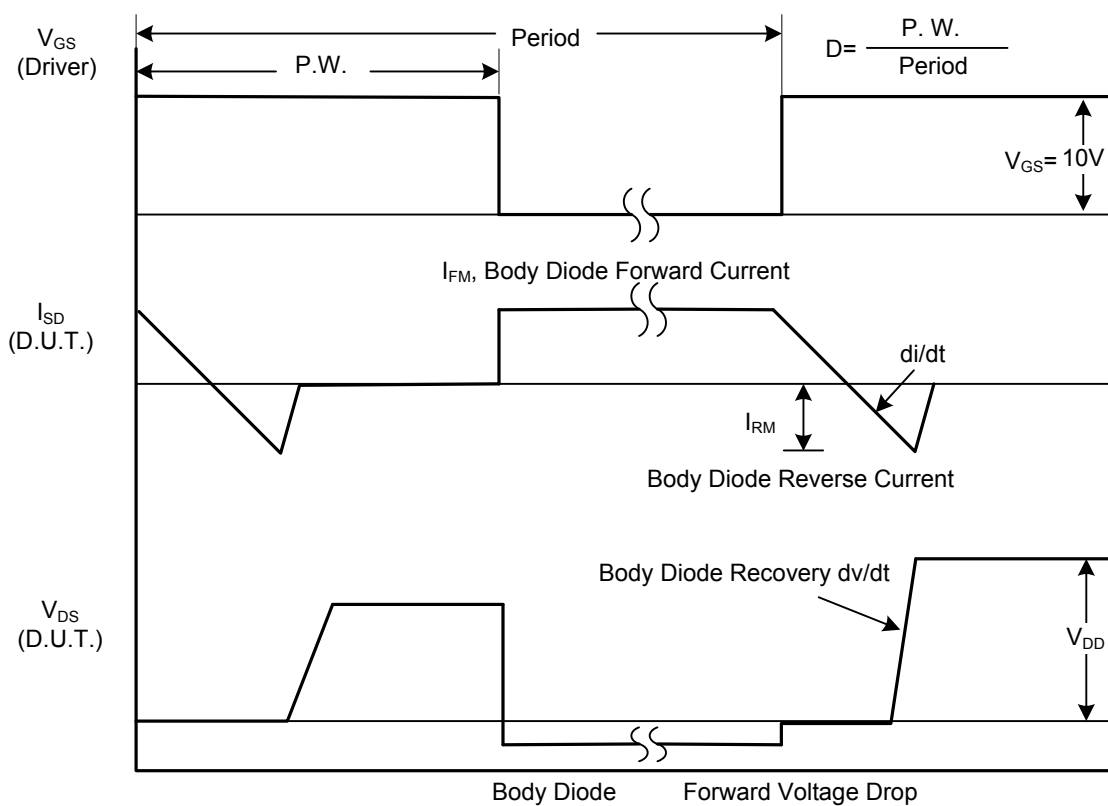
Notes: 1. Pulse Test: Pulse width $\leq 600\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

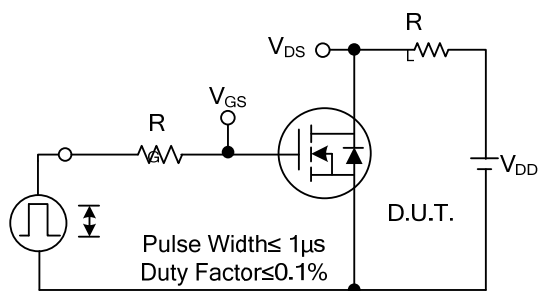


Peak Diode Recovery dv/dt Test Circuit

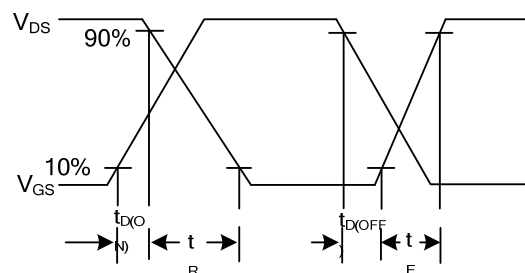


Peak Diode Recovery dv/dt Waveforms

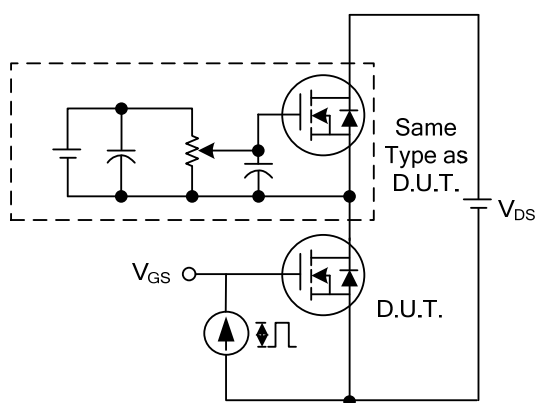
■ TEST CIRCUITS AND WAVEFORMS



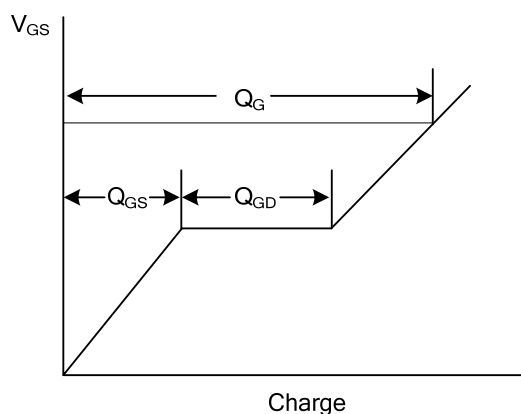
Switching Test Circuit



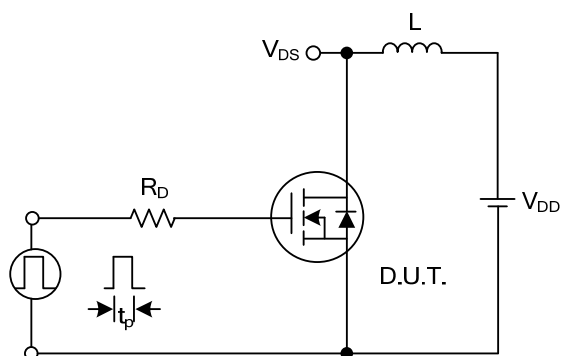
Switching Waveforms



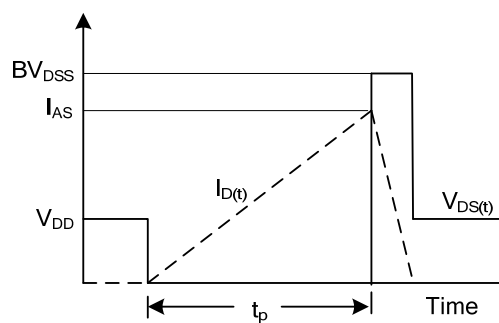
Gate Charge Test Circuit



Gate Charge Waveform

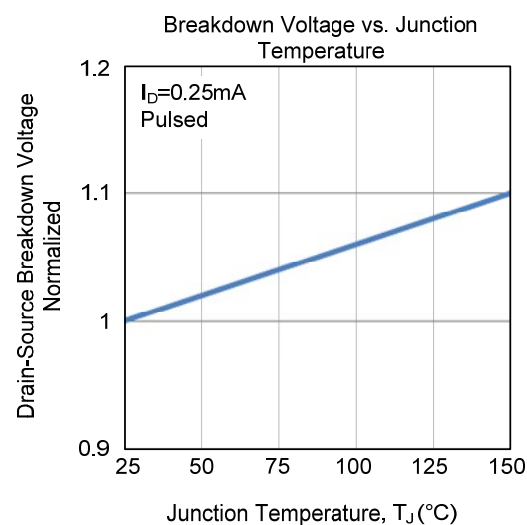
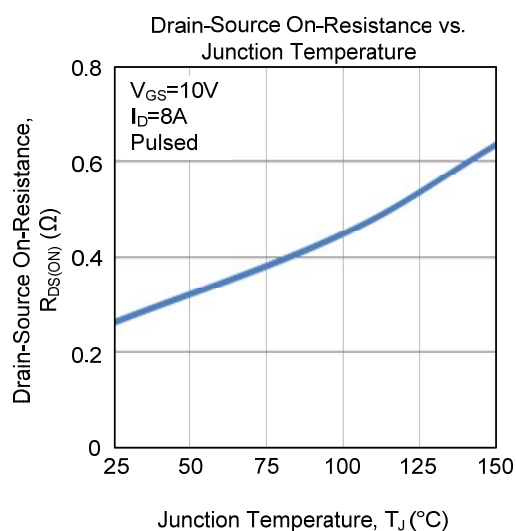
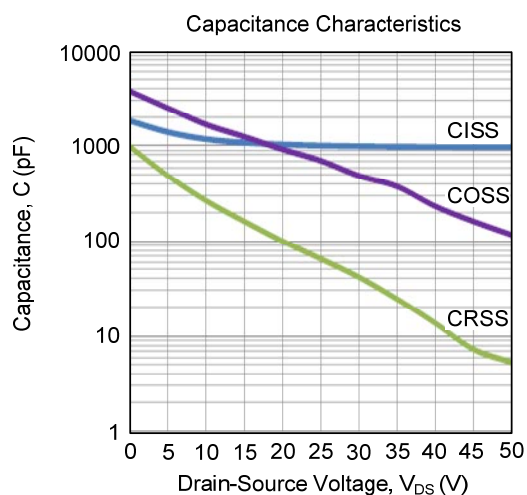
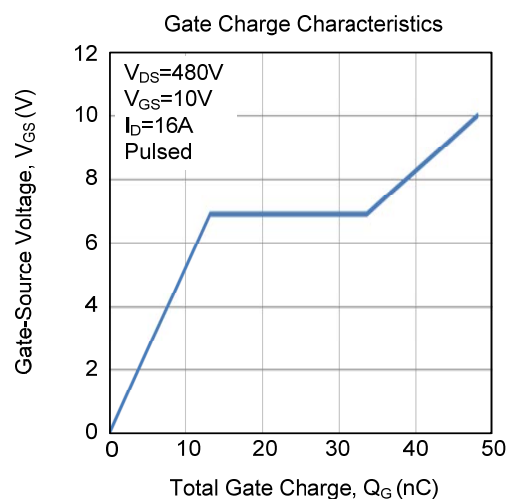
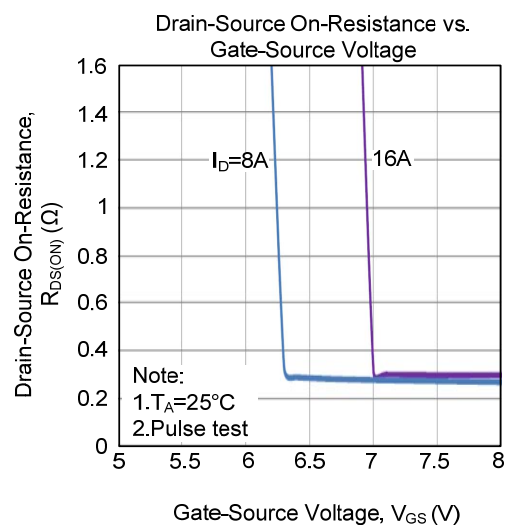
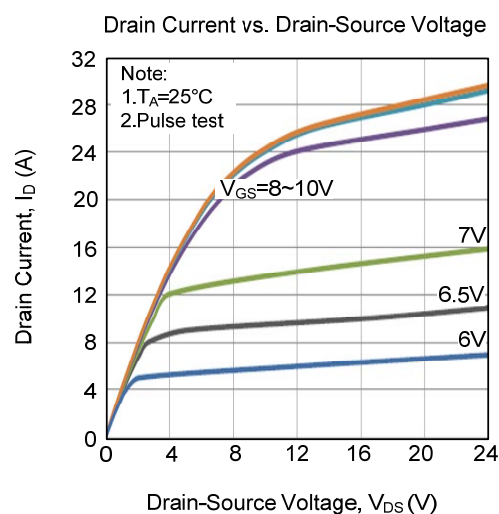


Unclamped Inductive Switching Test Circuit

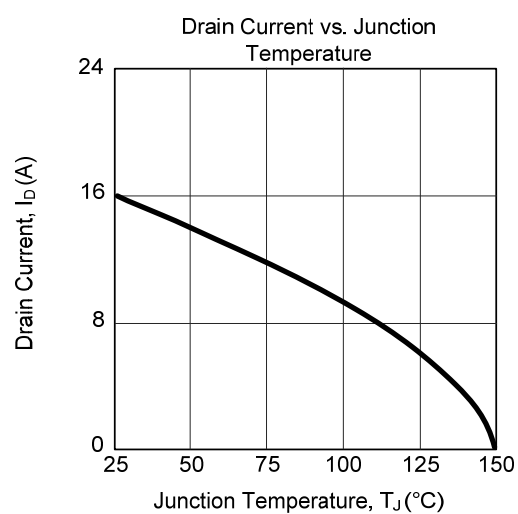
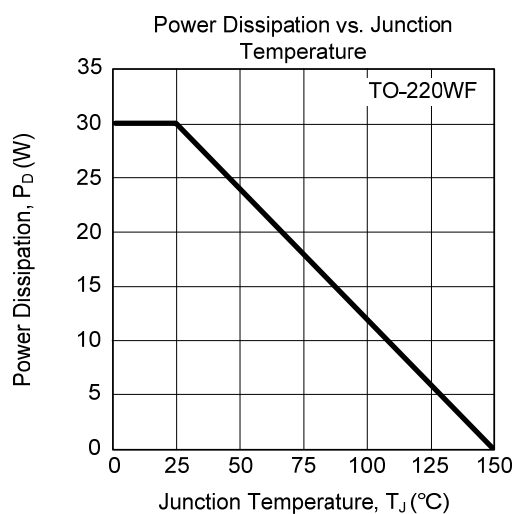
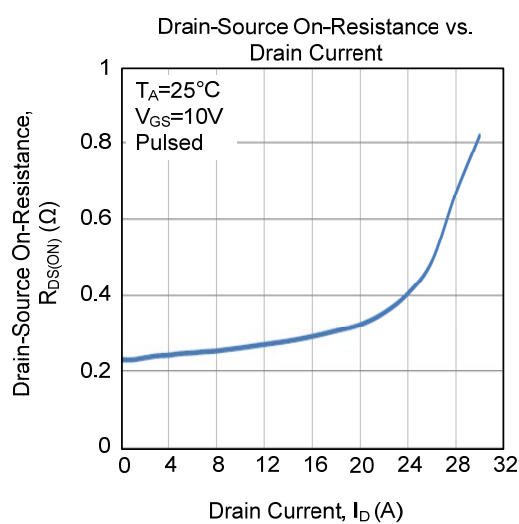
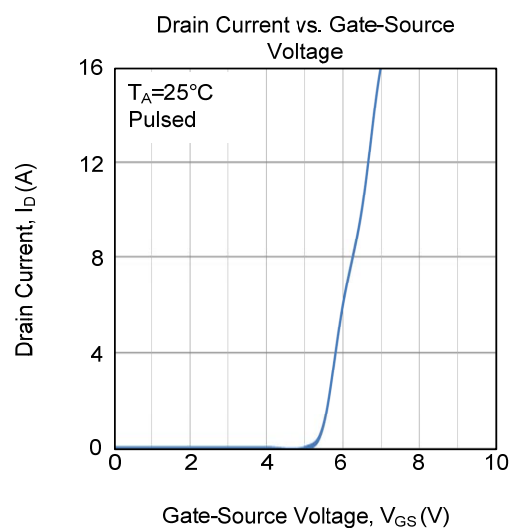
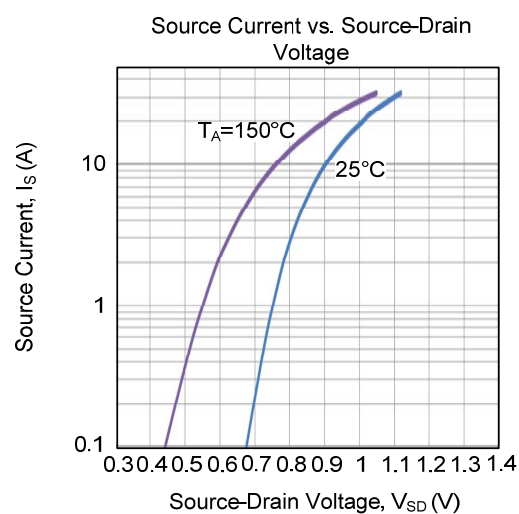
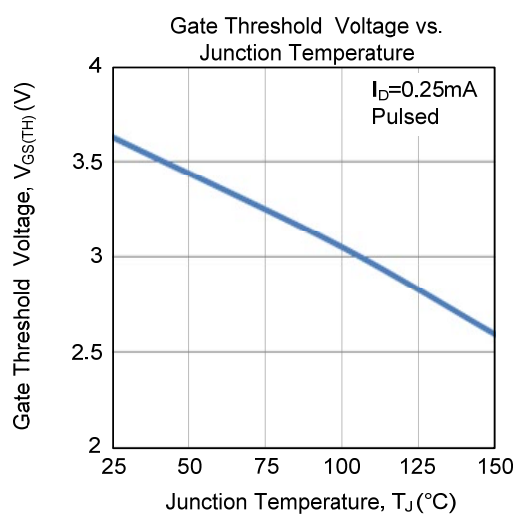


Unclamped Inductive Switching Waveforms

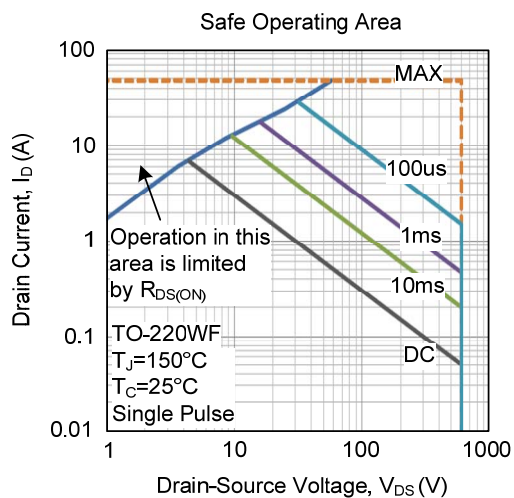
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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