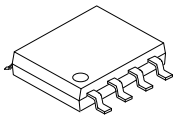
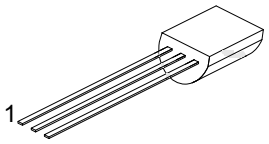
1.0A, 650V N-CHANNEL POWER MOSFET



SOP-8



TO-92

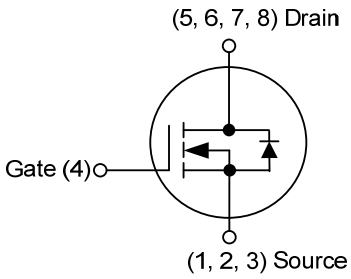
■ DESCRIPTION

The UTC **1N65-SE** is a high voltage MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

■ FEATURES

* $R_{DS(ON)} \leq 18\ \Omega$ @ $V_{GS} = 10V$, $I_D = 0.5A$
* Fast switching capability
* Avalanche energy specified
* Improved dv/dt capability, high ruggedness

■ SYMBOL



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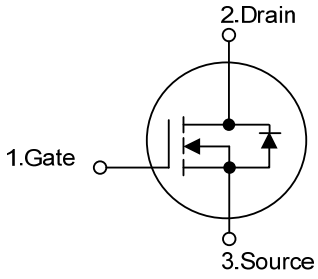
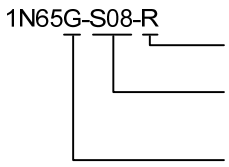


TO-92

■ ORDERING INFORMATION

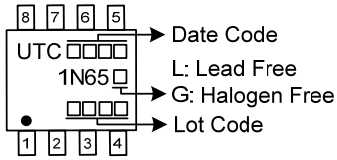
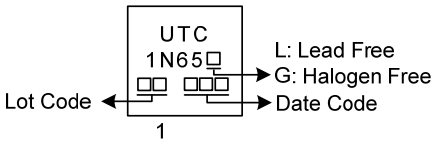
Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
1N65L-S08-R	1N65G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
1N65L-T92-B	1N65G-T92-B	TO-92	G	D	S	-	-	-	-	-	Tape Box
1N65L-T92-K	1N65G-T92-K	TO-92	G	D	S	-	-	-	-	-	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source



(1) R: Tape Reel, B: Tape Box, K: Bulk
(2) S08: SOP-8, T92: TO-92
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING

SOP-8	TO-92
	

■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	1.0	A
	Pulsed (Note 2)	I_{DM}	2.0	A
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.7	V/ns
Power Dissipation	SOP-8	P_D	1.5	W
	TO-92		1.35	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating : Pulse width limited by maximum junction temperature.
3. I_{SD} ≤ 1.0A, di/dt ≤200A/μs, V_{DD}≤ BV_{DSS}, Starting T_J= 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOP-8	θ_{JA}	190	°C/W
	TO-92		160	°C/W
Junction to Case	SOP-8	θ_{JC}	83.3	°C/W
	TO-92		92.5	°C/W

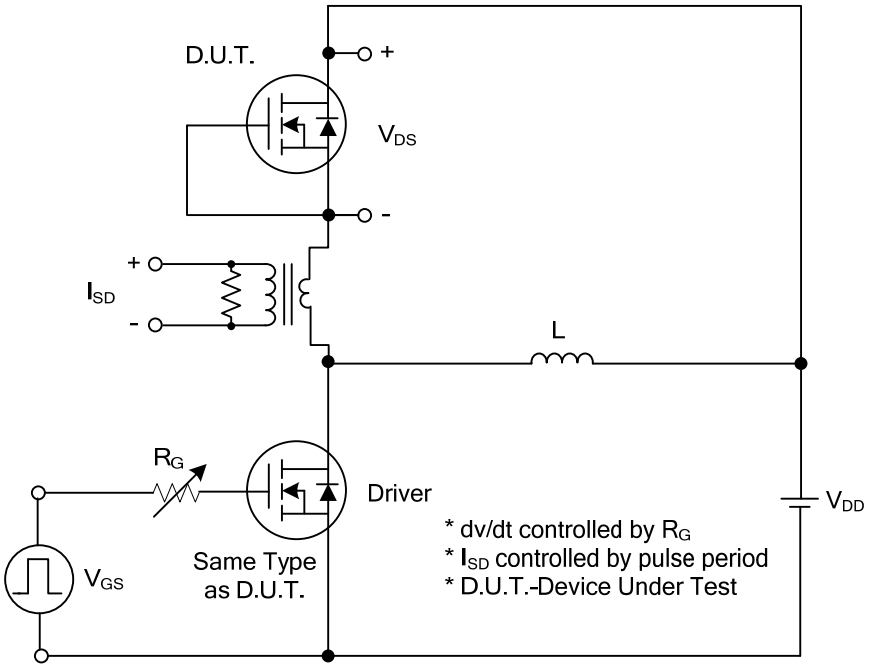
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

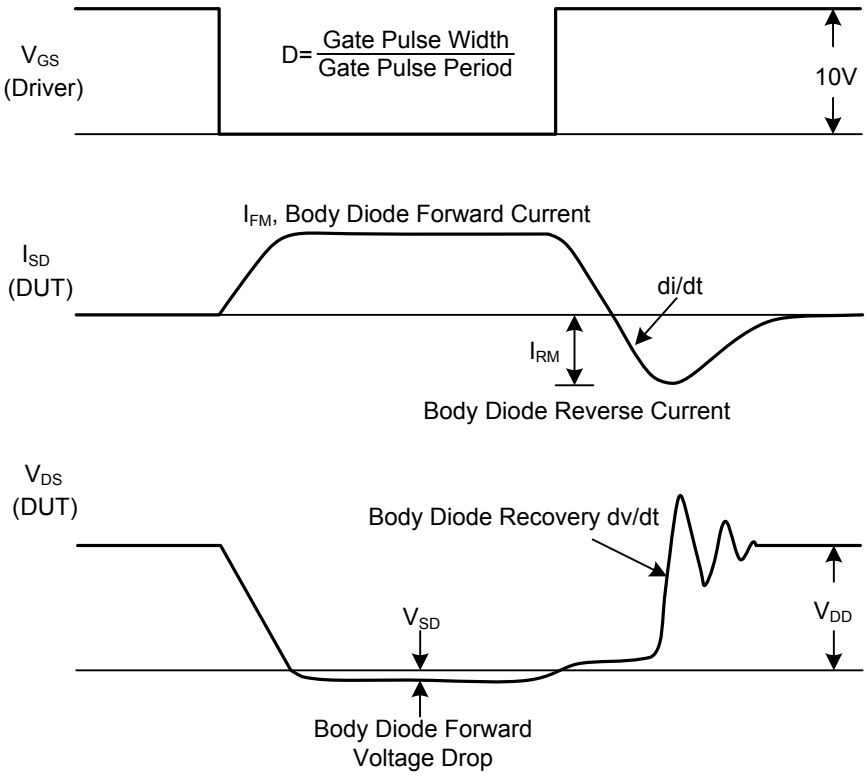
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250μA	650			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=650V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=30V, V_{DS}=0V			100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=0.5A			18	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1MHz		108.7		pF
Output Capacitance		C_{OSS}			35.7		pF
Reverse Transfer Capacitance		C_{RSS}			11.8		pF
SWITCHING CHARACTERISTICS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=520V, V_{GS}=10V, I_D=1.0A, I_G=1mA (Note 1, 2)		7.2		nC
Gate to Source Charge		Q_{GS}			2.48		nC
Gate to Drain Charge		Q_{GD}			0.6		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V, I_D=1.0A, R_G=25Ω (Note 1, 2)		20.8		ns
Rise Time		t_R			15.6		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			15		ns
Fall-Time		t_F			41		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Maximum Body-Diode Continuous Current		I_S				1.0	A
Maximum Body-Diode Pulsed Current (Note 1)		I_{SM}				2.0	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=1.0A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time		t_{rr}	I_S=1.0A, V_{GS}=0V, dI_F/dt=100A/μs		169		ns
Body Diode Reverse Recovery Charge		Q_{rr}			0.7		μC

Notes: 1. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

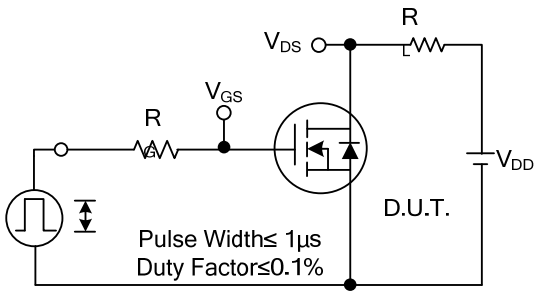


Peak Diode Recovery dv/dt Test Circuit

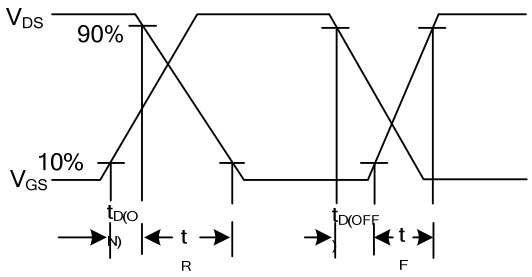


Peak Diode Recovery dv/dt Waveforms

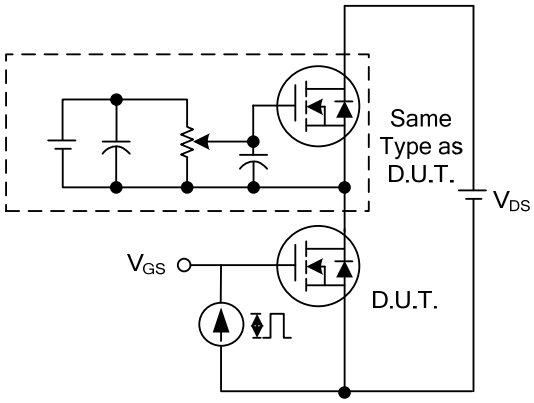
■ TEST CIRCUITS AND WAVEFORMS



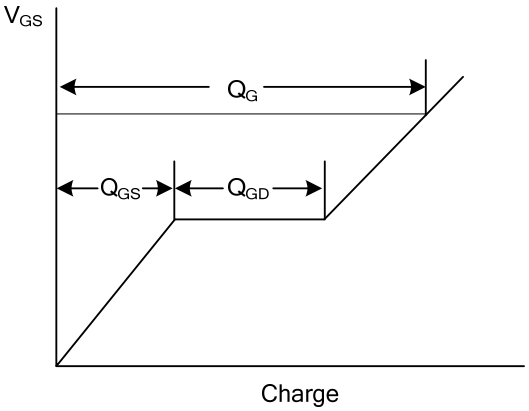
Switching Test Circuit



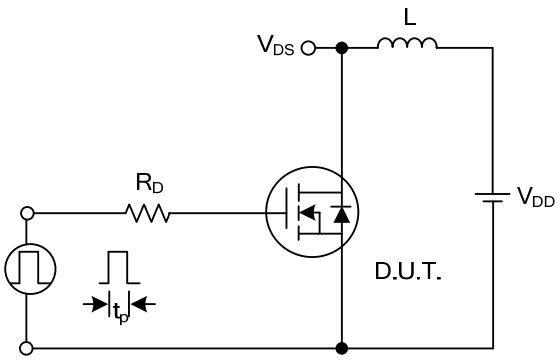
Switching Waveforms



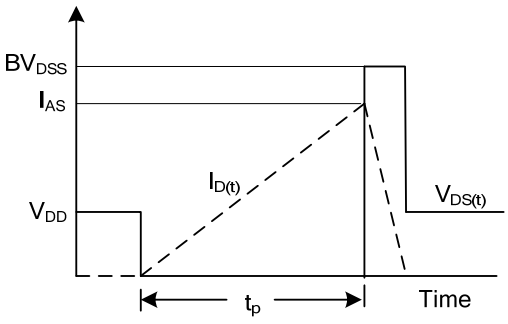
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms