

**2NM95-Q**

Preliminary

Power MOSFET

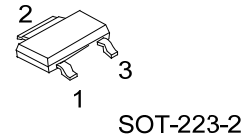
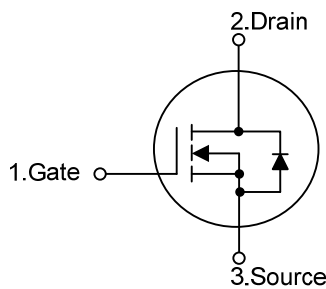
**2.0A, 950V N-CHANNEL
SUPER-JUNCTION MOSFET****DESCRIPTION**

The UTC **2NM95-Q** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

* $R_{DS(ON)} \leq 5.4 \Omega$ @ $V_{GS}=10V$, $I_D=1.0A$

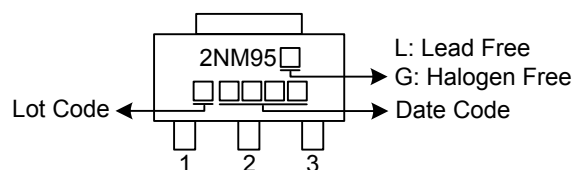
* High Switching Speed

SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
2NM95L-AA2-R	2NM95G-AA2-R	SOT-223-2	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

2NM95G-AA2-R (1) Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel (2) AA2: SOT-223-2 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING

■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	950	V
Gate-Source Voltage		V_{GS}	± 30	V
Continuous Drain Current	Continuous	I_D	2	A
	Pulsed	I_{DM}	4	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	40	mJ
Peak Diode Recovery dv/dt (Note 3)		dv/dt	2.7	V/ns
Power Dissipation		P_D	1.5	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100\text{mH}$, $I_{AS} = 0.9\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$ Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 2.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	150	°C/W
Junction to Case	θ_{JC}	83.3	°C/W

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

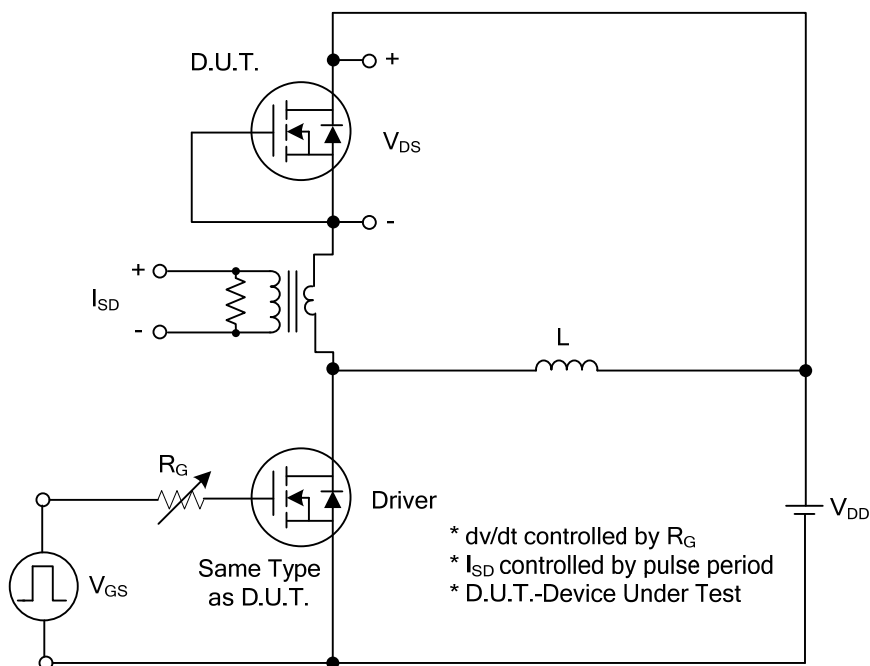
■ ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	950			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =950V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =1.0A			5.4	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =50V, f=1.0MHz		152		pF
Output Capacitance		C _{OSS}			24		pF
Reverse Transfer Capacitance		C _{RSS}			2.5		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G	V _{DS} =760V, V _{GS} =10V, I _D =2.0A (Note 1, 2)		15		nC
Gate to Source Charge		Q _{GS}			5.5		nC
Gate to Drain Charge		Q _{GD}			2.5		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DD} =100V, V _{GS} =10V, I _D =2.0A, R _G =25Ω (Note 1, 2)		3.2		ns
Rise Time		t _{RR}			16		ns
Turn-OFF Delay Time		t _{D(OFF)}			26		ns
Fall-Time		t _F			34		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I _S				2	A
Maximum Body-Diode Pulsed Current		I _{SM}				4	A
Drain-Source Diode Forward Voltage		V _{SD}	I _S =2.0A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time		t _{rr}	I _S =2.0A, V _{GS} =0V,		370		ns
Reverse Recovery Charge		Q _{rr}	dI _F /dt=100A/μs (Note 1)		1.2		μC

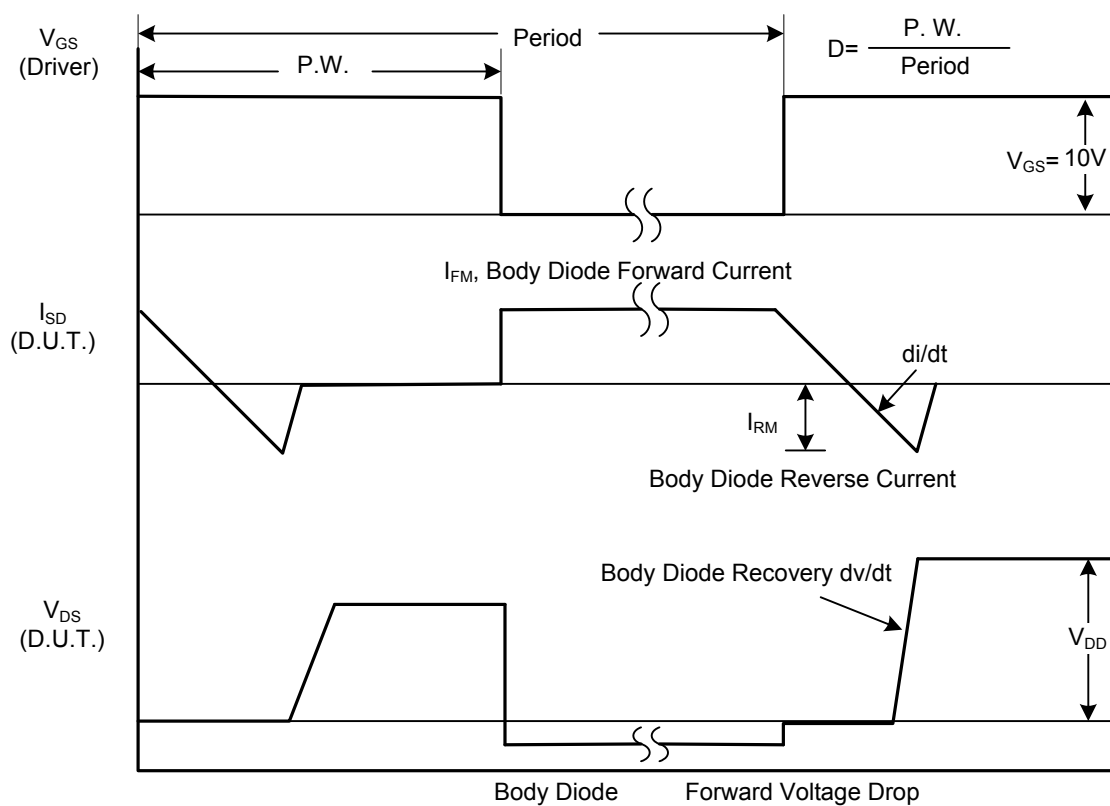
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

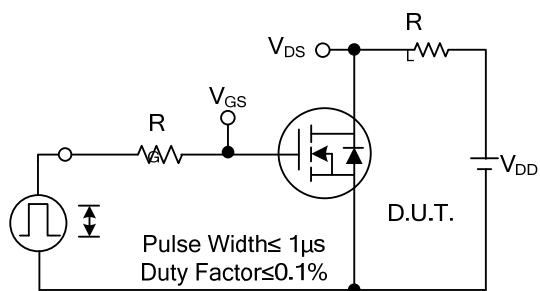


Peak Diode Recovery dv/dt Test Circuit

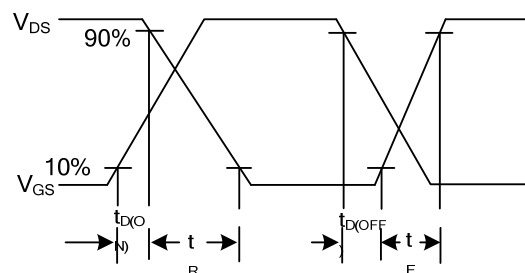


Peak Diode Recovery dv/dt Waveforms

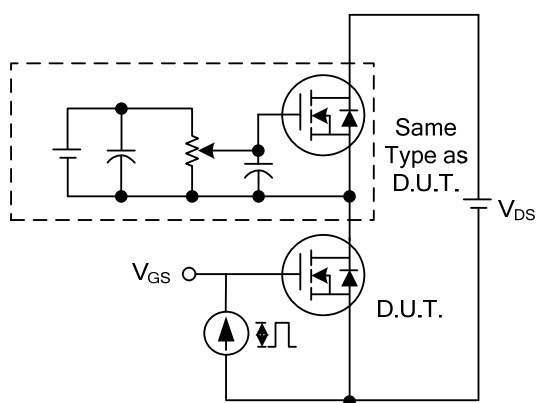
■ TEST CIRCUITS AND WAVEFORMS



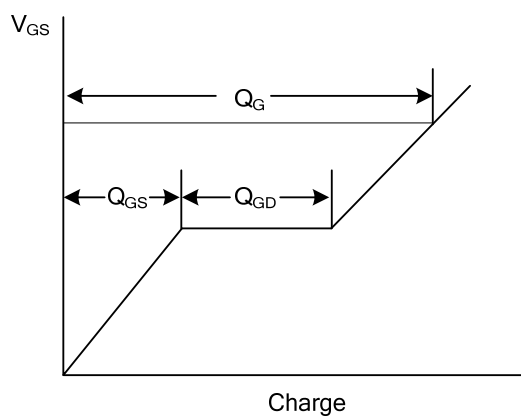
Switching Test Circuit



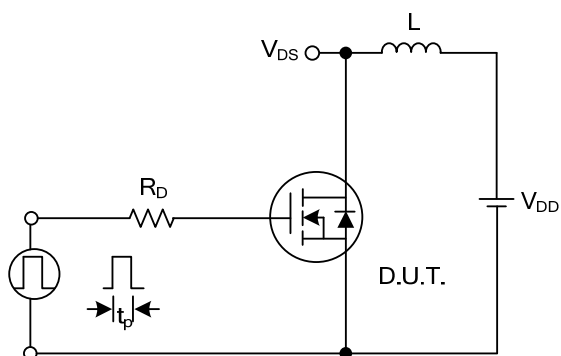
Switching Waveforms



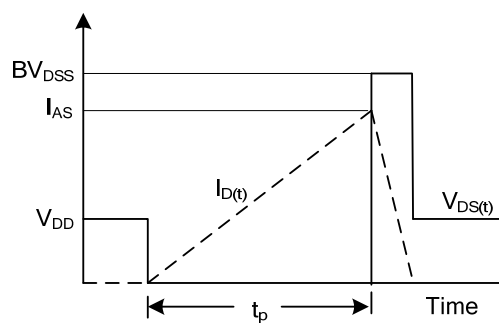
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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