



## 3NK90Z

## POWER MOSFET

### 3.0A, 900V N-CHANNEL POWER MOSFET

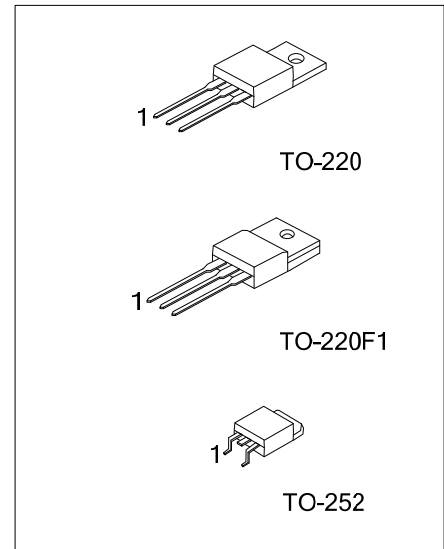
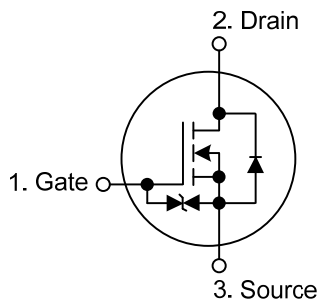
#### DESCRIPTION

The UTC **3NK90Z** is a silicon N-channel MOSFET, it uses UTC's advanced technology to provide the customers with a minimum on state resistance, high switching speed and low gate charge.

#### FEATURES

- \*  $R_{DS(ON)} \leq 6.4 \Omega$  @  $V_{GS}=10V$ ,  $I_D=1.5A$
- \* High switching speed
- \* Low input capacitance
- \* With ESD protection

#### SYMBOL



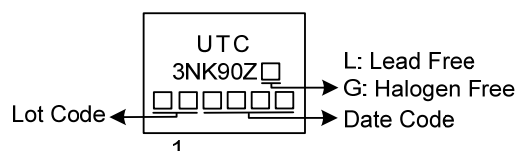
#### ORDERING INFORMATION

| Ordering Number |               | Package  | Pin Assignment |   |   | Packing   |
|-----------------|---------------|----------|----------------|---|---|-----------|
| Lead Free       | Halogen Free  |          | 1              | 2 | 3 |           |
| 3NK90ZL-TA3-T   | 3NK90ZG-TA3-T | TO-220   | G              | D | S | Tube      |
| 3NK90ZL-TF1-T   | 3NK90ZG-TF1-T | TO-220F1 | G              | D | S | Tube      |
| 3NK90ZL-TN3-R   | 3NK90ZG-TN3-R | TO-252   | G              | D | S | Tape Reel |

Note: Pin Assignment: G: Gate D: Drain S: Source

|               |                  |                                                 |
|---------------|------------------|-------------------------------------------------|
| 3NK90ZG-TA3-T | (1)Packing Type  | (1) T: Tube, R: Tape Reel                       |
|               | (2)Package Type  | (2) TA3: TO-220, TF1: TO-220F1, TN3: TO-252     |
|               | (3)Green Package | (3) G: Halogen Free and Lead Free, L: Lead Free |

#### MARKING



■ ABSOLUTE MAXIMUM RATING ( $T_C=25^{\circ}\text{C}$ , unless otherwise specified)

| PARAMETER                            |                        | SYMBOL    | RATINGS    | UNIT               |
|--------------------------------------|------------------------|-----------|------------|--------------------|
| Drain-Source Voltage                 |                        | $V_{DSS}$ | 900        | V                  |
| Gate-Source Voltage                  |                        | $V_{GSS}$ | $\pm 20$   | V                  |
| Drain Current                        | DC                     | $I_D$     | 3          | A                  |
|                                      | Pulsed (Note 2)        | $I_{DM}$  | 9          | A                  |
| Avalanche Energy                     | Single Pulsed (Note 3) | $E_{AS}$  | 200        | mJ                 |
| Peak Diode Recovery $dv/dt$ (Note 4) |                        | $dv/dt$   | 2.0        | V/ns               |
| Power Dissipation                    | TO-220                 | $P_D$     | 75         | W                  |
|                                      | TO-220F1               |           | 23         | W                  |
|                                      | TO-252                 |           | 51         | W                  |
| Junction Temperature                 |                        | $T_J$     | +150       | $^{\circ}\text{C}$ |
| Storage Temperature Range            |                        | $T_{STG}$ | -55 ~ +150 | $^{\circ}\text{C}$ |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 100\text{mH}$ ,  $I_{AS} = 2.0\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^{\circ}\text{C}$ .

4.  $I_{SD} \leq 3.0\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J=25^{\circ}\text{C}$ .

■ THERMAL DATA

| PARAMETER           |                 | SYMBOL        | RATINGS     | UNIT                        |
|---------------------|-----------------|---------------|-------------|-----------------------------|
| Junction to Ambient | TO-220/TO-220F1 | $\theta_{JA}$ | 62.5        | $^{\circ}\text{C}/\text{W}$ |
|                     | TO-252          |               | 110         | $^{\circ}\text{C}/\text{W}$ |
| Junction to Case    | TO-220          | $\theta_{JC}$ | 1.66        | $^{\circ}\text{C}/\text{W}$ |
|                     | TO-220F1        |               | 5.43        | $^{\circ}\text{C}/\text{W}$ |
|                     | TO-252          |               | 2.45 (Note) | $^{\circ}\text{C}/\text{W}$ |

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

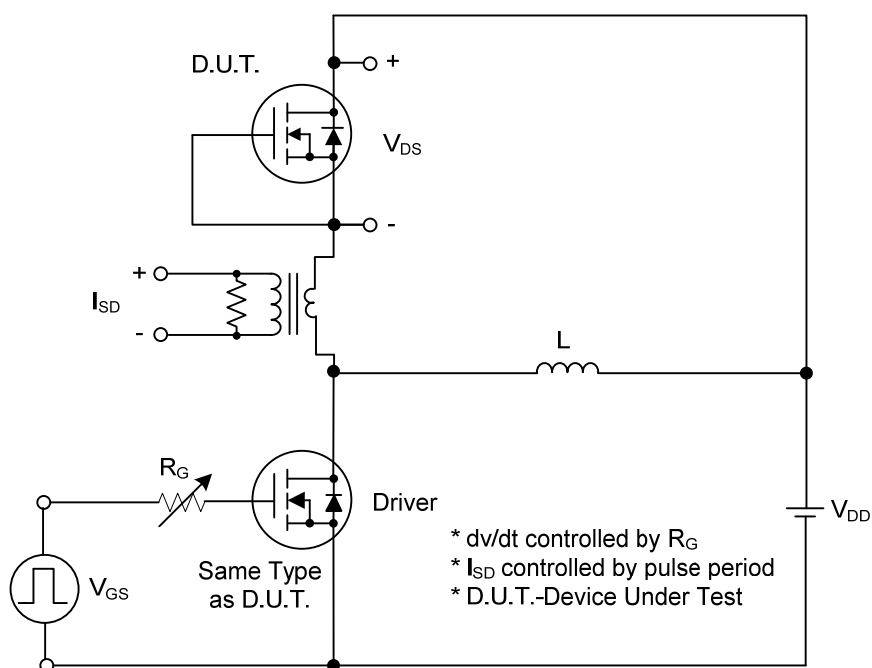
■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C unless otherwise specified)

| PARAMETER                                             |         | SYMBOL              | TEST CONDITIONS                                                                                       | MIN | TYP  | MAX | UNIT |
|-------------------------------------------------------|---------|---------------------|-------------------------------------------------------------------------------------------------------|-----|------|-----|------|
| OFF CHARACTERISTICS                                   |         |                     |                                                                                                       |     |      |     |      |
| Drain-Source Breakdown Voltage                        |         | BV <sub>DSS</sub>   | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA                                                            | 900 |      |     | V    |
| Drain-Source Leakage Current                          |         | I <sub>DSS</sub>    | V <sub>DS</sub> =900V, V <sub>GS</sub> =0V                                                            |     |      | 10  | μA   |
| Gate-Source Leakage Current                           | Forward | I <sub>GSS</sub>    | V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V                                                            |     |      | +10 | μA   |
|                                                       | Reverse |                     | V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V                                                            |     |      | -10 | μA   |
| ON CHARACTERISTICS                                    |         |                     |                                                                                                       |     |      |     |      |
| Gate Threshold Voltage                                |         | V <sub>GS(TH)</sub> | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                                              | 3.0 |      | 5.0 | V    |
| Static Drain-Source On-State Resistance               |         | R <sub>DS(ON)</sub> | V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A                                                            |     | 5.2  | 6.4 | Ω    |
| DYNAMIC PARAMETERS                                    |         |                     |                                                                                                       |     |      |     |      |
| Input Capacitance                                     |         | C <sub>ISS</sub>    | V <sub>GS</sub> =0V, V <sub>DS</sub> =50V, f=1.0MHz                                                   |     | 515  |     | pF   |
| Output Capacitance                                    |         | C <sub>OSS</sub>    |                                                                                                       |     | 36.8 |     | pF   |
| Reverse Transfer Capacitance                          |         | C <sub>RSS</sub>    |                                                                                                       |     | 5.1  |     | pF   |
| SWITCHING PARAMETERS                                  |         |                     |                                                                                                       |     |      |     |      |
| Total Gate Charge                                     |         | Q <sub>G</sub>      | V <sub>DS</sub> =720V, V <sub>GS</sub> =10V, I <sub>D</sub> =3.0A<br>(Note 1, 2)                      |     | 22.8 |     | nC   |
| Gate to Source Charge                                 |         | Q <sub>GS</sub>     |                                                                                                       |     | 10.3 |     | nC   |
| Gate to Drain Charge                                  |         | Q <sub>GD</sub>     |                                                                                                       |     | 4.9  |     | nC   |
| Turn-ON Delay Time                                    |         | t <sub>D(ON)</sub>  | V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =3.0A,<br>R <sub>G</sub> =25Ω (Note 1, 2) |     | 10   |     | ns   |
| Rise Time                                             |         | t <sub>R</sub>      |                                                                                                       |     | 16   |     | ns   |
| Turn-OFF Delay Time                                   |         | t <sub>D(OFF)</sub> |                                                                                                       |     | 36   |     | ns   |
| Fall-Time                                             |         | t <sub>F</sub>      |                                                                                                       |     | 29   |     | ns   |
| SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS        |         |                     |                                                                                                       |     |      |     |      |
| Maximum Continuous Drain-Source Diode Forward Current |         | I <sub>S</sub>      |                                                                                                       |     |      | 3   | A    |
| Maximum Pulsed Drain-Source Diode Forward Current     |         | I <sub>SM</sub>     |                                                                                                       |     |      | 9   | A    |
| Diode Forward Voltage                                 |         | V <sub>SD</sub>     | I <sub>F</sub> =3.0A, V <sub>GS</sub> =0V                                                             |     |      | 1.4 | V    |
| Reverse Recovery Time                                 |         | t <sub>rr</sub>     | I <sub>S</sub> =3.0A, V <sub>GS</sub> =0V,                                                            |     | 644  |     | ns   |
| Reverse Recovery Charge (Note 1)                      |         | Q <sub>rr</sub>     | dI <sub>F</sub> /dt = 100 A/μs                                                                        |     | 3.0  |     | uC   |

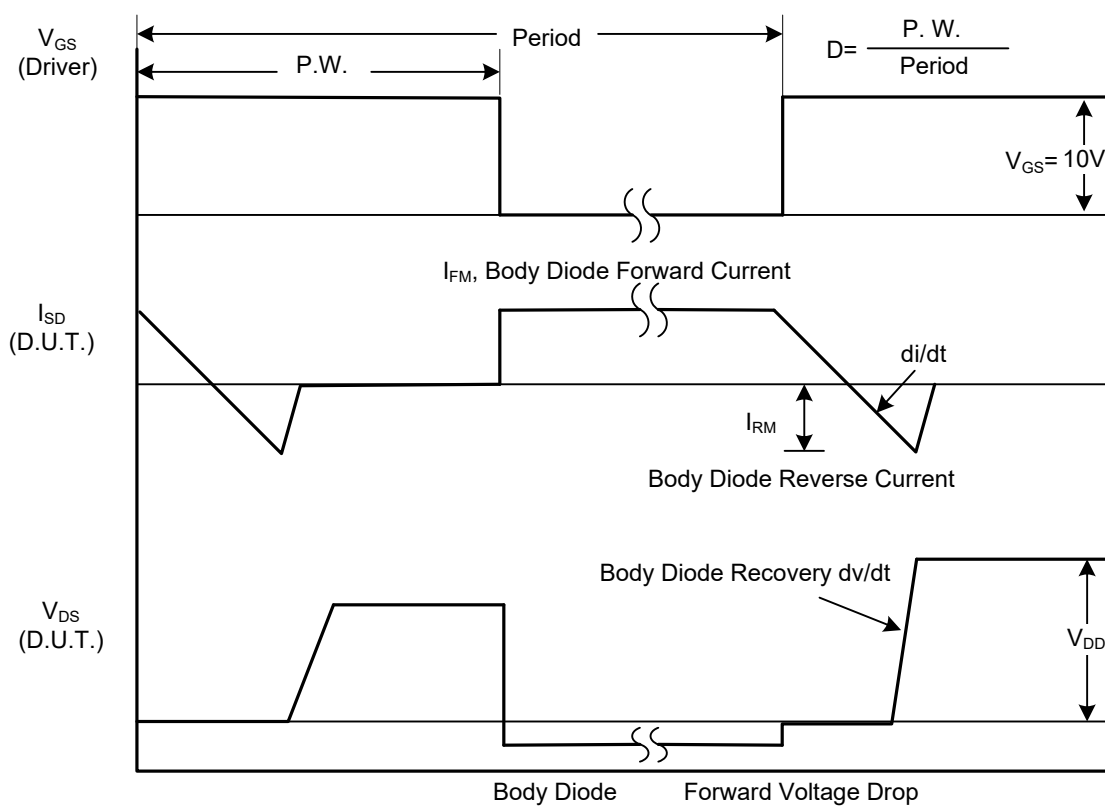
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

# ■ TEST CIRCUITS AND WAVEFORMS

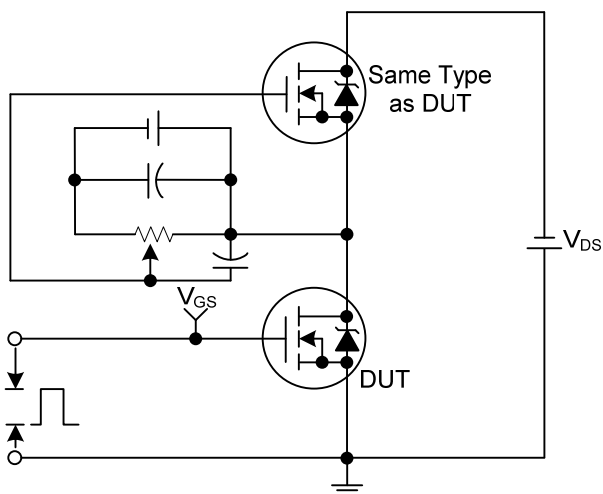


Peak Diode Recovery dv/dt Test Circuit

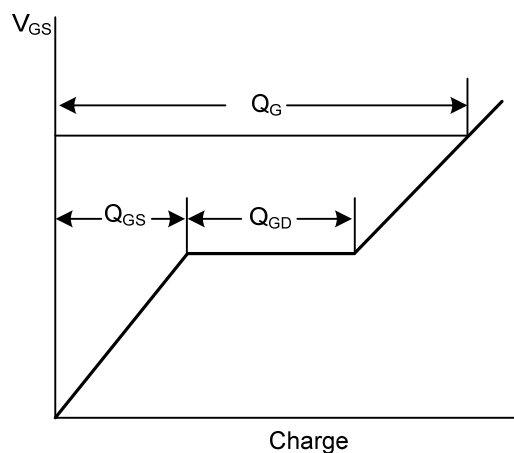


Peak Diode Recovery dv/dt Waveforms

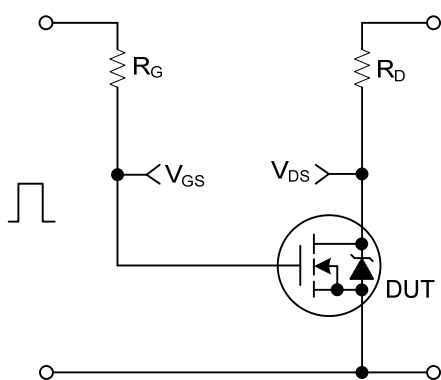
## ■ TEST CIRCUITS AND WAVEFORMS



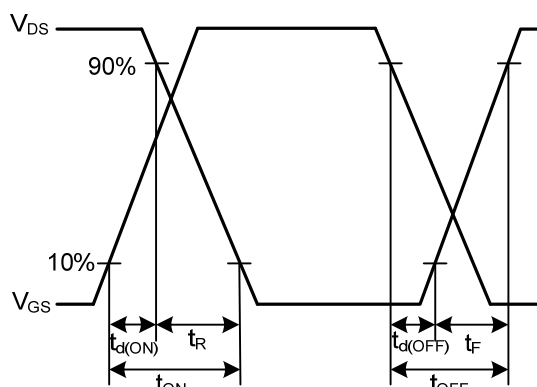
Gate Charge Test Circuit



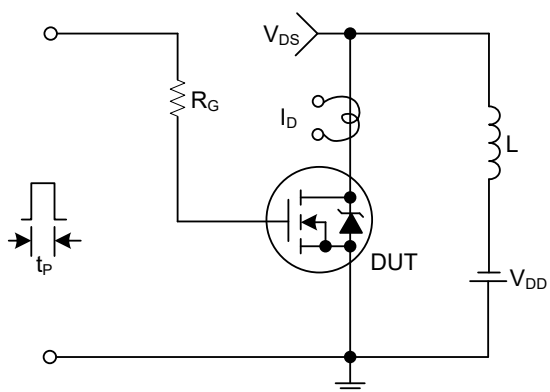
Gate Charge Waveforms



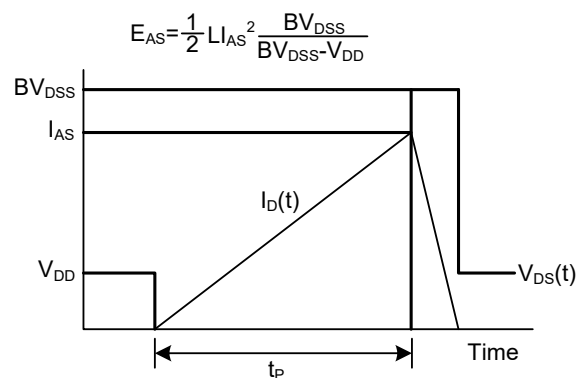
Resistive Switching Test Circuit



Resistive Switching Waveforms

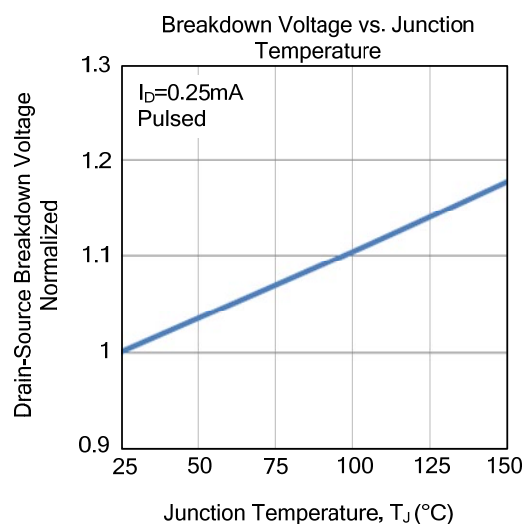
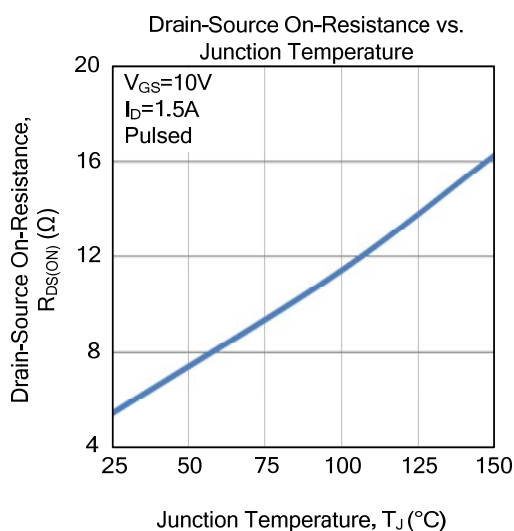
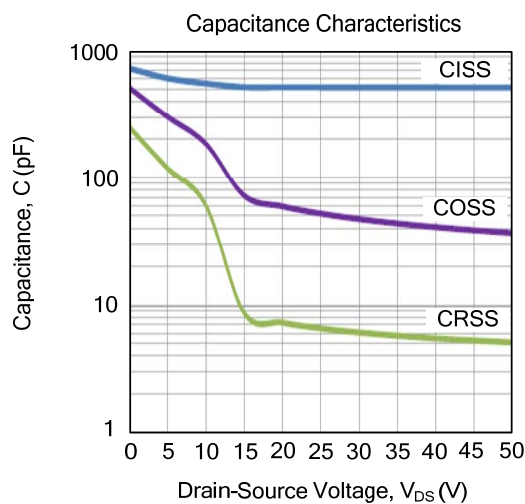
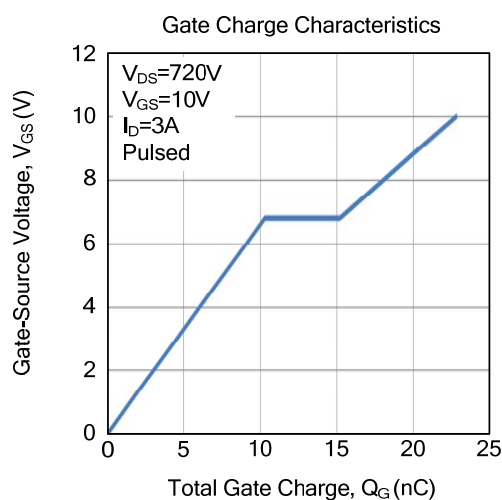
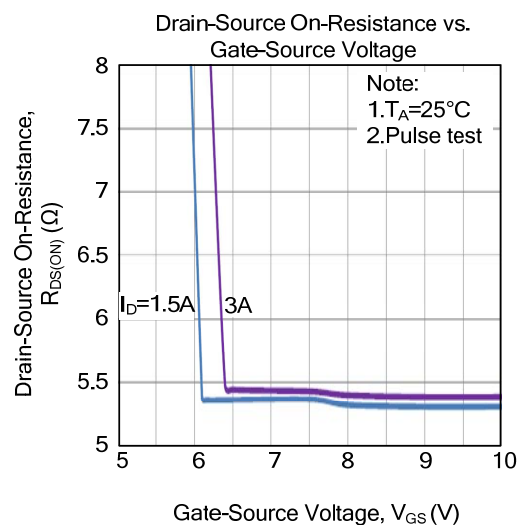
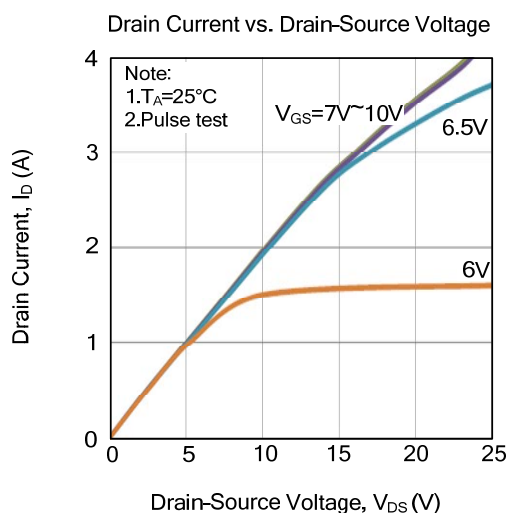


Unclamped Inductive Switching Test Circuit

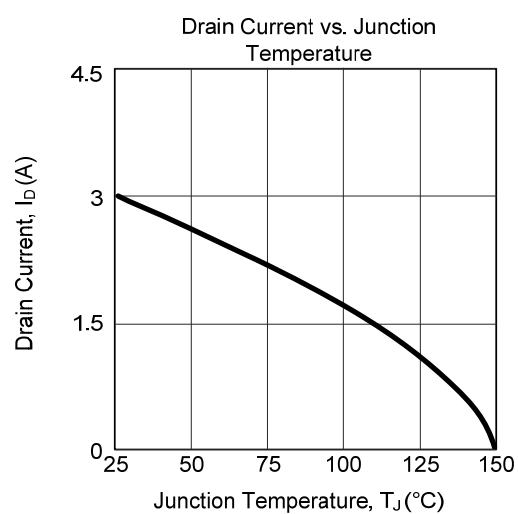
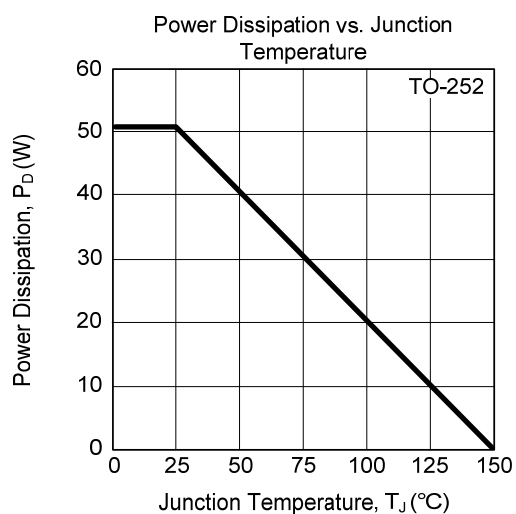
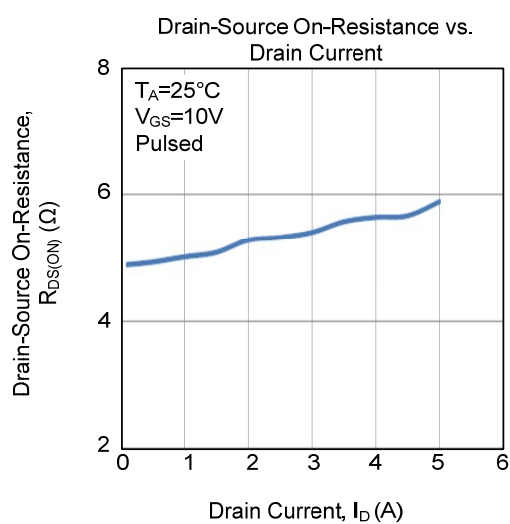
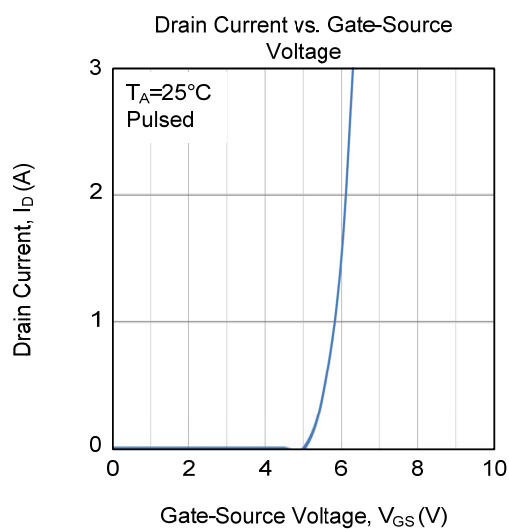
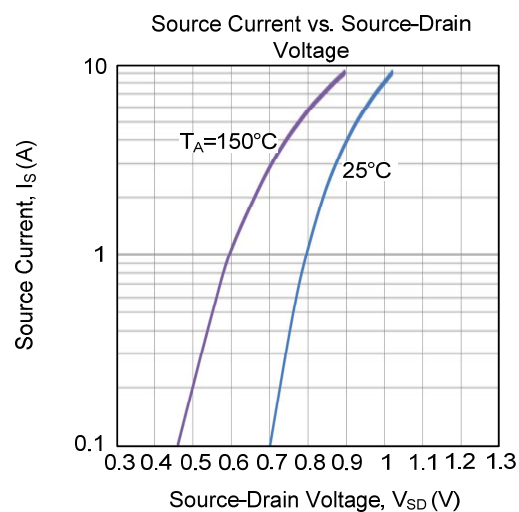
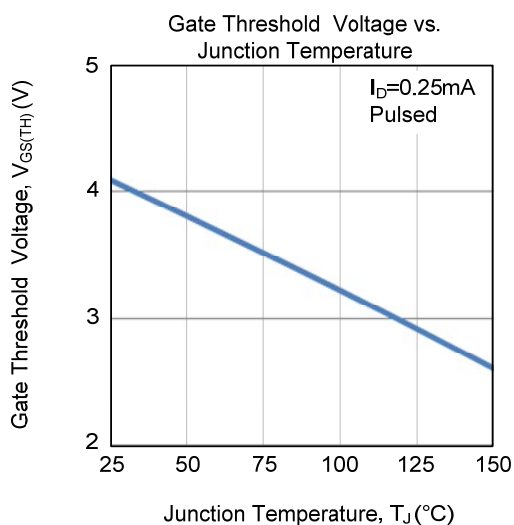


Unclamped Inductive Switching Waveforms

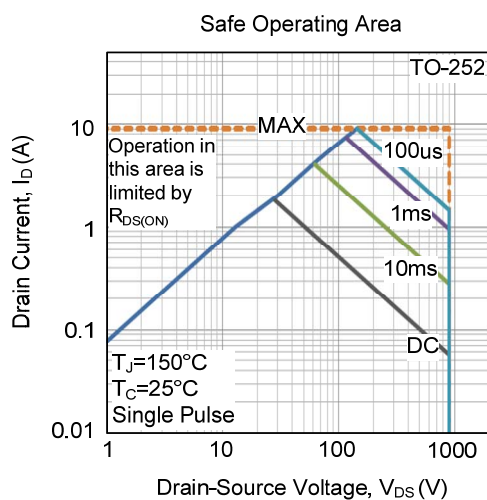
## ■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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