



UNISONIC TECHNOLOGIES CO., LTD

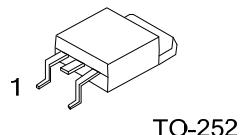
5N25Z-Q

Power MOSFET

5A, 250V N-CHANNEL
POWER MOSFET

■ DESCRIPTION

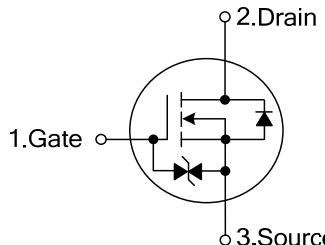
The UTC **5N25Z-Q** is a N-channel enhancement mode Power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.



■ FEATURES

- * $R_{DS(ON)} \leq 2.0 \Omega$ @ $V_{GS}=10V$, $I_D=2.5A$
- * High switching speed
- * 100% avalanche tested

■ SYMBOL



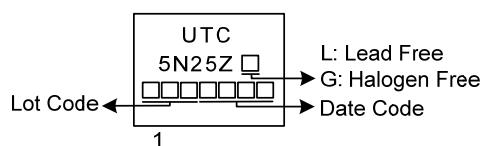
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5N25ZL-TN3-R	5N25ZG-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

5N25ZG-TN3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	250	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current	Continuous	I _D	5	A
	Pulsed	I _{DM}	10	A
Avalanche Energy		E _{AS}	54	mJ
Power Dissipation		P _D	40	W
Junction Temperature		T _J	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 6.2mH, I_{AS} = 4.2A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C

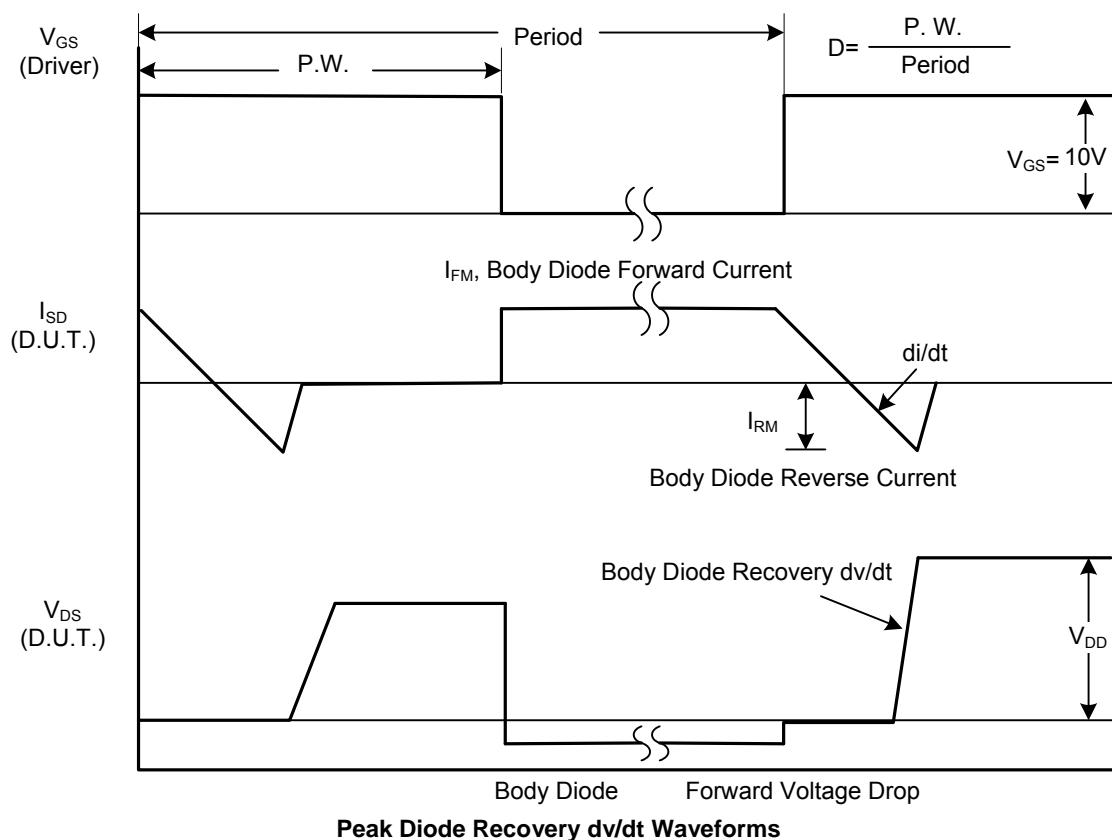
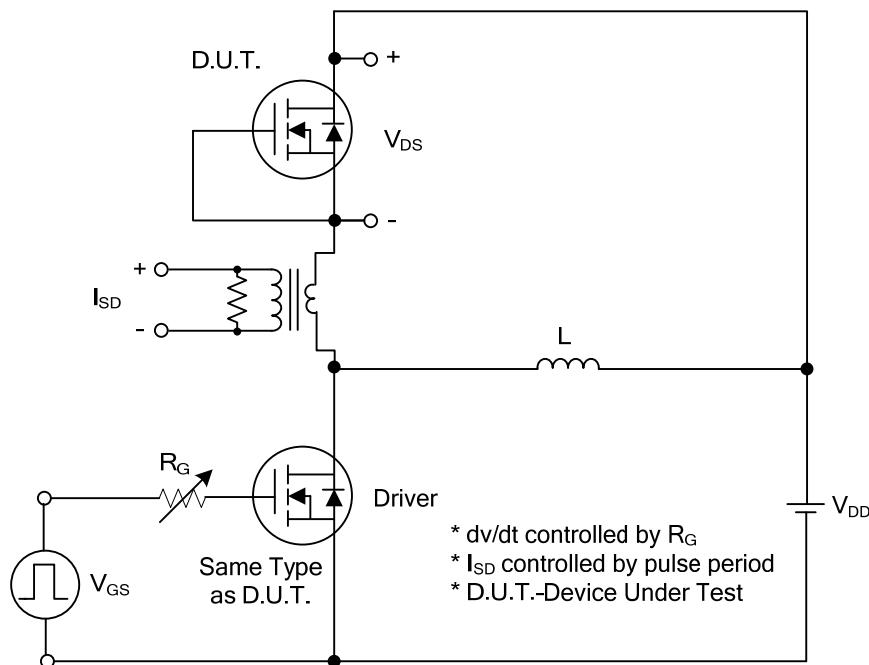
■ ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	250			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =250V			10	μA
Gate-Source Leakage Current	Forward	V _{GS} =+20V, V _{DS} =0V			10	μA
	Reverse	V _{GS} =-20V, V _{DS} =0V			-10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =2.5A			2.0	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1MHz		190		pF
Output Capacitance	C _{OSS}			35		pF
Reverse Transfer Capacitance	C _{RSS}			5		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{DS} =200V, V _{GS} =10V, I _D =5.0A I _G =1mA (Note 1, 2)		11		nC
Gate to Source Charge	Q _{GS}			5		nC
Gate to Drain Charge	Q _{GD}			2		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DS} =100V, V _{GS} =10V, I _D =5.0A, R _G =25Ω (Note 1, 2)		5		ns
Rise Time	t _R			16		ns
Turn-OFF Delay Time	t _{D(OFF)}			10		ns
Fall-Time	t _F			18		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				5	A
Maximum Body-Diode Pulsed Current	I _{SM}				10	A
Drain-Source Diode Forward Voltage	V _{SD}	I _S =5.0A			1.3	V
Reverse Recovery Time (Note 1)	t _{rr}	I _S =5.0A , V _{GS} =0V di/dt=100A/μs		135		ns
Reverse Recovery Charge	Q _{rr}			1.3		μC

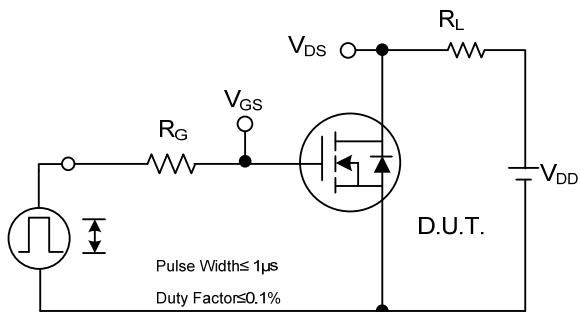
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

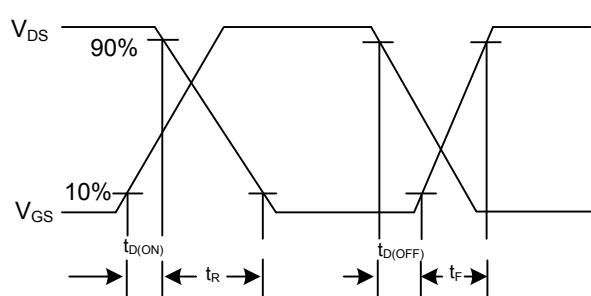
■ TEST CIRCUITS AND WAVEFORMS



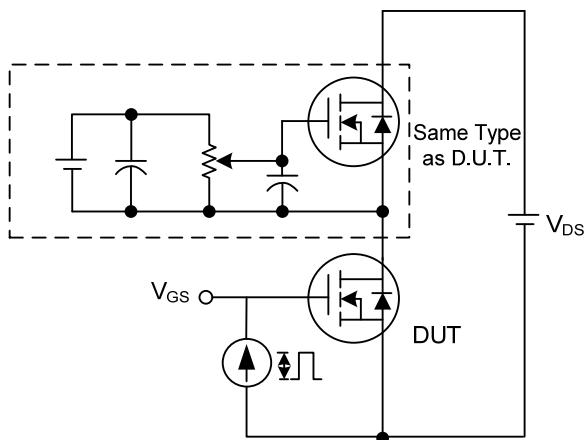
■ TEST CIRCUITS AND WAVEFORMS



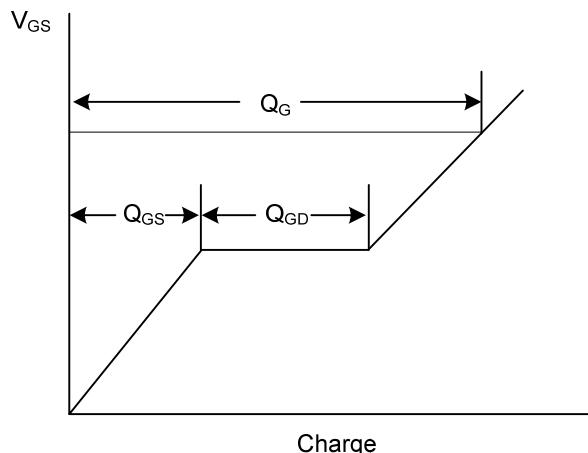
Switching Test Circuit



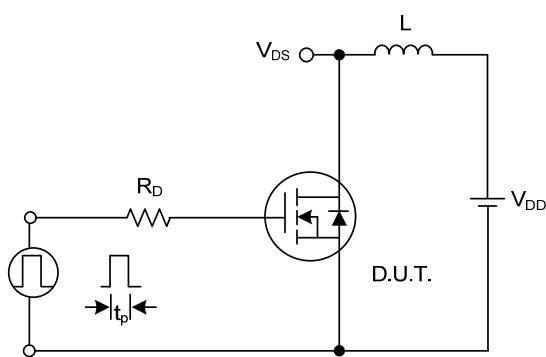
Switching Waveforms



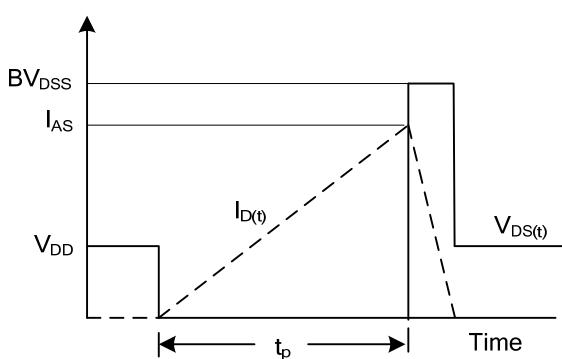
Gate Charge Test Circuit



Gate Charge Waveform

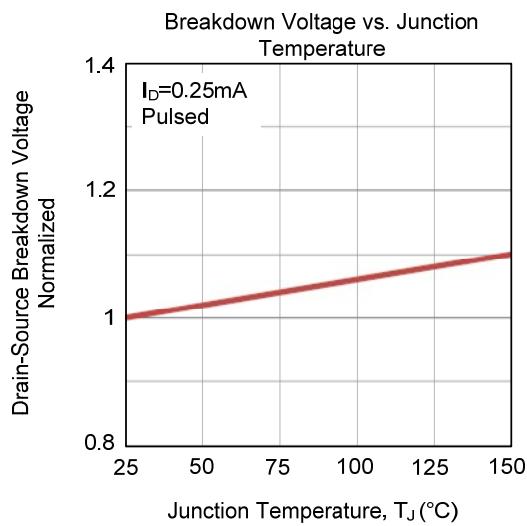
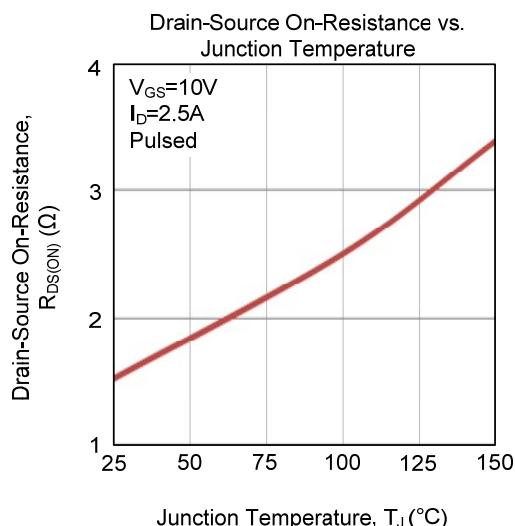
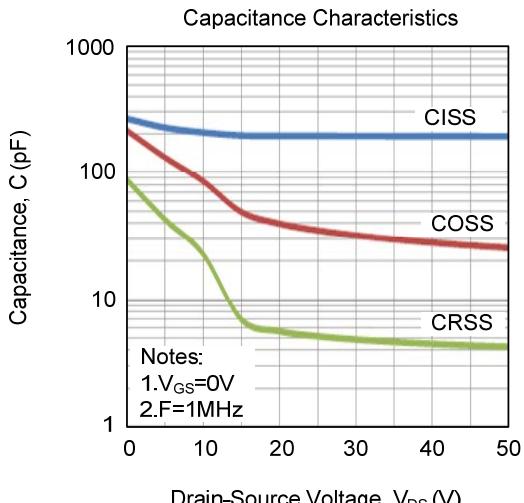
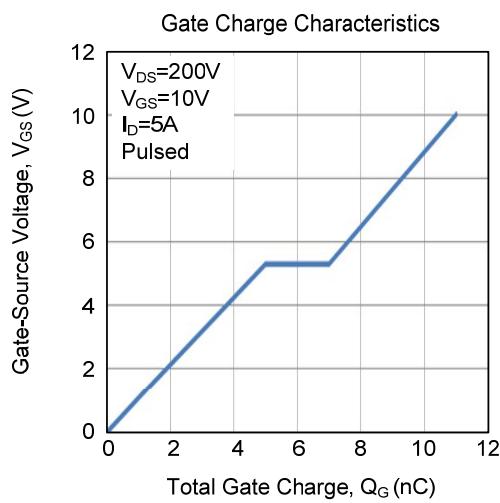
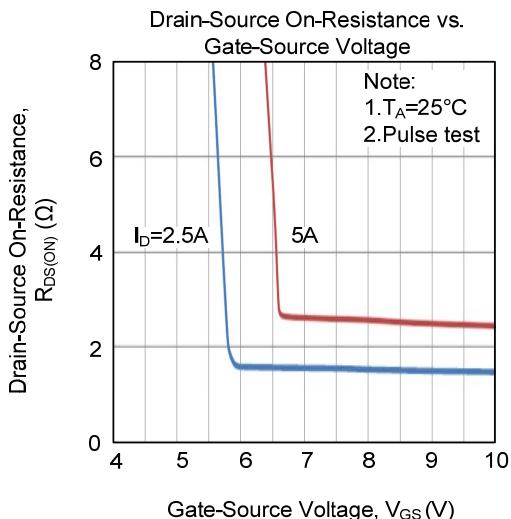
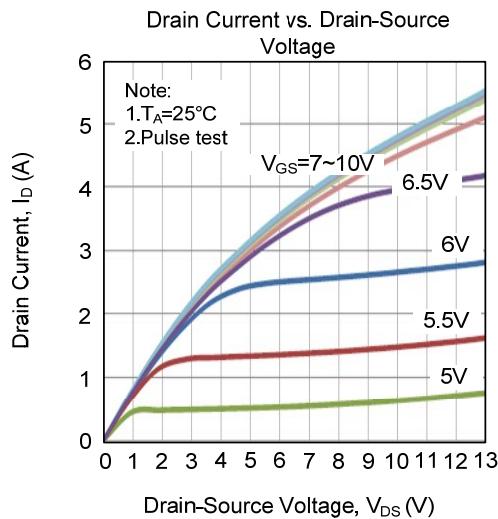


Unclamped Inductive Switching Test Circuit

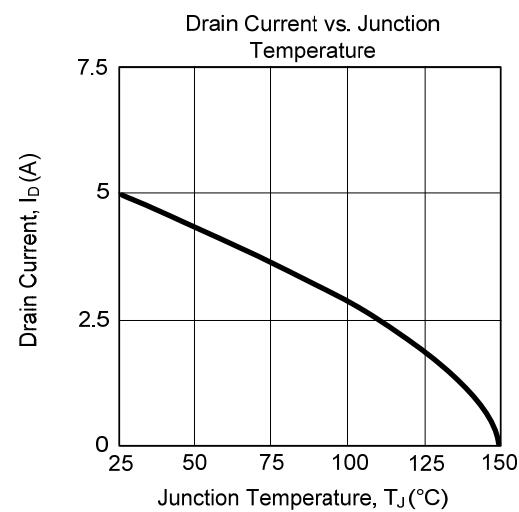
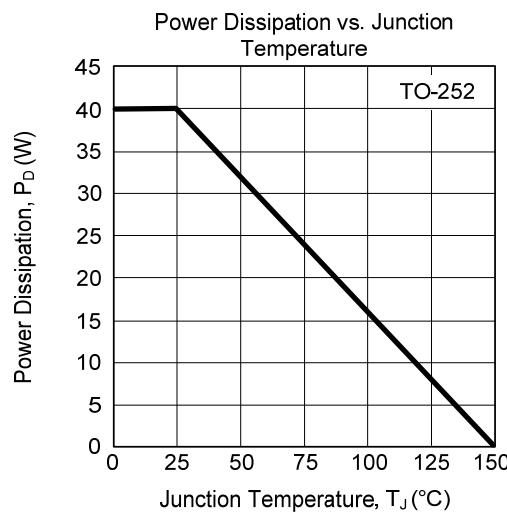
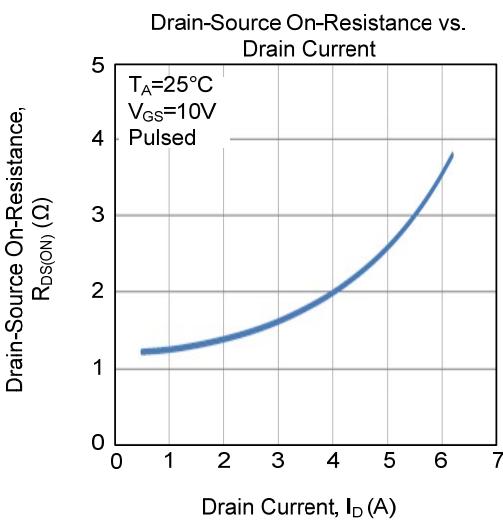
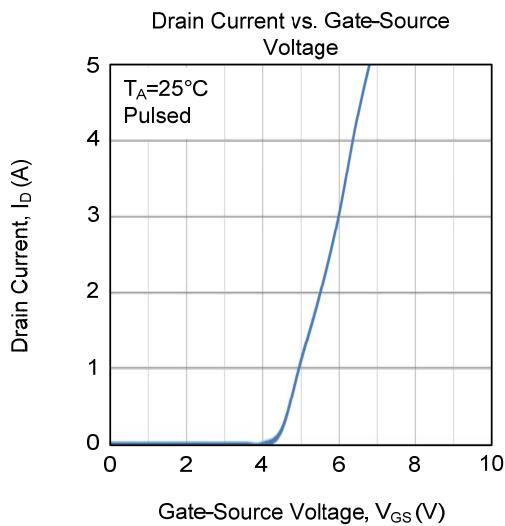
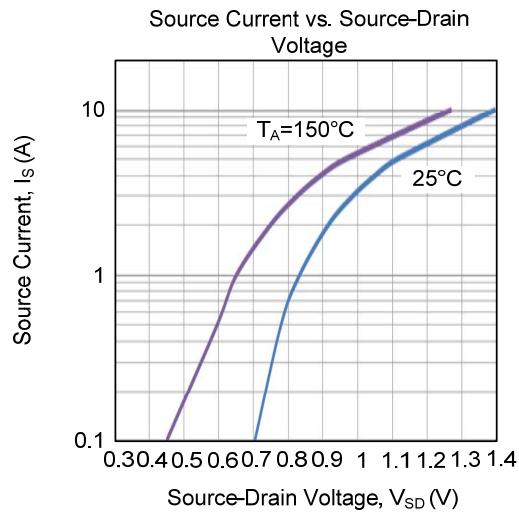
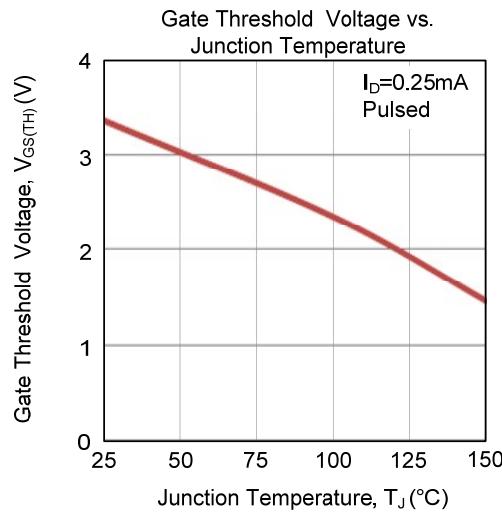


Unclamped Inductive Switching Waveforms

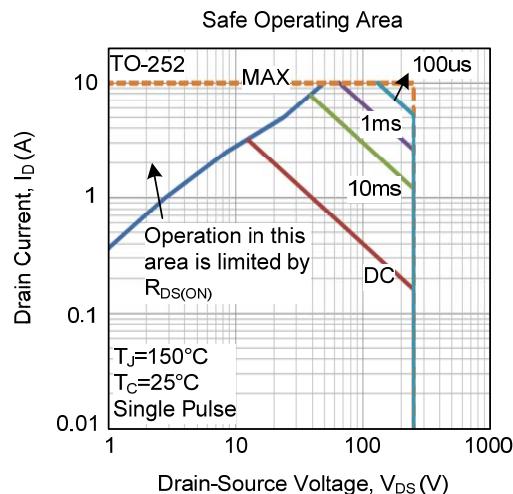
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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