

5N65-MLQ

Power MOSFET

5.0A, 650V N-CHANNEL POWER MOSFET

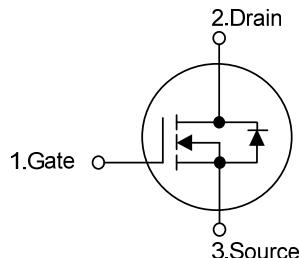
■ DESCRIPTION

The UTC **5N65-MLQ** is a high voltage power MOSFET combines advanced trench MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

- * $R_{DS(ON)} \leq 2.6 \Omega$ @ $V_{GS}=10V$, $I_D=2.0A$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

■ SYMBOL



■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
5N65L-TA3-T	5N65G-TA3-T	TO-220	G	D	S	Tube
5N65L-TF1-T	5N65G-TF1-T	TO-220F1	G	D	S	Tube
5N65L-TF2-T	5N65G-TF2-T	TO-220F2	G	D	S	Tube
5N65L-TF3-T	5N65G-TF3-T	TO-220F	G	D	S	Tube
5N65L-TM3-T	5N65G-TM3-T	TO-251	G	D	S	Tube
5N65L-TN3-R	5N65G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

5N65G-TA3-T

(1)Packing Type

(2)Package Type

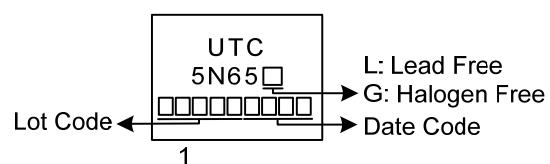
(3)Green Package

(1) T: Tube, R: Tape Reel

(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2,
TF3: TO-220F, TM3: TO-251, TN3: TO-252

(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current	I_D	5	A
Pulsed Drain Current (Note 2)	I_{DM}	10	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	173 mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	2.1 V/ns	
Power Dissipation	TO-220	P_D	105 W
	TO-220F/TO-220F1		32 W
	TO-220F2		49 W
	TO-251/TO-252		
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 30\text{mH}$, $I_{AS} = 3.4\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 5.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2		110	$^\circ\text{C/W}$
	TO-251/TO-252			
Junction to Case	TO-220	θ_{JC}	1.19	$^\circ\text{C/W}$
	TO-220F/TO-220F1		3.9	$^\circ\text{C/W}$
	TO-220F2			
	TO-251/TO-252		2.55 (Note)	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

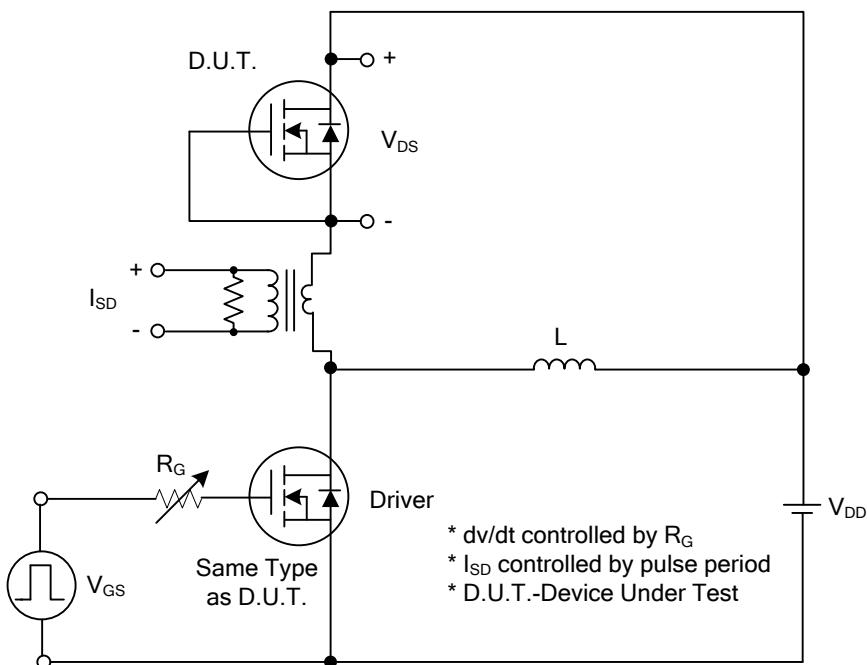
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	650			V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$		10		μA
Gate- Source Leakage Current	Forward	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=2.0\text{A}$			2.6	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$		560		pF
Output Capacitance	C_{OSS}			55		pF
Reverse Transfer Capacitance	C_{RSS}			5		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q_G	$\text{V}_{\text{DS}}=520\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=4.0\text{A}$ $\text{I}_G=1\text{mA}$ (Note 1, 2)		13		nC
Gate-Source Charge	Q_{GS}			4		nC
Gate-Drain Charge	Q_{GD}			2.2		nC
Turn-On Delay Time (Note 1)	$t_{\text{D(ON)}}$			7		ns
Turn-On Rise Time	t_R			16		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			36		ns
Turn-Off Fall Time	t_F			22		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Maximum Body-Diode Continuous Current	I_S				5	A
Maximum Body-Diode Pulsed Current	I_{SM}				10	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$\text{I}_S=5.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time (Note 1)	t_{rr}	$\text{I}_S=5.0\text{A}, \text{V}_{\text{GS}}=0\text{V}$ $d\text{i}/d\text{t}=100\text{A}/\mu\text{s}$		250		ns
Reverse Recovery Charge	Q_{rr}			4.5		μC

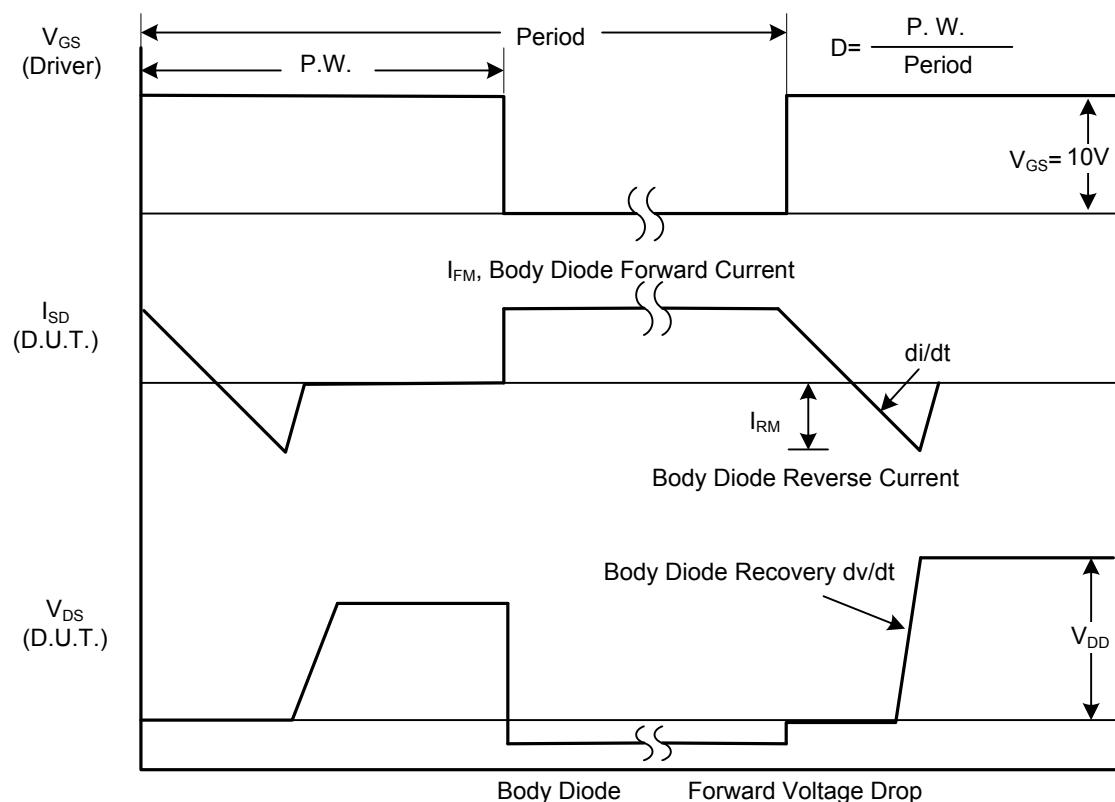
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

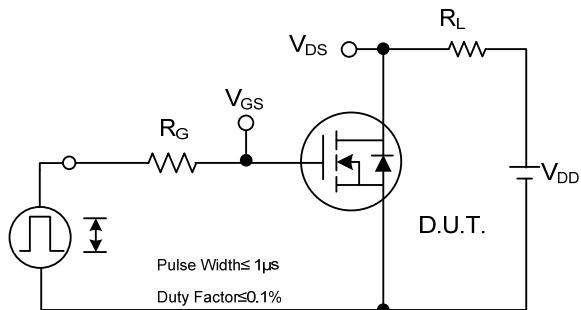


Peak Diode Recovery dv/dt Test Circuit

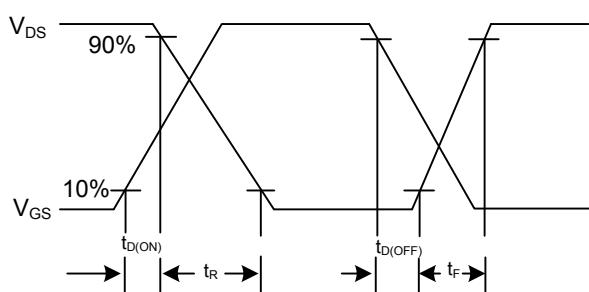


Peak Diode Recovery dv/dt Waveforms

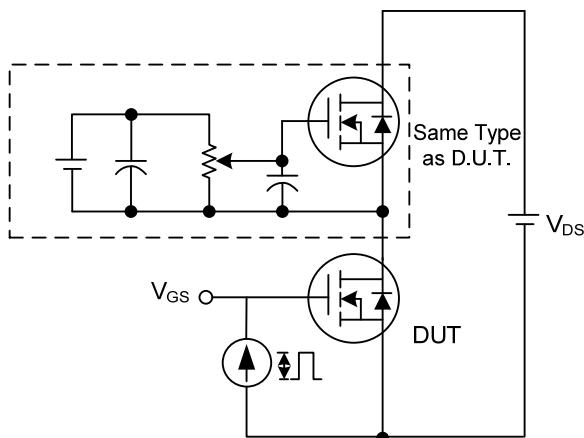
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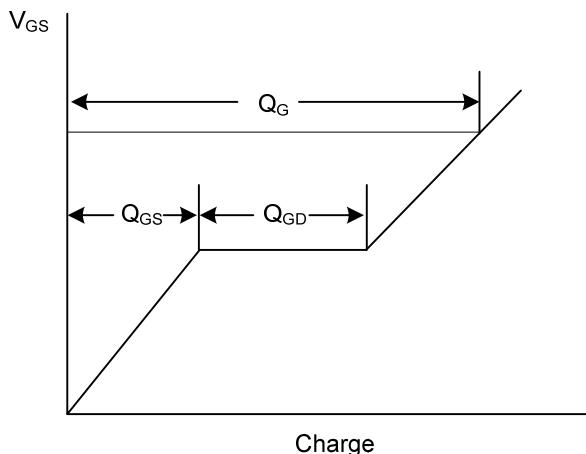
Switching Test Circuit



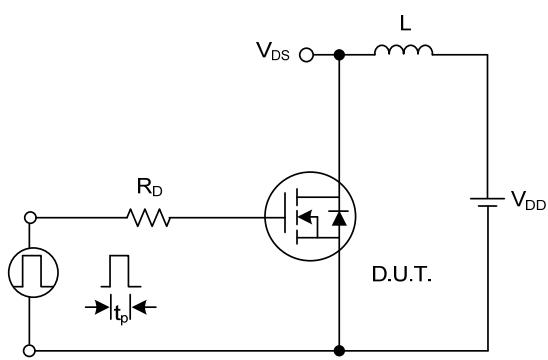
Switching Waveforms



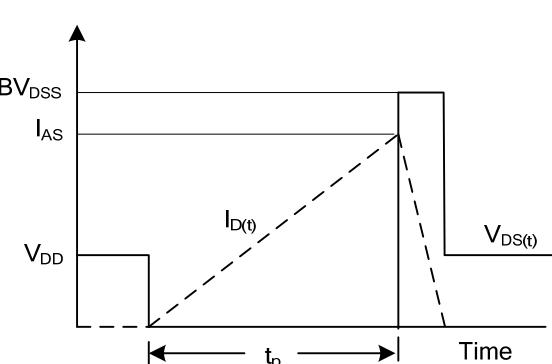
Gate Charge Test Circuit



Gate Charge Waveform

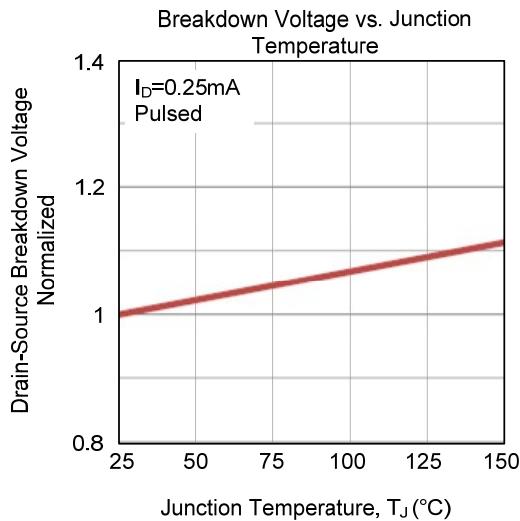
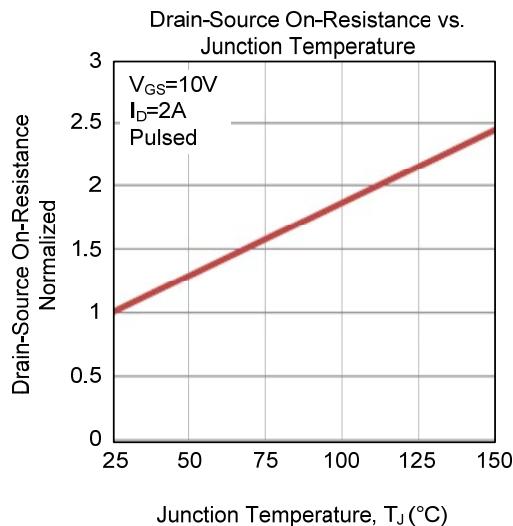
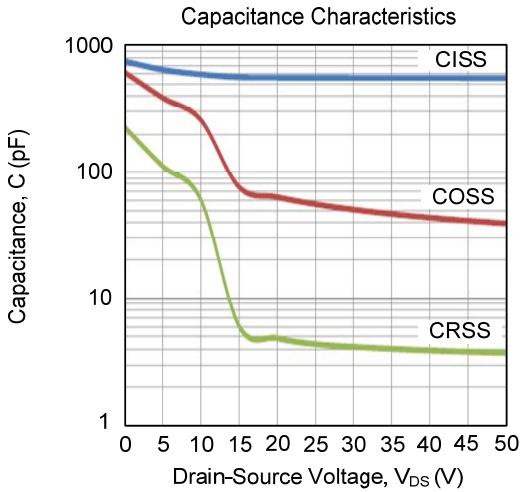
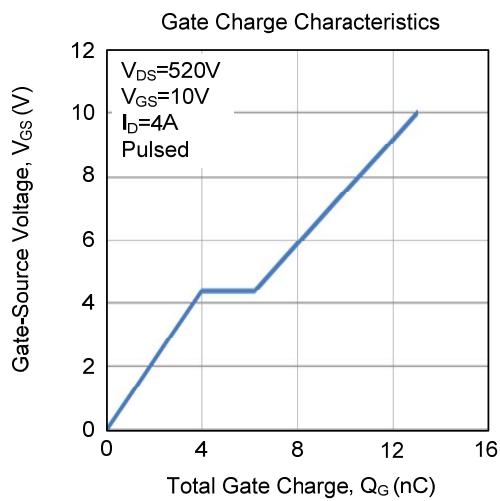
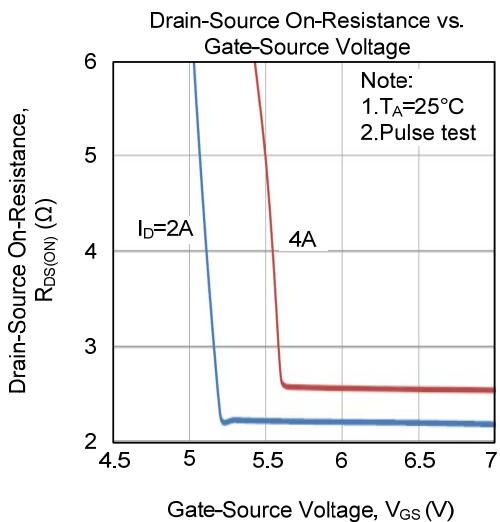
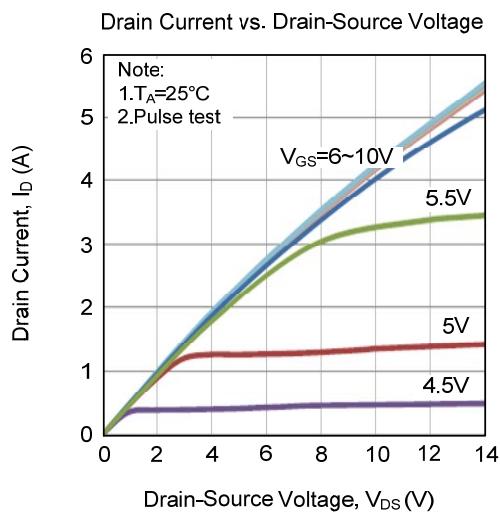


Unclamped Inductive Switching Test Circuit

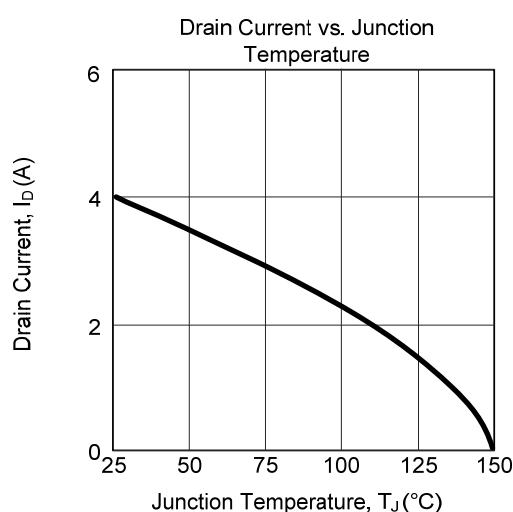
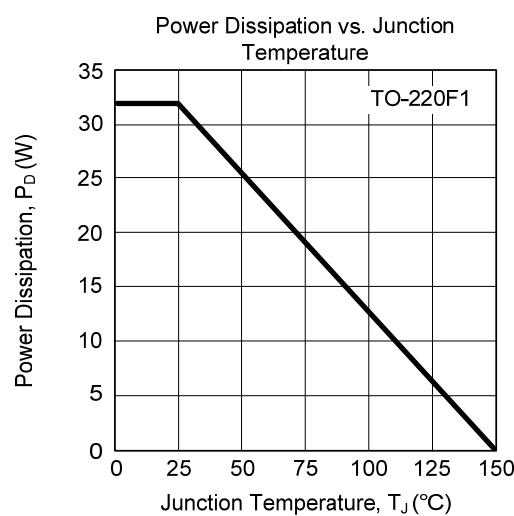
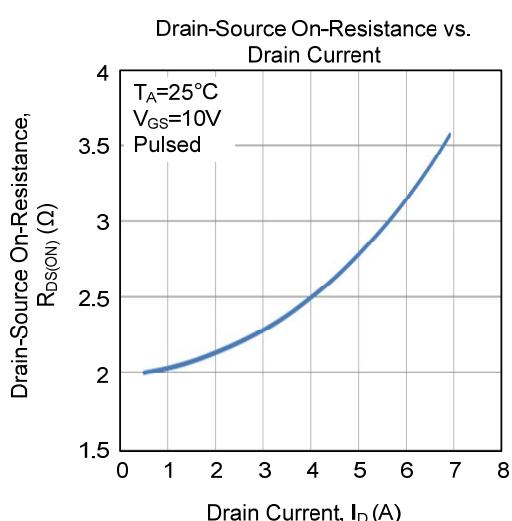
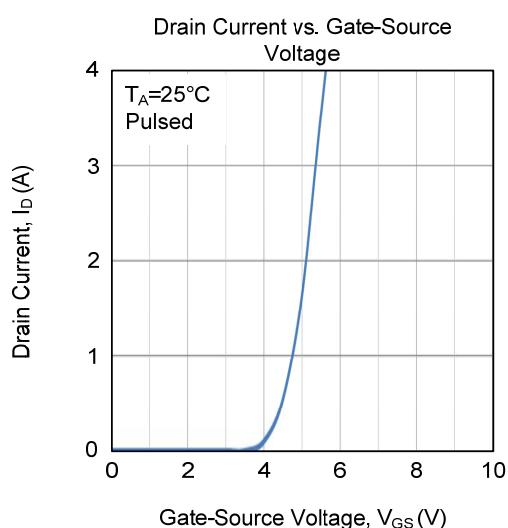
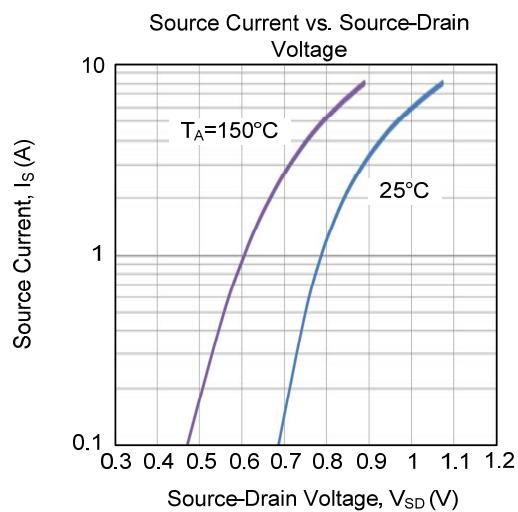
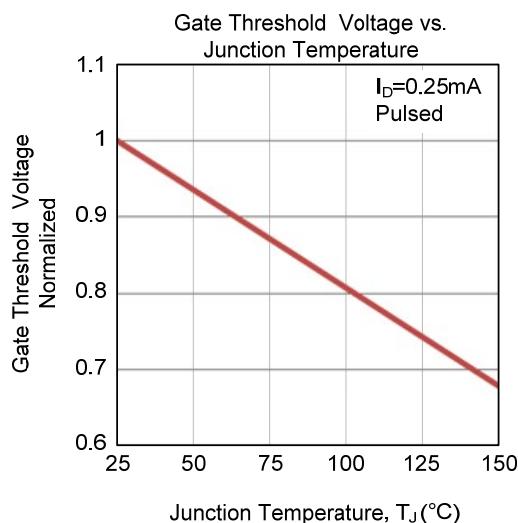


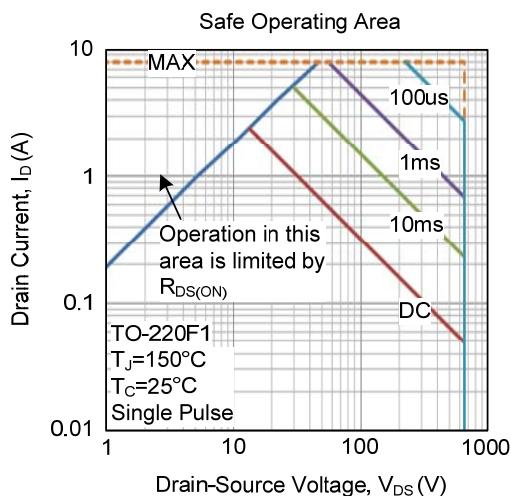
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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