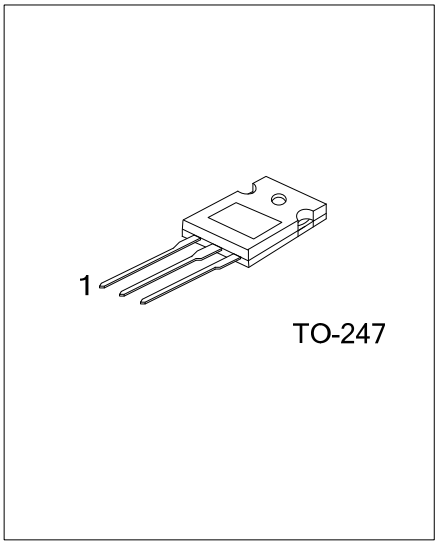
70A, 300V N-CHANNEL POWER MOSFET



■ DESCRIPTION

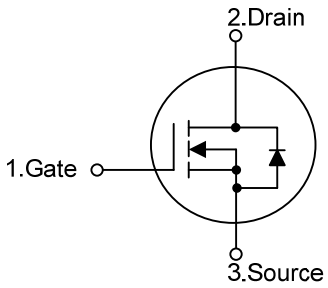
The UTC **70N30-HC** is a N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect $R_{DS(ON)}$, high switching speed, high current capacity and low gate charge.

The UTC **70N30-HC** is universally applied in low voltage such as automotive, high efficiency switching for AC/DC converters and DC motor control, etc.

■ FEATURES

* $R_{DS(ON)} \leq 45\ m\Omega$ @ V_{GS}=10V, I_D=35A
* High Switching Speed

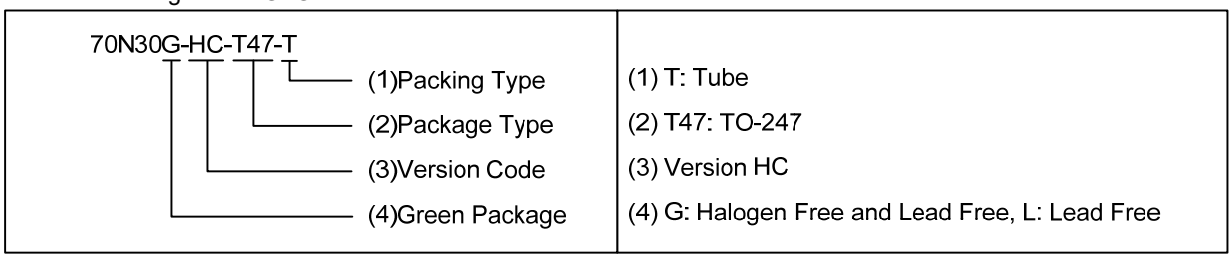
■ SYMBOL



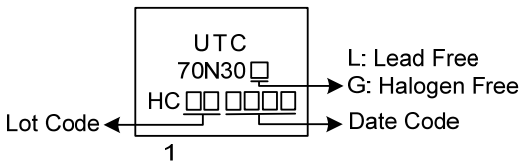
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
70N30L-HC-T47- T	70N30G-HC-T47 -T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	300	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current	Continuous	I_D	70	A
	Pulsed	I_{DM}	140	A
Single Pulsed Avalanche Energy		E_{AS}	1654	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	16.5	V/ns
Power Dissipation		P_D	320	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 30mH, I_{AS} = 10.5A, V_{DD} = 90V, R_G = 25Ω, Starting T_J = 25°C
4. I_{SD} ≤ 30A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

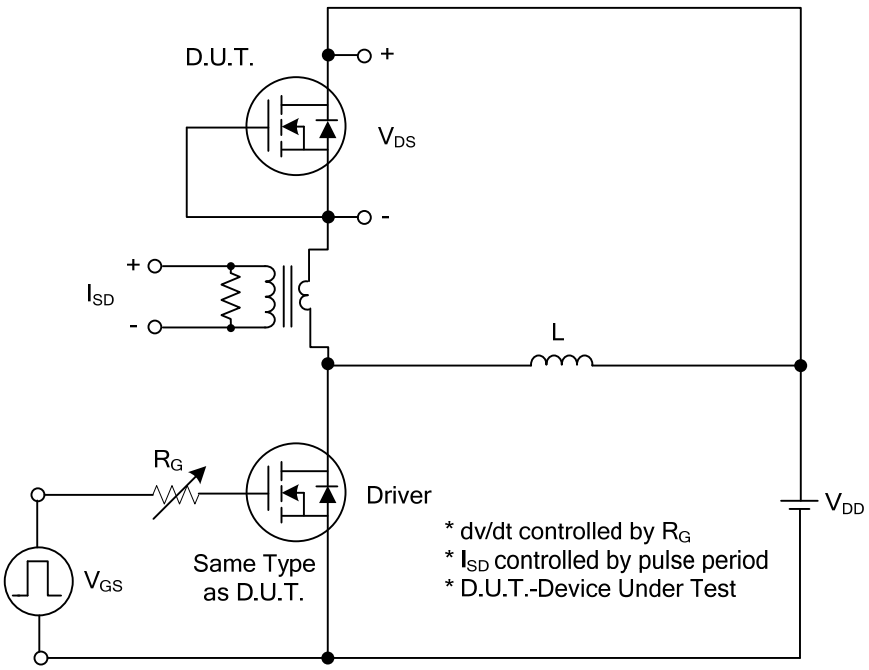
PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	50	°C/W
Junction to Case	θ_{JC}	0.39	°C/W

■ ELECTRICAL CHARACTERISTICS

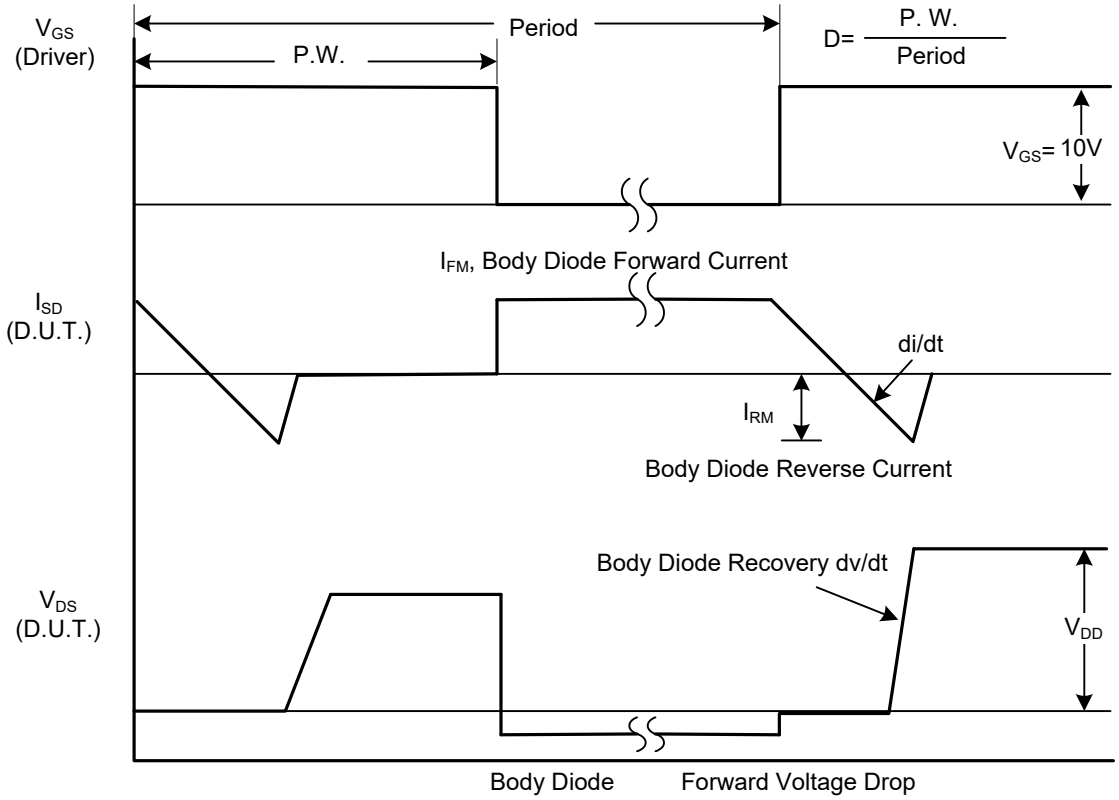
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	300			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=300V, V_{GS}=0V			10	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+30V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-30V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=35A			45	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		8086		pF
Output Capacitance		C_{OSS}			980		pF
Reverse Transfer Capacitance		C_{RSS}			28		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{DS}=240V, V_{GS}=10V, I_D=70A (Note 1, 2)		185		nC
Gate to Source Charge		Q_{GS}			55		nC
Gate to Drain Charge		Q_{GD}			64		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=100V, V_{GS}=10V, I_D=70A, R_G=25Ω (Note 1, 2)		112		ns
Rise Time		t_R			89		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			280		ns
Fall-Time		t_F			106		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				70	A
Maximum Body-Diode Pulsed Current		I_{SM}				140	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=70A, V_{GS}=0V			1.4	V
Body Diode Reverse Recovery Time		t_{rr}	I_S=30A, V_{GS}=0V, dI_F/dt=100A/μs (Note 1)		316		ns
Reverse Recovery Charge		Q_{rr}			3.6		μC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

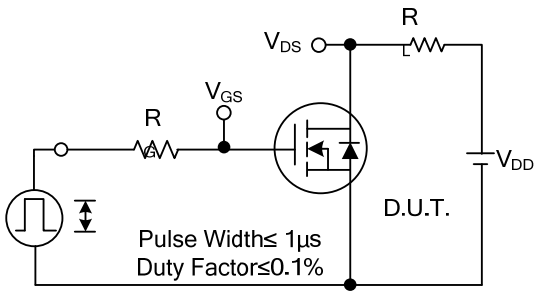


Peak Diode Recovery dv/dt Test Circuit

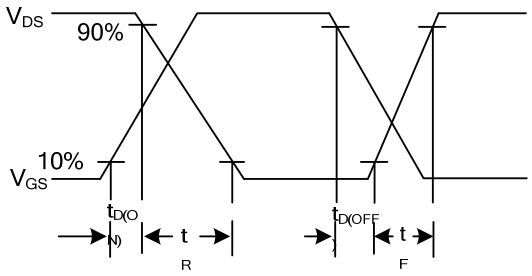


Peak Diode Recovery dv/dt Waveforms

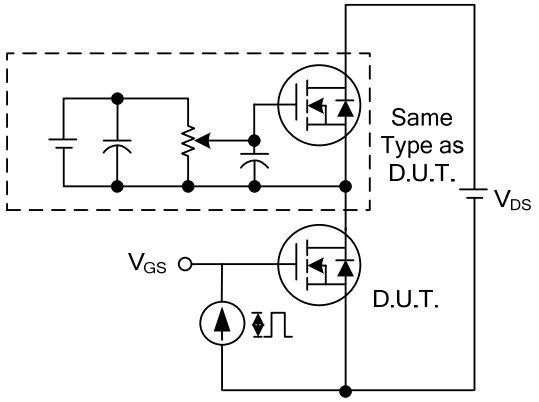
■ TEST CIRCUITS AND WAVEFORMS



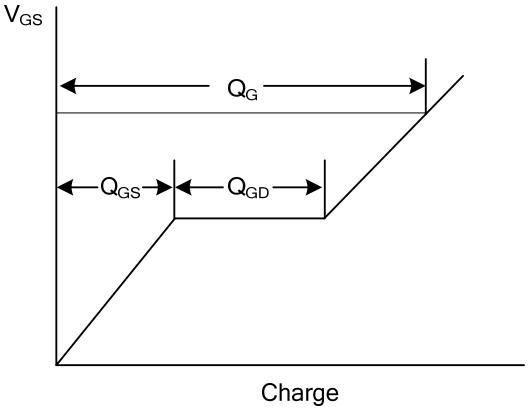
Switching Test Circuit



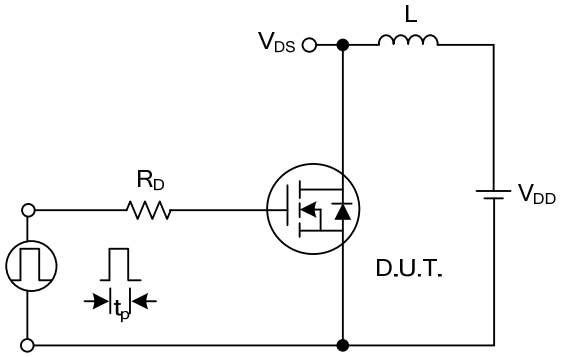
Switching Waveforms



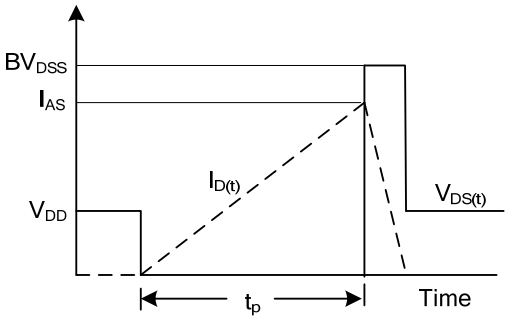
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms