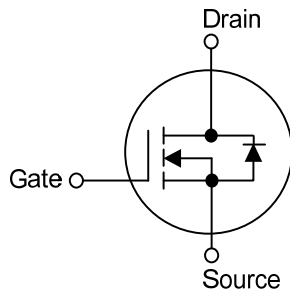


**75NM65****Power MOSFET****75A, 650V N-CHANNEL
SUPER-JUNCTION MOSFET****DESCRIPTION**

The **UTC 75NM65** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

FEATURES

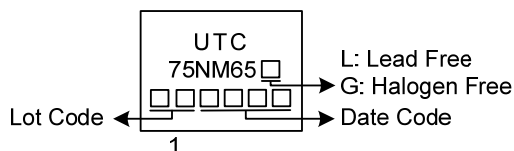
- * $R_{DS(ON)} \leq 42 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=37.5\text{A}$
- * Fast switching capability
- * Avalanche energy tested
- * Improved dv/dt capability, high ruggedness

SYMBOL**ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment				Packing
Lead Free	Halogen Free		1	2	3	4	
75NM65L-T47-T	75NM65G-T47-T	TO-247	G	D	S	-	Tube
75NM65L-T474-T	75NM65G-T474-T	TO-247-4	D	S	S	G	Tube
75NM65L-T64-T	75NM65G-T64-T	TO-264	G	D	S	-	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

75NM65G-T47-T (1) Packing Type (2) Package Type (3) Green Package	(1) T: Tube (2) , T47: TO-247, T474: TO-247-4, T64: TO-264 (3) G: Halogen Free and Lead Free, L: Lead Free
---	---

MARKING

■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	650	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	Continuous	I _D	75	A
	Pulsed (Note 2)	I _{DM}	150	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	1711	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	6	V/ns
Power Dissipation	TO-247/TO-247-4	P _D	240	W
	TO-264		446	W
Junction Temperature		T _J	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=10mH, I_{AS}=18.5A, V_{DD}=50V, R_G=25Ω, Starting T_J = 25°C.

4. I_{SD} ≤ 30A, di/dt ≤ 200A/μs, V_{DD} ≤ V_{(BR)DSS}, T_J = 25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ _{JA}	40	°C/W
Junction to Case	TO-247/TO-247-4	θ _{JC}	0.52	°C/W
	TO-264		0.28	°C/W

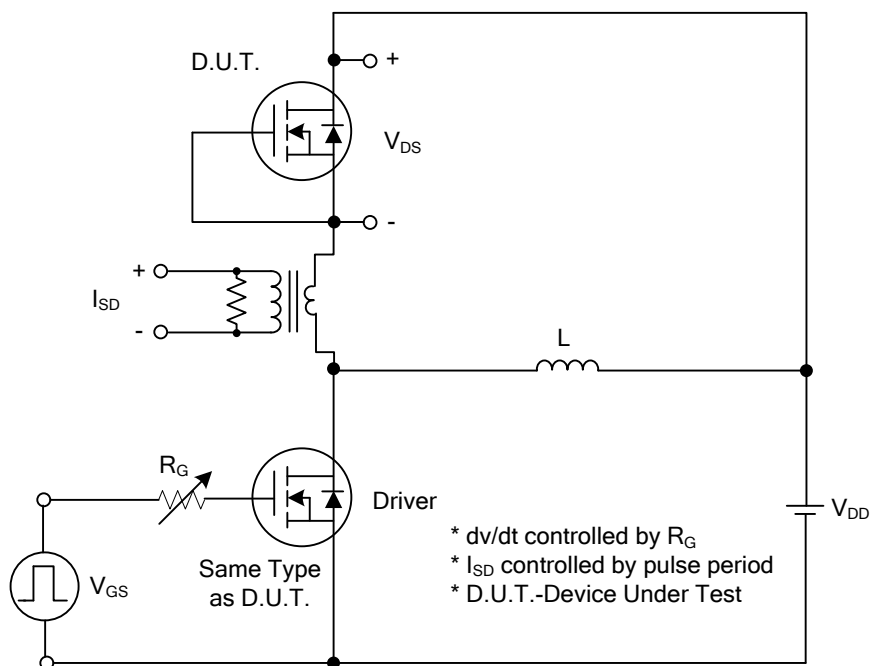
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	650			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V			10	μA
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±30V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =37.5A			42	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		6400		pF
Output Capacitance	C _{OSS}			4400		pF
Reverse Transfer Capacitance	C _{RSS}			280		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =520V, V _{GS} =10V, I _D =75A, I _G =1mA (Note 1, 2)		220		nC
Gate to Source Charge	Q _{GS}			29		nC
Gate to Drain Charge	Q _{GD}			89		nC
Turn-ON Delay Time (Note 1)	t _{D(ON)}	V _{DD} =100V, V _{GS} =10V, I _D =75A, R _G =3.3Ω (Note 1, 2)		30		ns
Rise Time	t _R			28		ns
Turn-OFF Delay Time	t _{D(OFF)}			130		ns
Fall-Time	t _F			29		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				75	A
Maximum Body-Diode Pulsed Current	I _{SM}				150	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =75A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =75A, V _{GS} =0V,		710		ns
Body Diode Reverse Recovery Charge	Q _{rr}	dI _F /dt=100A/μs		16.4		μC

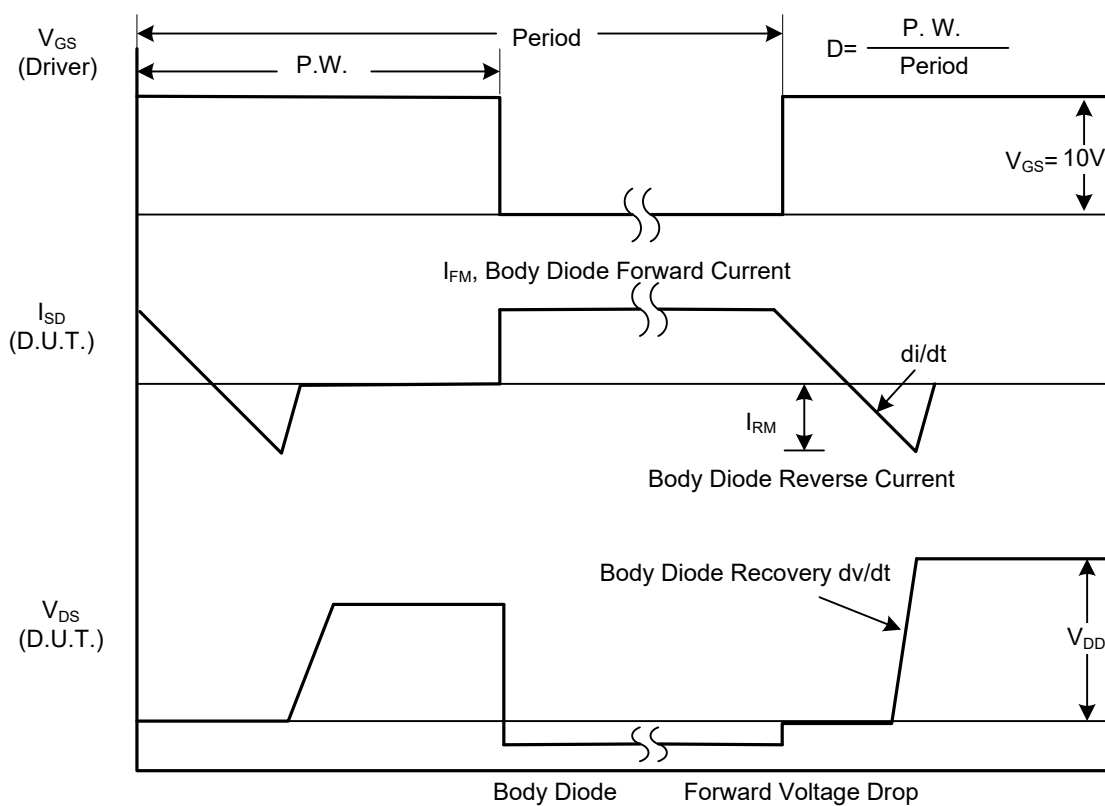
Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

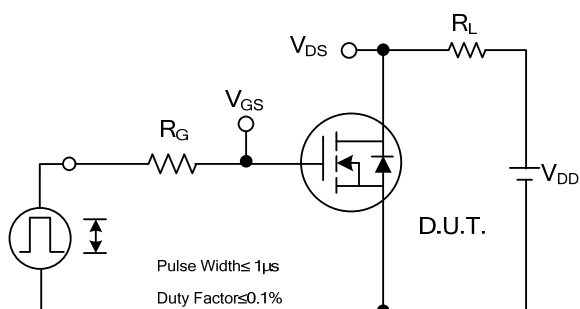


Peak Diode Recovery dv/dt Test Circuit

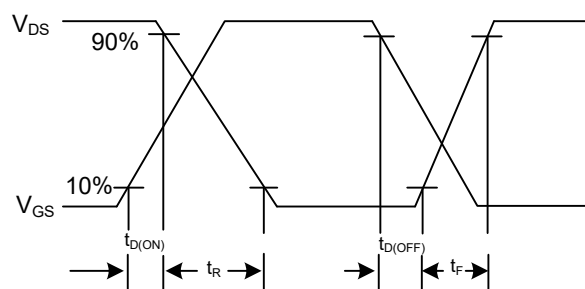


Peak Diode Recovery dv/dt Waveforms

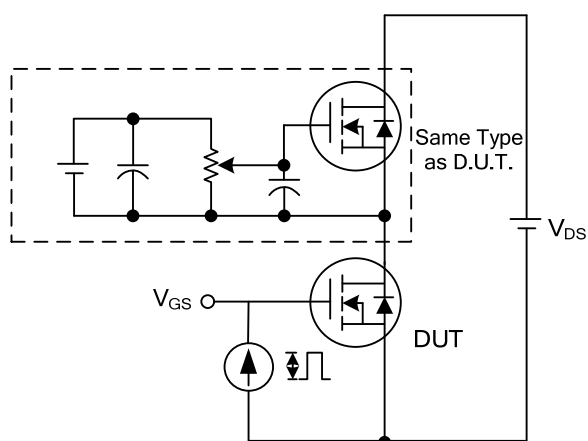
■ TEST CIRCUITS AND WAVEFORMS



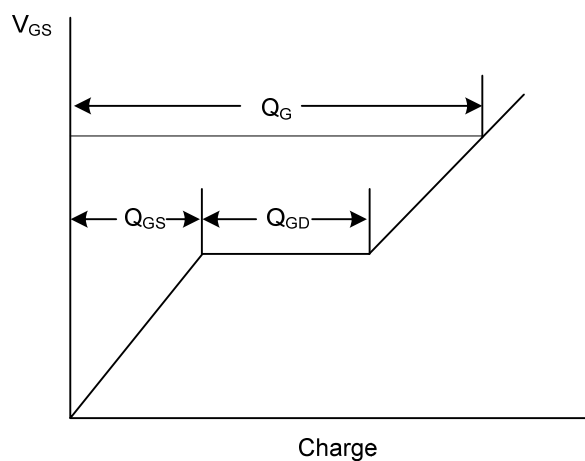
Switching Test Circuit



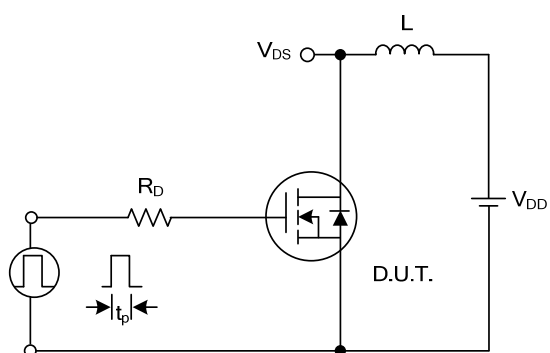
Switching Waveforms



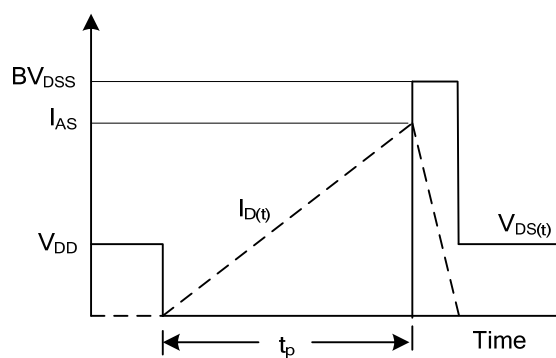
Gate Charge Test Circuit



Gate Charge Waveform

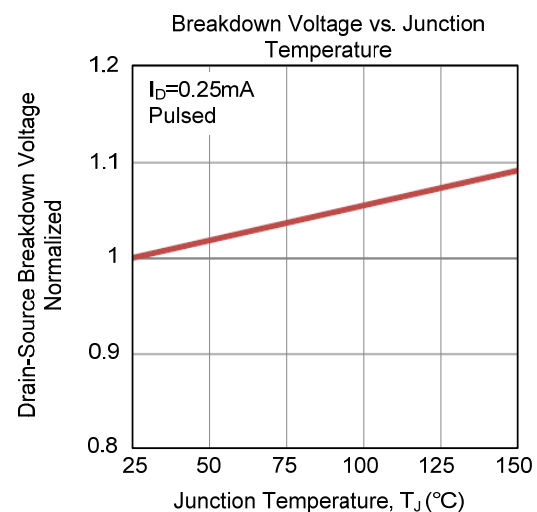
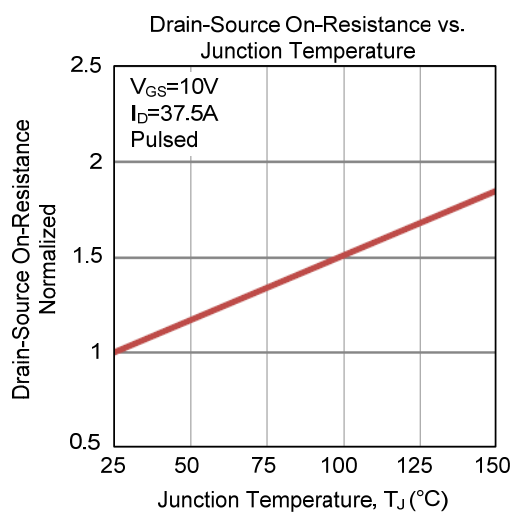
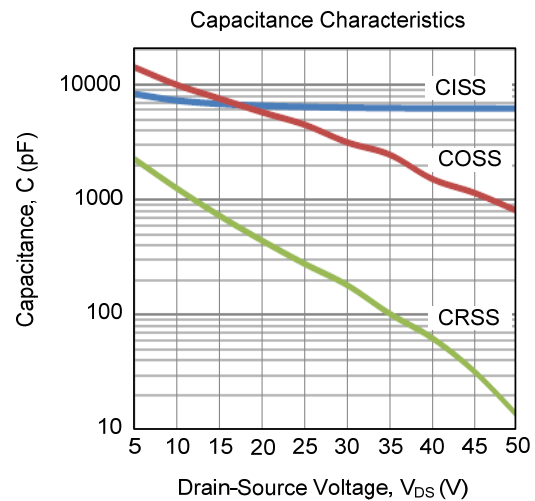
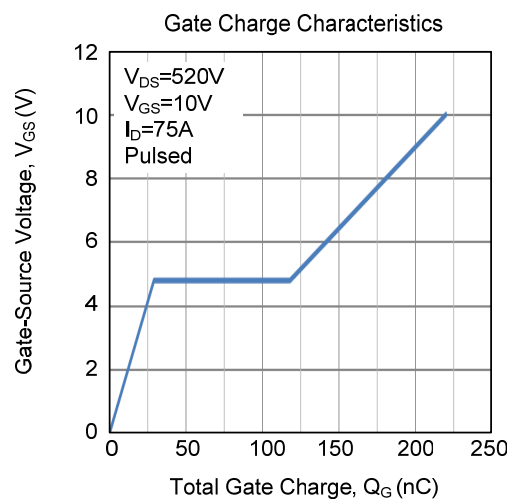
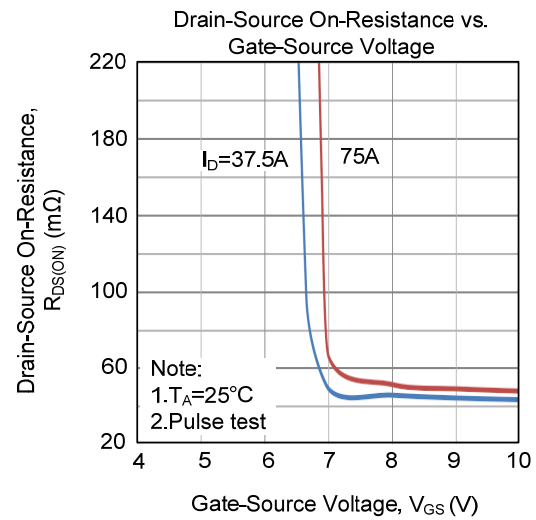
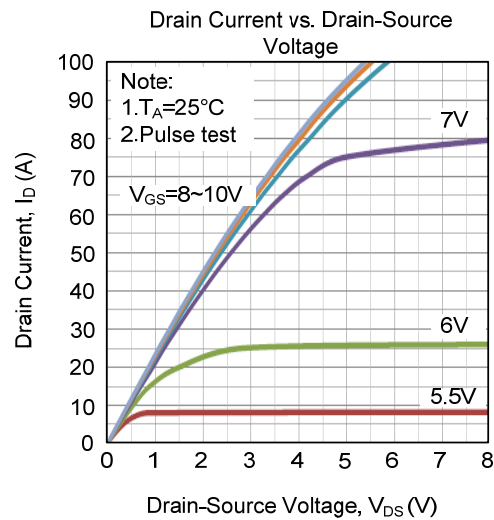


Unclamped Inductive Switching Test Circuit

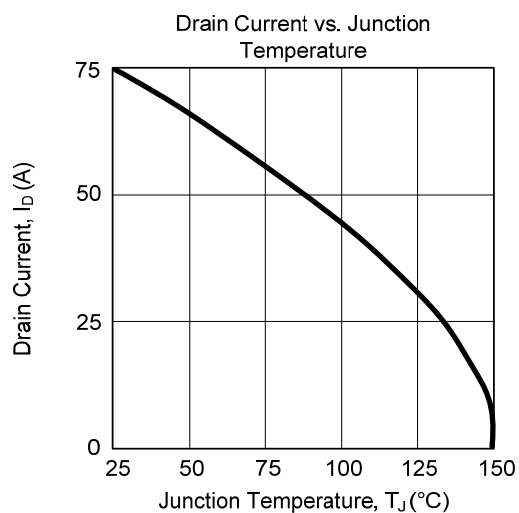
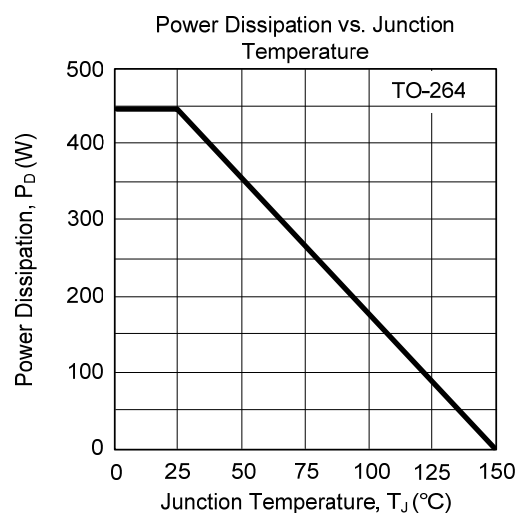
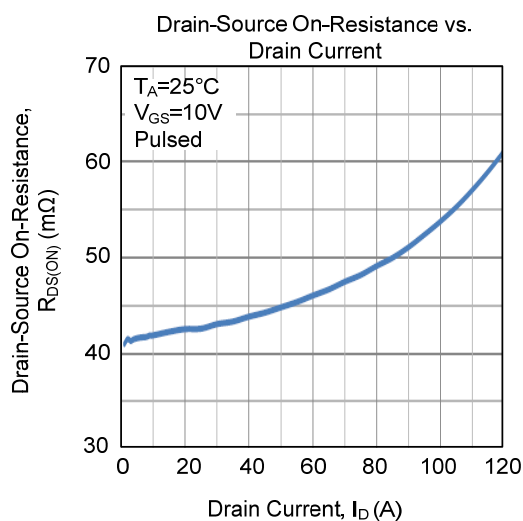
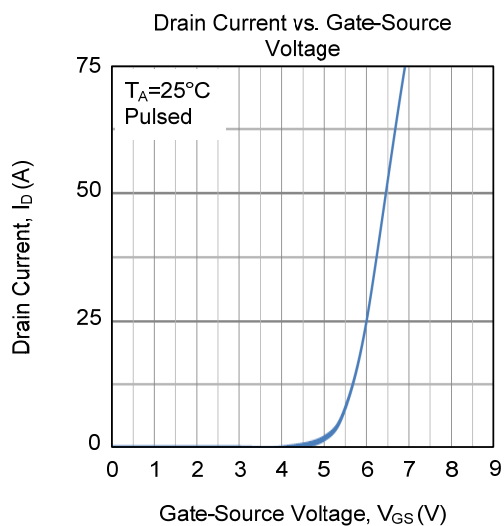
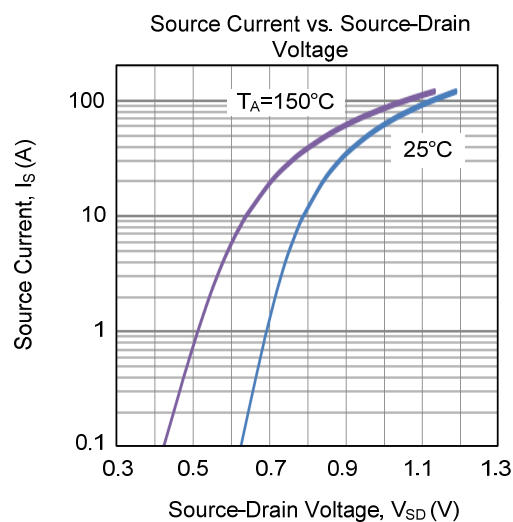
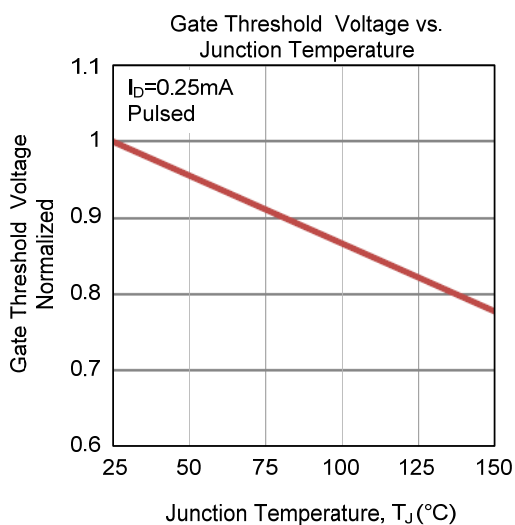


Unclamped Inductive Switching Waveforms

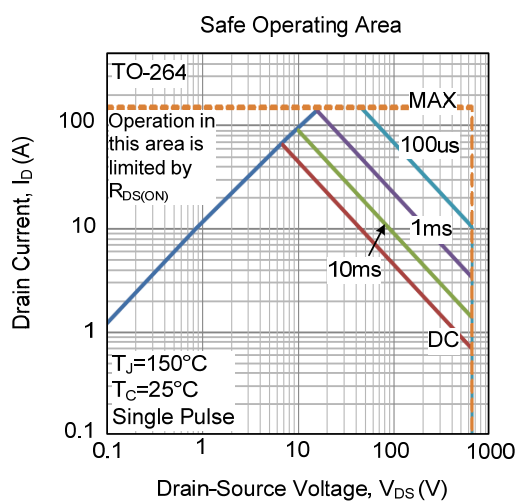
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. UTC reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.