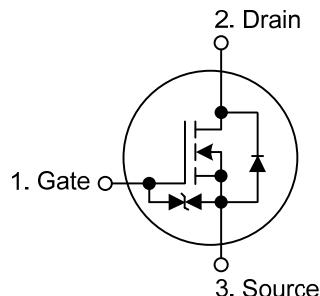


8NK90Z**POWER MOSFET****8.0A, 900V N-CHANNEL
POWER MOSFET****■ DESCRIPTION**

The UTC **8NK90Z** is a silicon N-channel MOSFET, it uses UTC's advanced technology to provide the customers with a minimum on state resistance, high switching speed and low gate charge.

■ FEATURES

- * $R_{DS(ON)} \leq 2.3 \Omega$ @ $V_{GS}=10V$, $I_D=3.1A$
- * High switching speed
- * Low input capacitance
- * With ESD protection

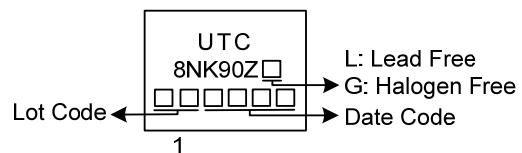
■ SYMBOL**■ ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
8NK90ZL-TF1-T	8NK90ZG-TF1-T	TO-220F1	G	D	S	Tube
8NK90ZL-TF34-T	8NK90ZG-TF34-T	TO-220F4	G	D	S	Tube
8NK90ZL-TQ2-T	8NK90ZG-TQ2-T	TO-263	G	D	S	Tube
8NK90ZL-TQ2-R	8NK90ZG-TQ2-R	TO-263	G	D	S	Tape Reel
8NK90ZL-T47-T	8NK90ZG-T47-T	TO-247	G	D	S	Tube
8NK90ZL-T47S-T	8NK90ZG-T47S-T	TO-247S	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1) T: Tube, R: Tape Reel (2) TF1: TO-220F1, TF34: TO-220F4, TQ2: TO-263, T47: TO-247, T47S: TO-247S (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATING (T_c=25°C unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	900	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	DC	I _D	8	A
	Pulsed (Note 2)	I _{DM}	24	A
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	613	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.4	V/ns
Power Dissipation	TO-220F1/TO-220F4	P _D	41	W
	TO-247/TO-247S		120	W
	TO-263		110	W
Junction Temperature		T _J	+150	°C
Storage Temperature Range		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L=100mH, I_{AS} = 3.5A, V_{DD}=50V, R_G=25Ω, Starting T_J=25°C.

4. I_{SD} ≤ 8.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J=25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F1/TO-220F4	θ _{JA}	62.5	°C/W
	TO-263		50	°C/W
	TO-247/TO-247S		3.05	°C/W
Junction to Case	TO-220F1/TO-220F4	θ _{JC}	1.04	°C/W
	TO-247/TO-247S		1.13	°C/W
	TO-263			

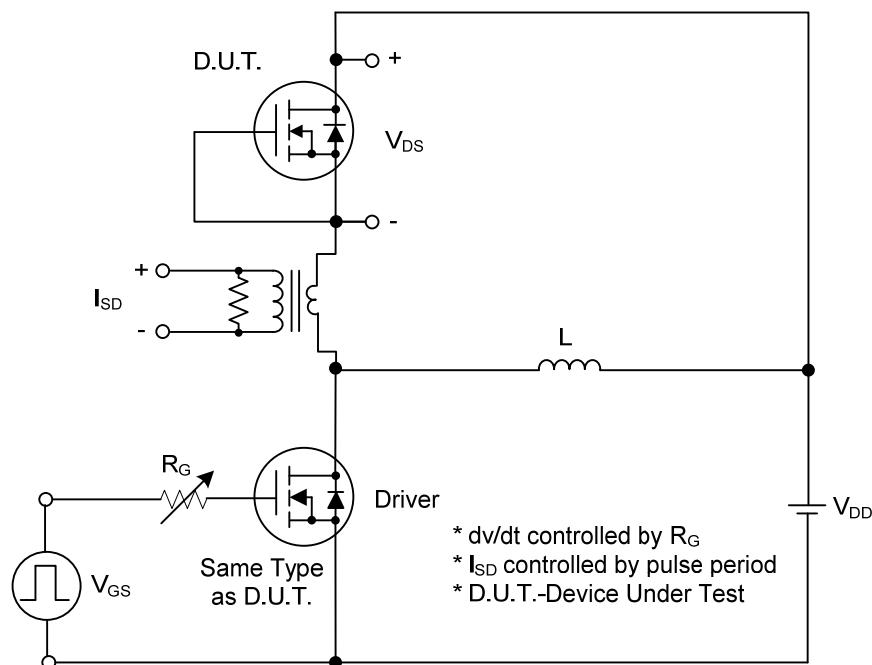
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	900			V
Drain-Source Leakage Current	$I_{\text{DS}}^{\text{SS}}$	$V_{\text{DS}}=900\text{V}, V_{\text{GS}}=0\text{V}$			10	μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}}=+20\text{V}, V_{\text{DS}}=0\text{V}$			+10	μA
	Reverse	$V_{\text{GS}}=-20\text{V}, V_{\text{DS}}=0\text{V}$			-10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	3.0		5.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=3.1\text{A}$		1.8	2.3	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=50\text{V}, f=1.0\text{MHz}$		1367		pF
Output Capacitance	C_{OSS}			79.2		pF
Reverse Transfer Capacitance	C_{RSS}			10		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{\text{DS}}=720\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=8.0\text{A}$ (Note 1, 2)		49		nC
Gate to Source Charge	Q_{GS}			19		nC
Gate to Drain Charge	Q_{GD}			15.3		nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=100\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=8.0\text{A}, R_{\text{G}}=25\Omega$ (Note 1, 2)		29.6		ns
Rise Time	t_R			21		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			91.2		ns
Fall-Time	t_F			45.8		ns
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				8	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				24	A
Diode Forward Voltage	V_{SD}	$I_F=8.0\text{A}, V_{\text{GS}}=0\text{V}$			1.4	V
Reverse Recovery Time	t_{rr}	$I_S=8.0\text{A}, V_{\text{GS}}=0\text{V}, \frac{dI_F}{dt} = 100 \text{ A}/\mu\text{s}$		919		ns
Reverse Recovery Charge (Note 1)	Q_{rr}			7.4		μC

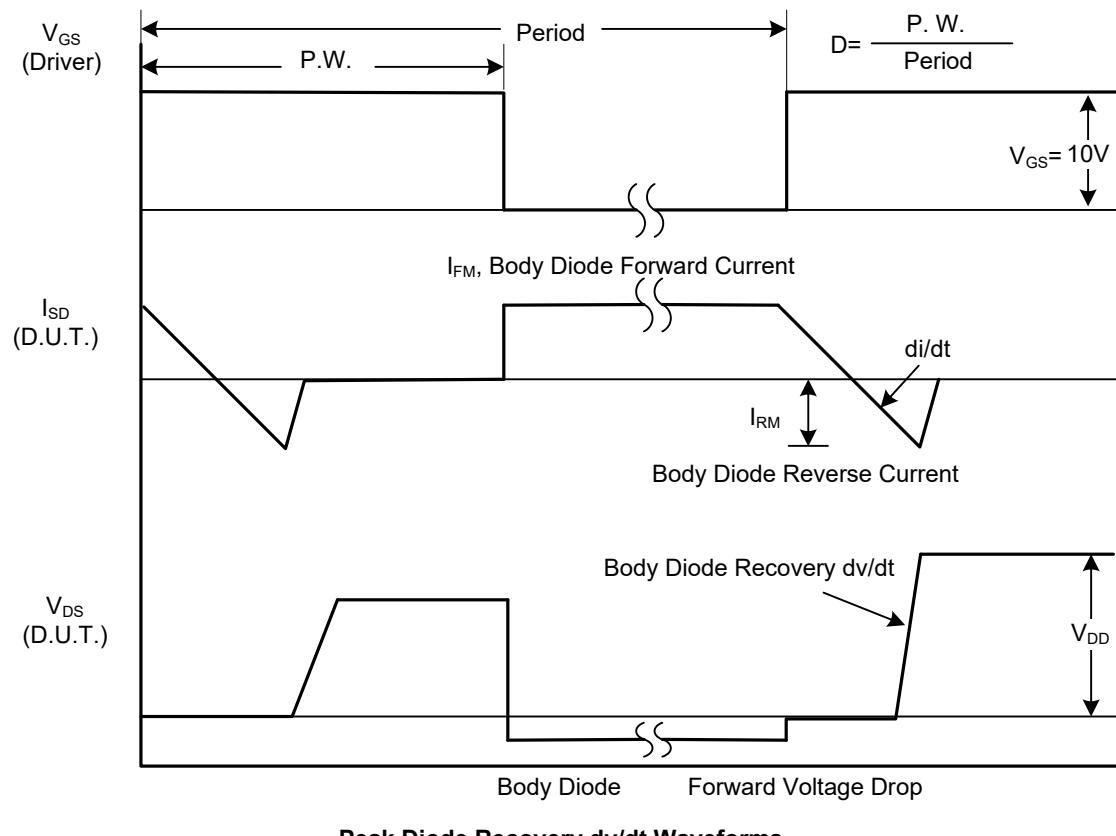
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

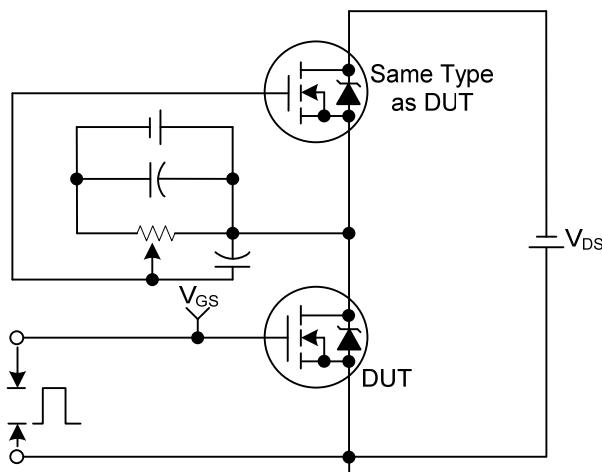


Peak Diode Recovery dv/dt Test Circuit

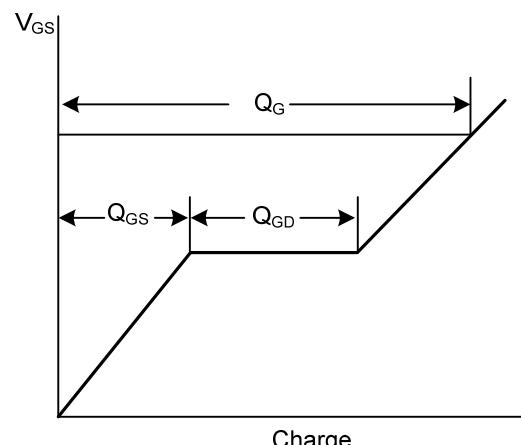


Peak Diode Recovery dv/dt Waveforms

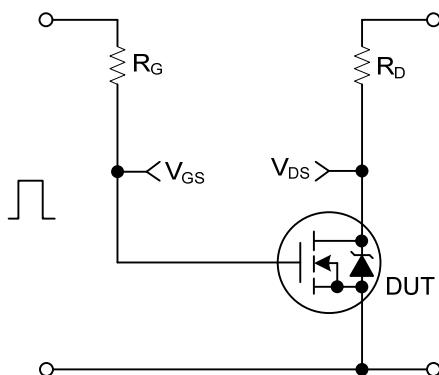
■ TEST CIRCUITS AND WAVEFORMS



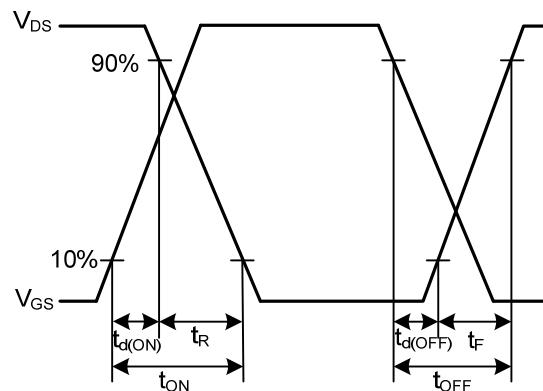
Gate Charge Test Circuit



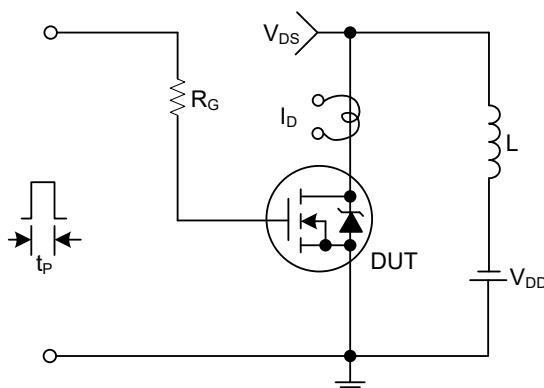
Gate Charge Waveforms



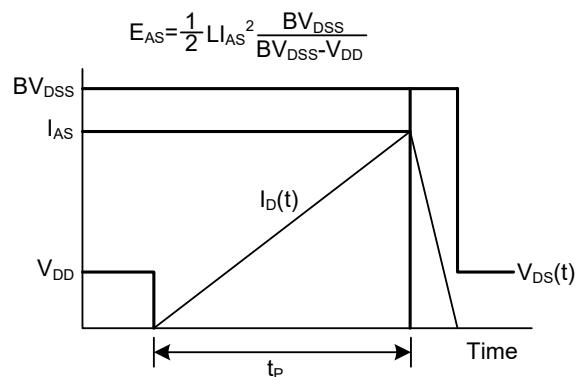
Resistive Switching Test Circuit



Resistive Switching Waveforms

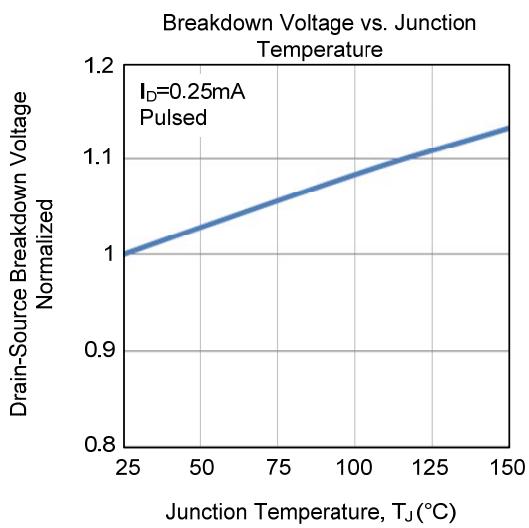
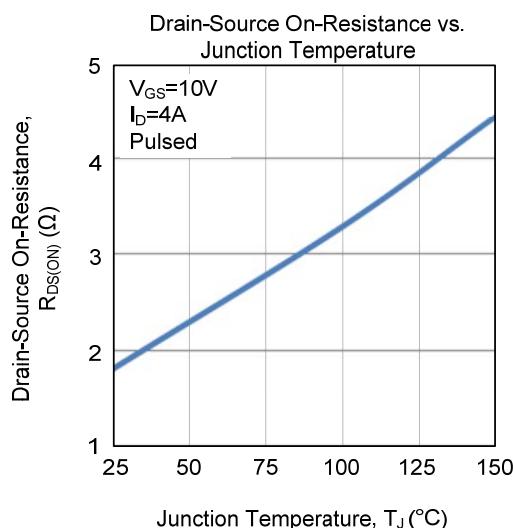
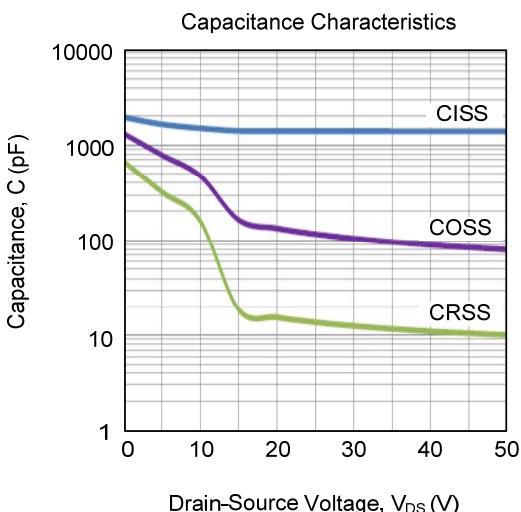
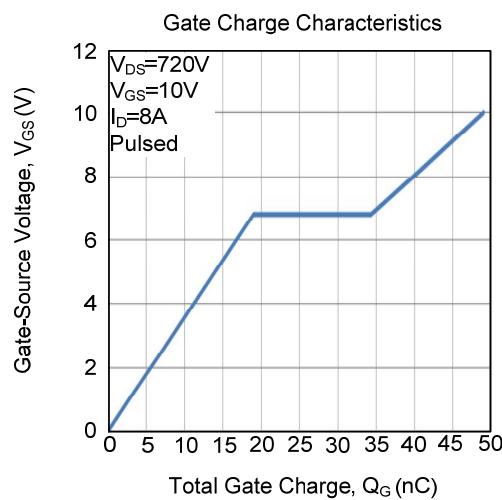
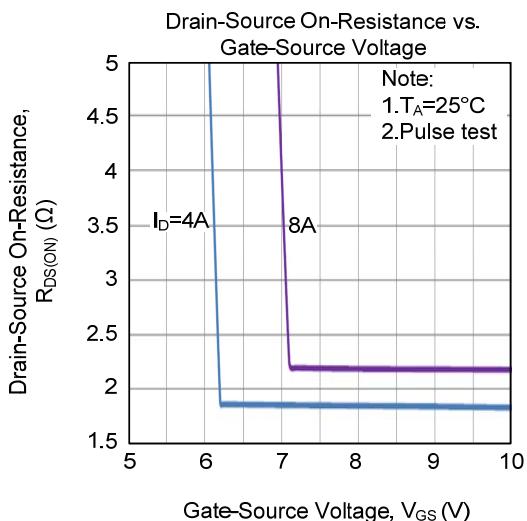
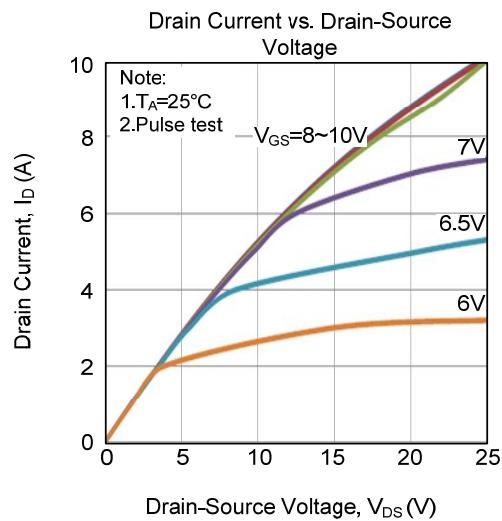


Unclamped Inductive Switching Test Circuit

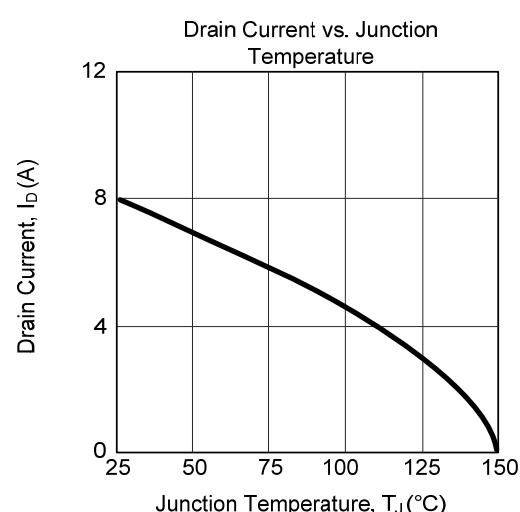
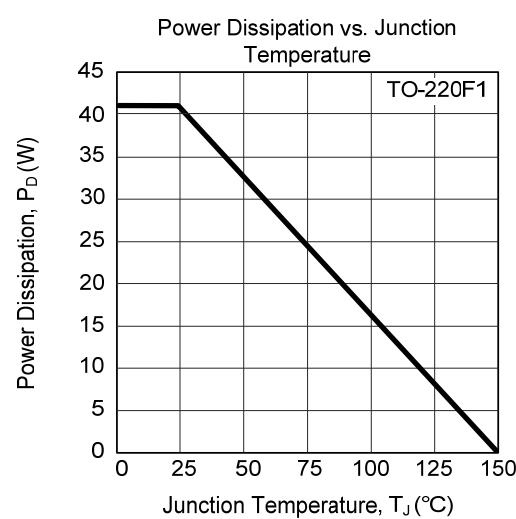
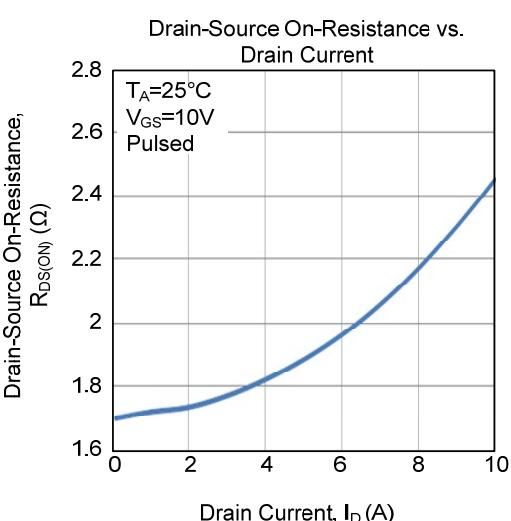
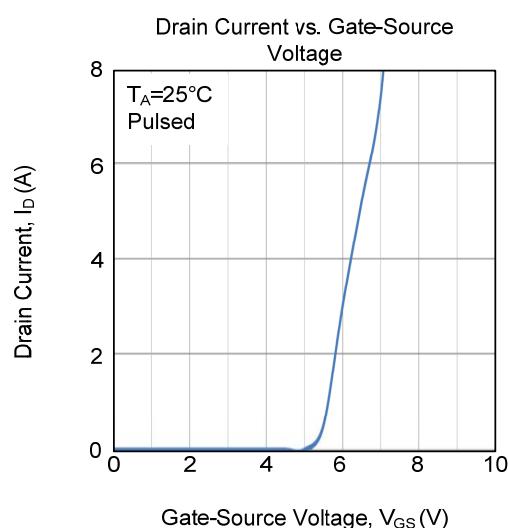
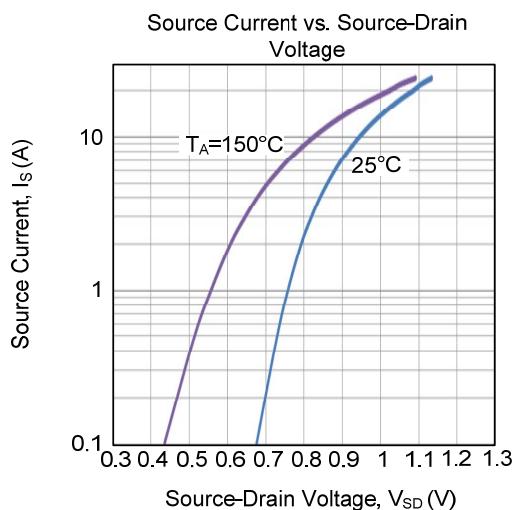
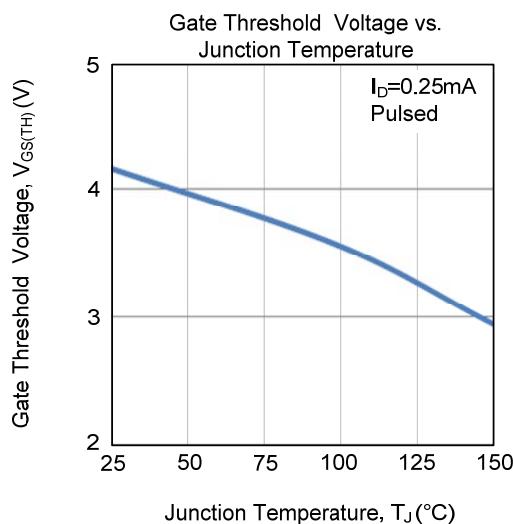


Unclamped Inductive Switching Waveforms

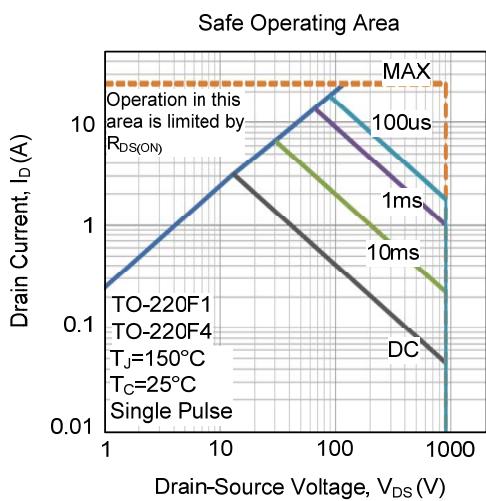
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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