

# BSS138W-Q

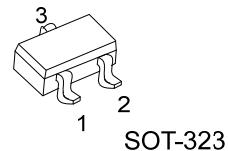
**Power MOSFET**

## N-CHANNEL LOGIC LEVEL ENHANCEMENT MODE

### ■ DESCRIPTION

This device employs advanced MOSFET technology and features low gate charge while maintaining low on-resistance.

Optimized for switching applications, this device improves the overall efficiency of DC/DC converters and allows operation to higher switching frequencies.



### ■ FEATURES

\*  $R_{DS(ON)} \leq 3.5 \Omega$  @  $V_{GS}=10 V$ ,  $I_D=0.22A$

$R_{DS(ON)} \leq 6.0 \Omega$  @  $V_{GS}=4.5V$ ,  $I_D=0.22A$

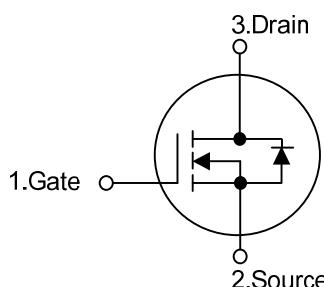
\* Low Capacitance

\* Low Gate Charge

\* Fast Switching Capability

\* Avalanche Energy Specified

### ■ SYMBOL



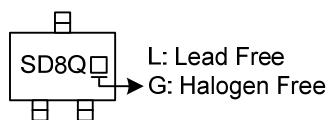
### ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
BSS138WL-AL3-R	BSS138WG-AL3-R	SOT-323	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain

BSS138WG-AL3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AL3: SOT-323 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	$V_{DSS}$	50	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current	DC	0.22	A
	Pulse	0.88	A
Peak Diode Recovery dv/dt	dv/dt	3	V/ns
Power Dissipation( $T_A=25^\circ\text{C}$ )	$P_D$	0.2	W
Junction Temperature	$T_J$	+150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	625	$^\circ\text{C}/\text{W}$

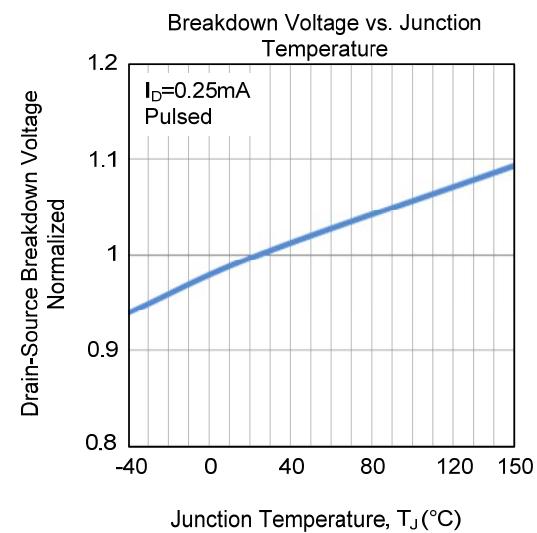
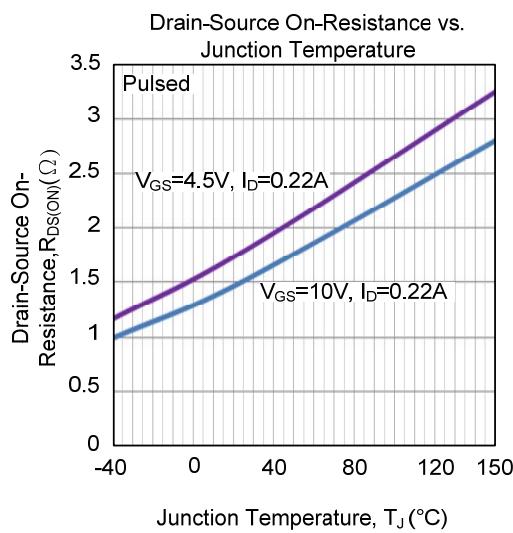
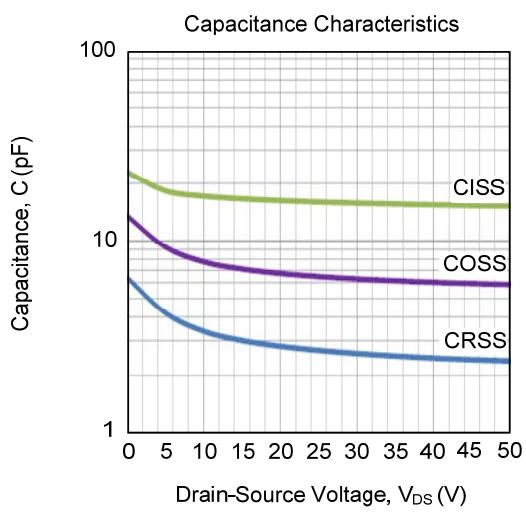
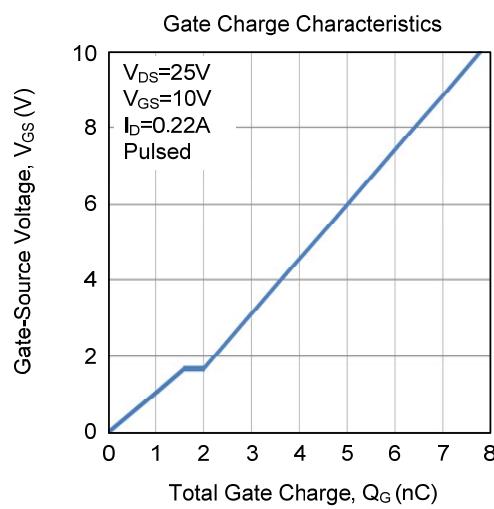
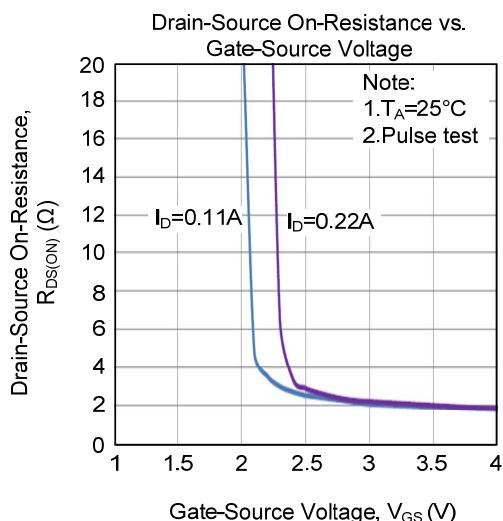
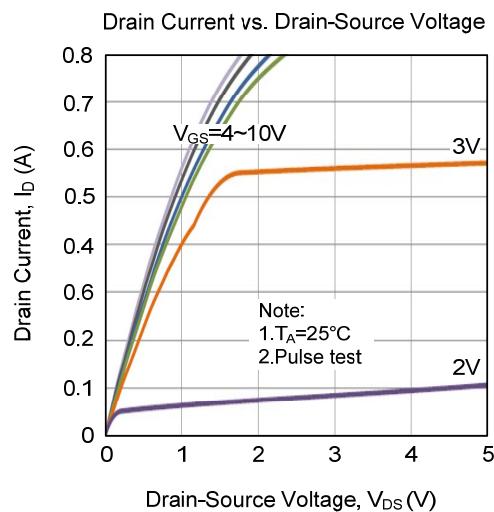
Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$ , unless otherwise specified)

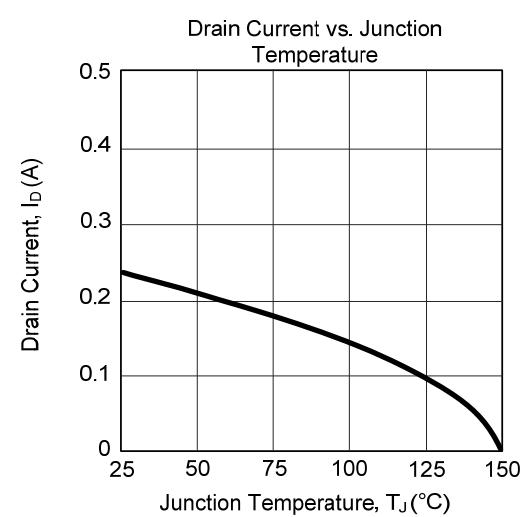
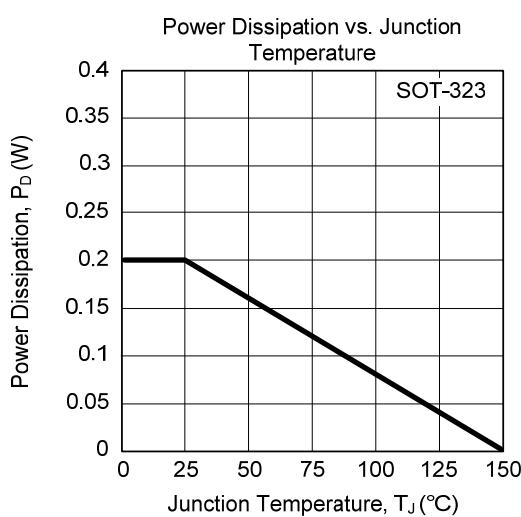
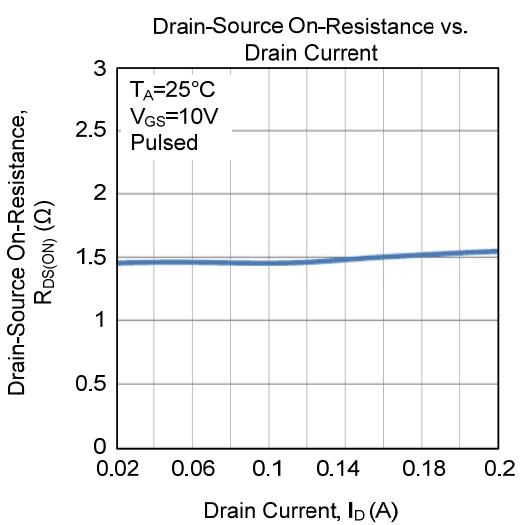
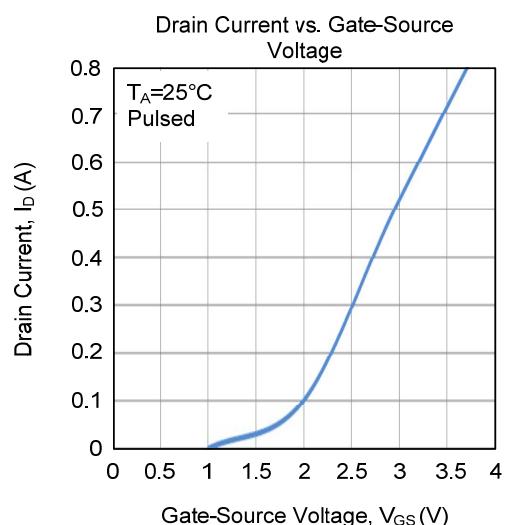
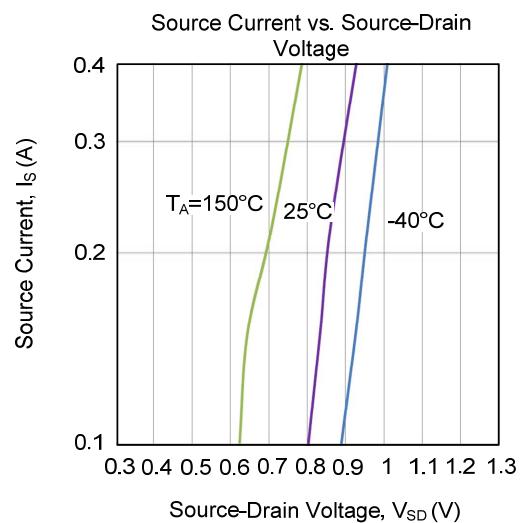
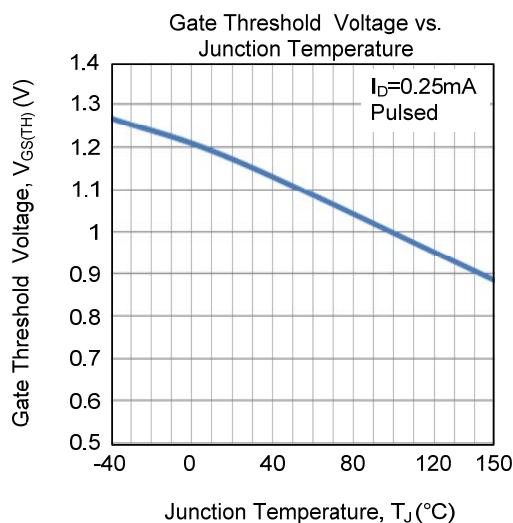
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	50			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=50\text{V}, V_{GS}=0\text{V}$			0.5	$\mu\text{A}$
		$V_{DS}=30\text{V}, V_{GS}=0\text{V}$			0.1	$\mu\text{A}$
Gate-Body Leakage, Forward	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS (Note)</b>						
Gate-Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=1\text{m A}$	0.5		2.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{ V}, I_D=0.22\text{A}$			3.5	$\Omega$
		$V_{GS}=4.5\text{ V}, I_D=0.22\text{A}$			6.0	$\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		17		pF
Output Capacitance	$C_{oss}$			9		pF
Reverse Transfer Capacitance	$C_{rss}$			4		pF
<b>SWITCHING PARAMETERS (Note)</b>						
Total Gate Charge	$Q_G$	$V_{DS}=25\text{V}, V_{GS}=10\text{V}, I_D=0.22\text{A}$		7.8		nC
Gate Source Charge	$Q_{GS}$			1.6		nC
Gate Drain Charge	$Q_{GD}$			0.4		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=30\text{V}, I_D=0.29\text{A}, V_{GS}=10\text{V}, R_G=6\Omega$		1.6		ns
Turn-ON Rise Time	$t_R$			16		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			5		ns
Turn-OFF Fall-Time	$t_F$			14		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Max. Diode Forward Current	$I_S$			0.22		A
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}= 0\text{V}, I_S=0.44\text{A}$ (Note)		0.9	1.4	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0\text{V}, I_S=0.22\text{A}, d/I/dt=100\text{A}/\mu\text{s}$		36		ns
Reverse Recovery Charge	$Q_{rr}$			5.5		nC

Note: Pulse test, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

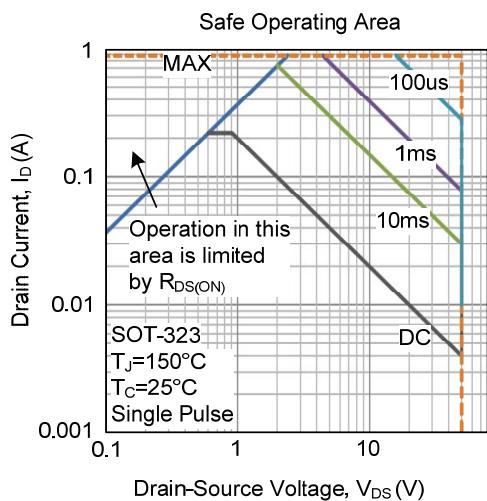
■ TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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