

UNISONIC TECHNOLOGIES CO., LTD

LR9273 Preliminary CMOS IC

SUPER LOW ON RESISTANCE/LOW VOLTAGE 1A LDO REGULATOR

DESCRIPTION

The UTC **LR9273** is a typical LDO (linear regulator) with features of super low dropout, 1A output current capability, and -3mV typical load regulation at 1A.

During operation of the UTC **LR9273**, the dropout voltage is very low and the response of line transient and load transient are very well.

Internally, there're many functions of UTC LR9273 which can be seen in the block figure. There are a voltage reference unit, an error amplifier, resistor-net for voltage setting, a current limit circuit, and a chip enable circuit in each UTC LR9273.

The UTC **LR9273** can be used as an ideal of the power supply for hand-held communication equipment, such as: power source for portable communication equipment, power source for electrical appliances, for example, cameras, VCRs and camcorders and power source for battery-powered equipment.



* Ultra Supply Current: 60μA (Typ.)
 * Standby Mode: 0.1μA (Typ.)
 * Very Low Dropout Voltage: 0.18V (Typ.)

@I_{OUT} =1A, V_{OUT} =2.85V

* Ripple Rejection: 70dB (Typ.)

 $@f=1kHz,V_{OUT}=2.85V$

* Temperature-Drift Coefficient ±100ppm/°C (Typ.)

of Output Voltage:

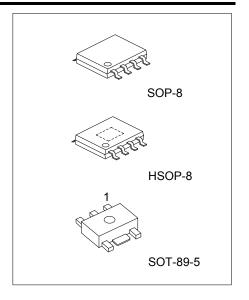
* Well Line Regulation: 0.02%/ V (Typ.)

* Output Voltage Accuracy: ±1.5% (Typ.)

* Internal Fold Back Protection 250mA (Typ.) @ short mode

Circuit:

* C_{IN} = C_{OUT} =4.7 μ F or more (Ceramic capacitors) are recommended to be used with this IC

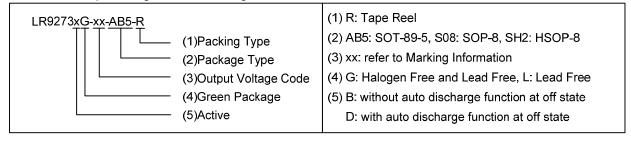


ORDERING INFORMATION

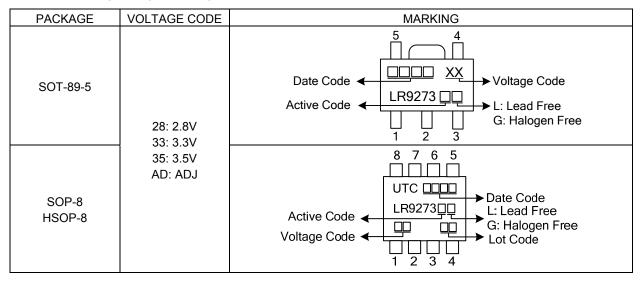
| Ordering | Number | Package | Docking | |
|-------------------------------------|------------------------|----------|-----------|--|
| Lead Free | Lead Free Halogen Free | | Packing | |
| LR9273xL-xx-AB5-R LR9273xG-xx-AB5-R | | SOT-89-5 | Tape Reel | |
| LR9273xL-xx-S08-R | LR9273xG-xx-S08-R | SOP-8 | Tape Reel | |
| LR9273xL-xx-SH2-R | LR9273xG-xx-SH2-R | HSOP-8 | Tape Reel | |

Notes: 1. x: The auto discharge function at off state are options as follows.

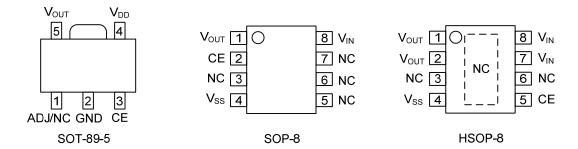
2. xx: Output Voltage, refer to Marking Information.



MARKING INFORMATION



■ PIN CONFIGURATION



■ PIN DESCRIPTION

SOT-89-5

| PIN NO. | PIN NAME | DESCRIPTION |
|---------|-----------------|---|
| 4 | ADJ | ADJUST Pin (For Adjustable Version) |
| I | NC | No Connection (For Fixed Version) |
| 2 | GND | Ground Pin |
| 3 | CE | Chip Enable Pin. Active when this Pin is high |
| 4 | V _{IN} | Input Pin |
| 5 | Vout | Output Pin |

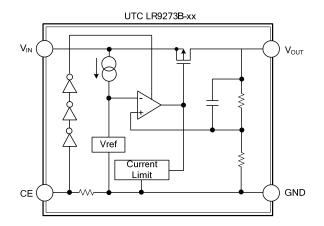
| PIN NO. | | DININIANAE | DECORIDATION | |
|------------|-------------|------------------|---|--|
| SOP-8 | HSOP-8 | PIN NAME | DESCRIPTION | |
| 1 | 1, 2 | V _{OUT} | Output Pin | |
| 2 | 5 | CE | Chip Enable Pin. Active when this Pin is high | |
| 3, 5, 6, 7 | 3, 6 | NC | No connection (Note) | |
| 4 | 4 | V _{SS} | GND Pin | |
| 8 | 7, 8 | V_{IN} | Input Pin | |
| - | Exposed Pad | NC | Thermal pad | |

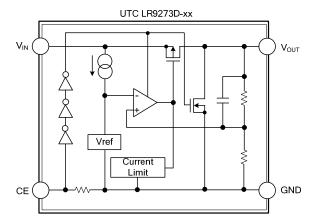
Note: The NC pin is electrically open.

The NC pin can be connected to V_{IN} or V_{SS} .

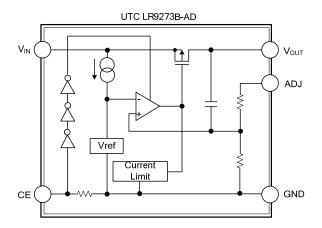
■ BLOCK DIAGRAM

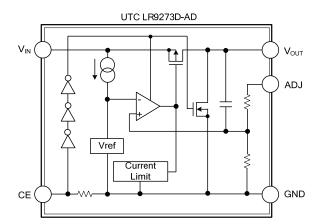
For Fixed Version





For Adjustable Version





■ ABSOLUTE MAXIMUM RATING

| PARAMETE | R | SYMBOL | RATINGS | UNIT |
|------------------------|----------|------------------|----------------------|------|
| Input Voltage | | V_{IN} | 6.5 | V |
| Input Voltage (CE Pin) | | V_{CE} | 6.5 | V |
| Output Voltage | | V _{OUT} | V _{IN} +0.3 | V |
| | SOT-89-5 | | 550 | mW |
| Power Dissipation | SOP-8 | P_{D} | 700 | mW |
| | HSOP-8 | | | mW |
| Operating Temperature | | T _{OPT} | -40 ~ +85 | °C |
| Storage Temperature | | T _{STG} | -55 ~ + 125 | °C |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS

LR9273B/D-xx (Fixed Output Voltage Type)

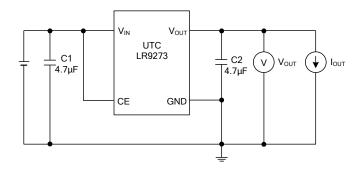
| PARAMETE | | SYMBOL | TEST CONDITIONS | MIN | TYP | MAX | UNIT |
|---|----------------|---|---|-------|------|-------|--------|
| Input Voltage | | V _{IN} | | | | 6.0 | V |
| Supply Current | | I _{SS} | V _{IN} -V _{OUT} =1.0V, V _{CE} =V _{IN} , I _{OUT} =0A | | 60 | 100 | μA |
| Standby Current | | Istandby | V _{IN} =6.0V, V _{CE} =0V | | 0.1 | 1.0 | μΑ |
| Output Voltage | | V _{OUT} | V _{IN} -V _{OUT} =1.0V, V _{OUT} >1.5V | ×0.98 | | ×1.02 | V |
| Output Voltage | | | I _{OUT} =100mA V _{OUT} ≤1.5V | -30 | | +30 | mV |
| | | ΔV_{OUT} | V_{IN} - V_{OUT} =0.3V, 1mA≤ I_{OUT} ≤300mA, If V_{OUT} ≤1.1V, then V_{IN} =1.4V | -15 | -2 | 15 | mV |
| Load Regulation | | Δl _{OUT} | V_{IN} - V_{OUT} =0.3V, 1mA≤ I_{OUT} ≤1A, If V_{OUT} ≤1.1V, then V_{IN} =1.7V | | -3 | | mV |
| Dropout Voltage | | \ / | I _{OUT} =300mA, 2.8≤V _{OUT} | | 0.15 | | V |
| (T _{OPT} =25°C) | | V_{DIF} | I _{OUT} =1A, 2.8≤V _{OUT} | | 0.45 | | V |
| Line Regulation | | $\frac{\Delta V_{OUT}}{\Delta V_{IN}}$ | I _{OUT} =100mA, V _{OUT} +0.5V≤ _{VIN} ≤6.0V, If V _{OUT} ≤0.9V, 1.4V≤VIN≤6.0V | | 0.05 | 0.20 | %/V |
| | | | f=1kHz (V _{OUT} ≤4.0V) | | 70 | | dB |
| Ripple Rejection | | RR | f=1kHz (V_{OUT} >4.0V) Ripple 0.5Vp-p, V_{IN} - V_{OUT} =1.0V, I_{OUT} =100mA, If V_{OUT} ≤1.2V, V_{IN} - V_{OUT} =1.5V, I_{OUT} =100mA | | 60 | | dB |
| Output Voltage Temperature Coefficient | | $\frac{\Delta V_{OUT}}{\Delta T_{OPT}}$ | I _{OUT} =100mA, -40°C≤T _{OPT} ≤85°C | | ±100 | | ppm/°C |
| Output Current | Output Current | | V _{IN} -V _{OUT} =1.0V | 1 | | | Α |
| Short Current Limit | | I _{SC} | V _{OUT} =0V | | 250 | | mA |
| Pull-Down Resistance for CE PIn | | R_{PD} | | | 5.0 | | ΜΩ |
| CE Input Voltage | High | V _{CEH} | | 1.2 | | 6.0 | V |
| | Low | V_{CEL} | | 0 | | 0.4 | V |
| Thermal Shutdown Detector Threshold Temperature | | T_{TSD} | Junction temperature | | 150 | | °C |

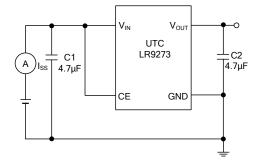
■ ELECTRICAL CHARACTERISTICS

LR9273B/D-xx (Adjustable Output Voltage Type)

| LK92/3D/D-XX (Au | justable v | Julpul voil | age Type) | | | | | |
|---|------------|---|---|-----------------------------|-------|-------|----------|--------|
| PARAMETE | R | SYMBOL | TEST CONDITIONS | | | TYP | MAX | UNIT |
| Input Voltage | | V_{IN} | | | 1.4 | | 6.0 | V |
| Supply Current | | I _{SS} | V _{OUT} =V _{ADJ} , V _{IN} =2.0, V _{CE} =V _{IN} | | | 60 | 100 | μΑ |
| Standby Current | | Istandby | V _{IN} =6.0V, V _{CE} =0V | | | 0.1 | 1.0 | μΑ |
| Reference Voltage Adjustable Voltage | | V _{OUT} | V _{OUT} =V _{ADJ} , V _{IN} =2.0 | OV, I _{OUT} =100mA | 1.176 | 1.200 | 1.224 | ٧ |
| Output Voltage Rar | nge | RV _{OUT} | | | 1.0 | | V_{IN} | V |
| Land Danidation | | ΔV_{OUT} | V_{IN} =1.4V, 1mA \leq I _{OUT} \leq 300mA V_{IN} =1.7V, 1mA \leq I _{OUT} \leq 1A | | -15 | -2 | 15 | mV |
| Load Regulation | | ΔI_{OUT} | | | | -3 | | mV |
| D | | | ., ., | I _{OUT} =300mA | | 0.70 | | V |
| Dropout Voltage | | V_{DIF} | V _{OUT} =V _{ADJ} | I _{OUT} =1A | | 0.56 | | V |
| Line Regulation | | $\frac{\Delta V_{OUT}}{\Delta V_{IN}}$ | V _{OUT} =V _{ADJ} , I _{OUT} =100mA, 1.5V≤V _{IN} ≤6.0V | | | 0.05 | 0.20 | %/V |
| Ripple Rejection | | RR | f=1kHz Ripple 0.5Vp-p, V _{OUT} =V _{ADJ} , V _{IN} =2.5V, I _{OUT} =100mA | | | 70 | | dB |
| Output Voltage Temperature Coefficient | | $\frac{\Delta V_{OUT}}{\Delta T_{OPT}}$ | I _{OUT} =100mA, -40°C≤T _{OPT} ≤85°C | | | ±100 | | ppm/°C |
| Output Current | | I _{LIM} | V _{OUT} =V _{ADJ} , V _{IN} =2.0 | | 1 | | | Α |
| Short Current Limit | | I _{SC} | V _{OUT} =V _{ADJ} =0V | | | 250 | | mA |
| Pull-Down Resistance for CE PIn | | R _{PD} | | | | 5.0 | | МΩ |
| CE Input Voltage | High | V _{CEH} | | | 1.2 | | 6.0 | V |
| | Low | V _{CEL} | | | 0 | | 0.4 | V |
| Thermal Shutdown Detector Threshold Temperature | | T _{TSD} | Junction temperature | | | 150 | | °C |

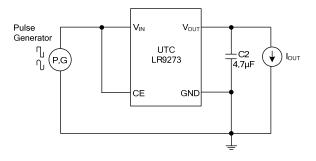
■ TEST CIRCUIT



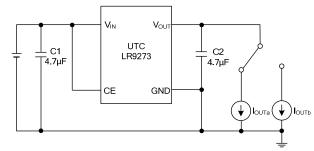


Basic Test Circuit

Test Circuit for Supply Current

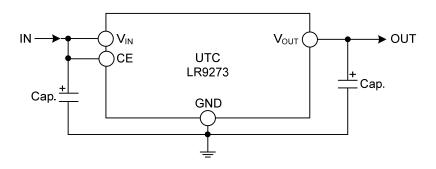


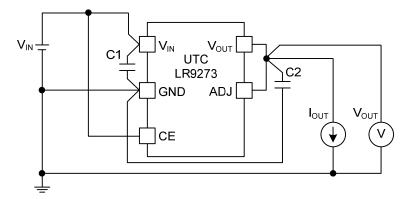




Test Circuit for Load Transient Response

■ TYPICAL APPLICATION CIRCUIT





Example of the Typical Application of UTC LR9273 (Fixed Output Type)

Phase Compensation

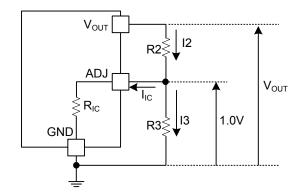
In these ICs, phase compensation is made with the output capacitor for securing stable operation even if the load current is varied. For this purpose, use as much as a capacitor as C2. Recommendation value is as follows:

Mounting on PCB

Make V_{DD} and GND lines sufficient. If their impedance is high, a current flows, the noise picked up or unstable operation may result. Further use a 4.7 μ F or more value capacitor between V_{DD} pin and GND pin as close as possible.

Set an Output capacitor between V_{OUT} pin and GND pin for phase compensation as close as possible.

Technical Notes on Output Voltage Setting of Adjustable Output type



■ TYPICAL APPLICATION CIRCUIT(Cont.)

The Output Voltage may be adjustable for any output voltage between its 1.0V reference and its V_{DD} setting level. An external pair of resistors is required, as shown above.

The complete equation for the output voltage is described step by step as follows;

Thus,

$$I2=I_{IC}+1.0/R3$$
(3)

Therefore,

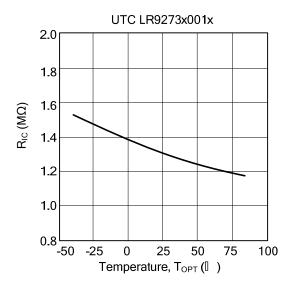
Put Equation (3) into Equation (4), then

$$V_{OUT}$$
=1.0+R2(I_{IC} +1.0/R3)
=1.0(1+R2/R3)+R2× I_{IC}(5)

In 2nd term, or R2×IIC will produce an error in V_{OUT} .

In Equation (5),

For better accuracy, choosing R2(<<RIC) reduces this error.



The graph is a typical characteristic, please evaluate the circuit with an actual condition.

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