TGBR5S100 Preliminary DIODE

# TRENCH MOS SCHOTTKY BARRIER RECTIFIER

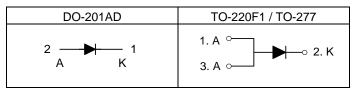
### DESCRIPTION

The UTC **TGBR5S100** is a trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

### ■ FEATURES

- \* Super low forward voltage drop
- \* High switching speed



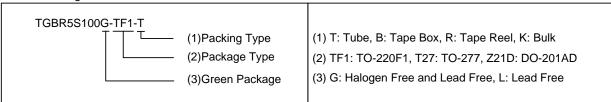


# DO-201AD TO-277 TO-220F1

### ORDERING INFORMATION

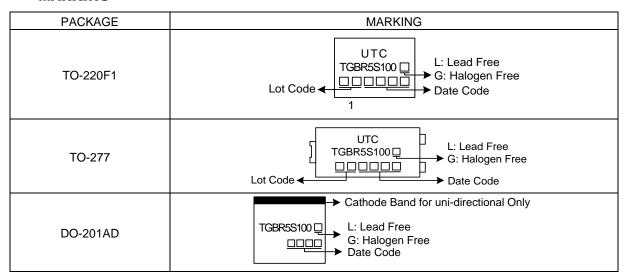
Ordering Number		Doolsone	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
TGBR5S100L-TF1-T	TGBR5S100G-TF1-T	TO-220F1	Α	K	K A Tube		
TGBR5S100L-T27-T	TGBR5S100G-T27-T	TO-277	Α	K	Α	Tape Reel	
TGBR5S100L-Z21D-B	TGBR5S100G-Z21D-B	B DO-201AD K A - Tap		Tape Box			
TGBR5S100L-Z21D-R	TGBR5S100G-Z21D-R	DO-201AD K A - T		Tape Reel			
TGBR5S100L-Z21D-K	TGBR5S100G-Z21D-K	DO-201AD	K	Α	-	Bulk	

Note: Pin Assignment: A: Anode K: Cathode



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### MARKING



## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	$V_{RM}$	100	V
Working Peak Reverse Voltage	$V_{RWM}$	100	V
Peak Repetitive Reverse Voltage	$V_{RRM}$	100	V
Average Rectified Output Current	lo	5	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	150	А
Operating Junction Temperature	$T_J$	-65 ~ <b>+</b> 150	°C
Storage Temperature	T <sub>STG</sub>	-65 ~ <b>+</b> 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# ■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER		SYMBOL	RATINGS	UNIT
Typical Thermal Resistance	TO-220F1		4	°C/W
	TO-277	$\theta_{JC}$	4 (Note)	°C/W
	DO-201AD		22	°C/W

Note: FR-4 PCB, 2 oz Copper. Minimum recommended pad layout.

## ■ ELECTRICAL CHARACTERISTICS (PER LEG) (T<sub>A</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage	$V_{(BR)R}$	$I_R=0.5mA$				V
	$V_{FM}$	I <sub>F</sub> =1A, T <sub>J</sub> =25°C		0.41		V
		I <sub>F</sub> =1A, T <sub>J</sub> =125°C		0.31		V
Farmed Valle of Door		I <sub>F</sub> =3A, T <sub>J</sub> =25°C		0.50		V
Forward Voltage Drop		I <sub>F</sub> =3A, T <sub>J</sub> =125°C		0.45		V
		I <sub>F</sub> =5A, T <sub>J</sub> =25°C		0.58	0.60	V
		I <sub>F</sub> =5A, T <sub>J</sub> =125°C		0.51	0.55	V
Lookena Current	I <sub>RM</sub>	V <sub>R</sub> =100V, T <sub>J</sub> =25°C		15	300	μA
Leakage Current		V <sub>R</sub> =100V, T <sub>J</sub> =125°C		8	30	mA

Note: Pulse Test: Pulse width  $\leq 300 \mu s$ , Duty cycle  $\leq 2\%$ .

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