



UNISONIC TECHNOLOGIES CO., LTD

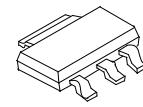
UF4N20VZ

Power MOSFET

4.0A, 200V N-CHANNEL
POWER MOSFET

■ DESCRIPTION

The UTC **UF4N20VZ** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

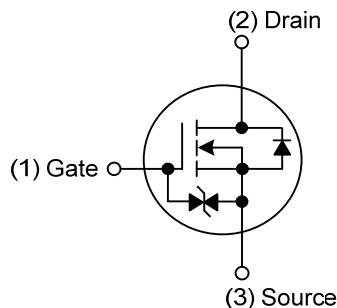


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SOT-223

■ FEATURES

- * $R_{DS(ON)} \leq 1.5 \Omega$ @ $V_{GS}=10V$, $I_D=4.0A$
- $R_{DS(ON)} \leq 1.8 \Omega$ @ $V_{GS}=4.5V$, $I_D=4.0A$
- * High switching speed
- * 100% avalanche tested

■ SYMBOL



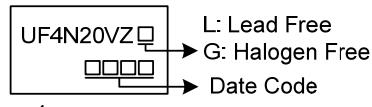
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF4N20VZL-AA3-R	UF4N20VZG-AA3-R	SOT-223	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UF4N20VZG-AA3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) R: Tape Reel (2) AA3: SOT-223 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



1

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	200	V
Gate-Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current	I_D	4	A
Avalanche Energy Single Pulsed	E_{AS}	72	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	3.4	V/ns
Power Dissipation	P_D	2	W
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 30\text{mH}$, $I_{AS} = 2.2\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25 \Omega$ Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 4.0\text{A}$, $dI/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	140	$^\circ\text{C/W}$
Junction to Case	θ_{JC}	62.5	$^\circ\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

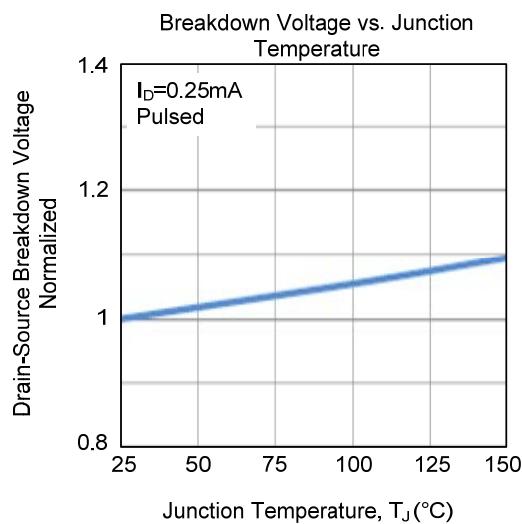
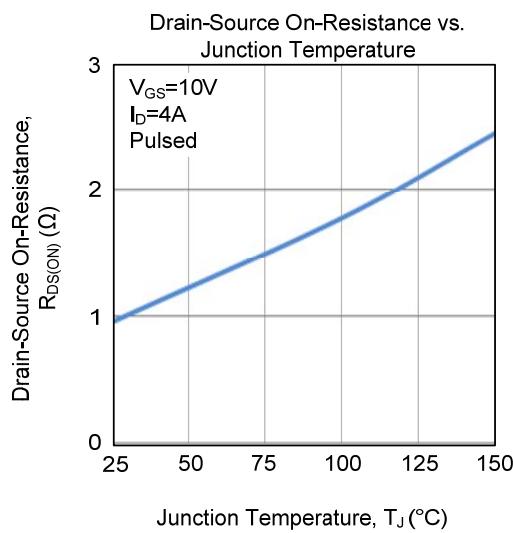
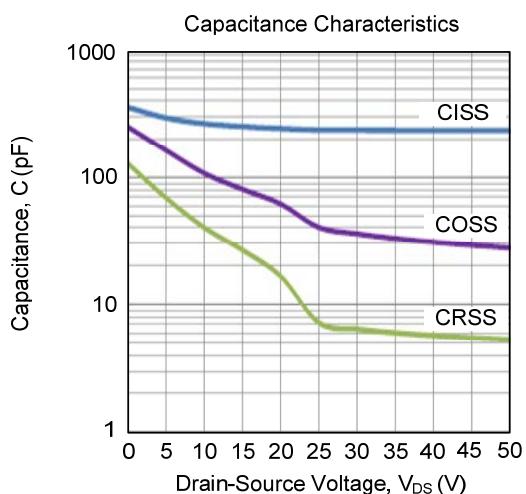
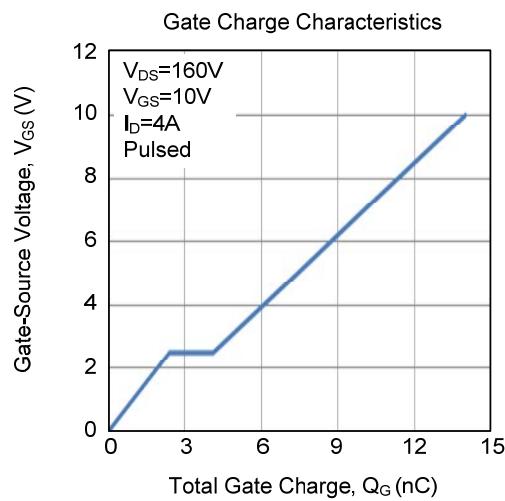
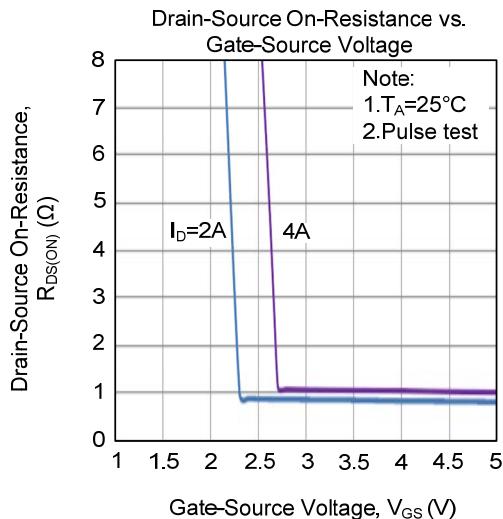
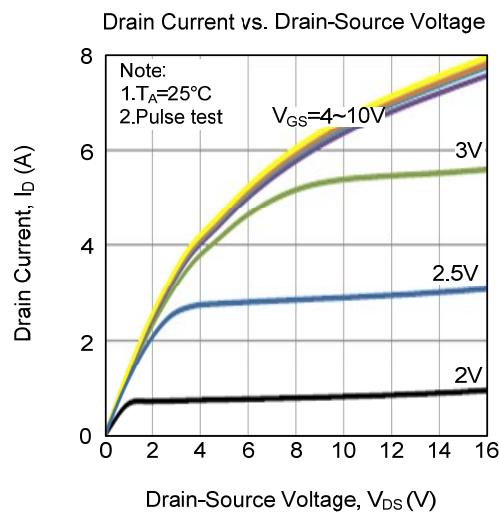
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	200			V
Drain-Source Leakage Current	$I_{\text{DS}}^{\text{SS}}$	$V_{DS}=200\text{V}, V_{GS}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{GS}=+12\text{V}, V_{DS}=0\text{V}$		10		μA
	Reverse	$V_{GS}=-12\text{V}, V_{DS}=0\text{V}$		-10		μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0		3.0	V
Static Drain-Source On-State Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=4.0\text{A}$		1.1	1.5	Ω
		$V_{GS}=4.5\text{V}, I_D=4.0\text{A}$			1.8	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1\text{MHz}$		240		pF
Output Capacitance	C_{oss}			40		pF
Reverse Transfer Capacitance	C_{rss}			7.2		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{DD}=160\text{V}, V_{GS}=10\text{V}, I_D=4.0\text{A}$ (Note 1, 2)		14		nC
Gate to Source Charge	Q_{GS}			2.4		nC
Gate to Drain Charge	Q_{GD}			1.7		nC
Turn-ON Delay Time	$t_{D(\text{ON})}$	$V_{DD}=100\text{V}, V_{GS}=10\text{V}, I_D=4.0\text{A},$ $R_G=25\Omega$ (Note 1, 2)		1.6		ns
Rise Time	t_R			16		ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			38		ns
Fall-Time	t_F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				4	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				8	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=4.0\text{A}, V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=4.0\text{A}, V_{GS}=0\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$		150		nS
Body Diode Reverse Recovery Charge	Q_{rr}			495		nC

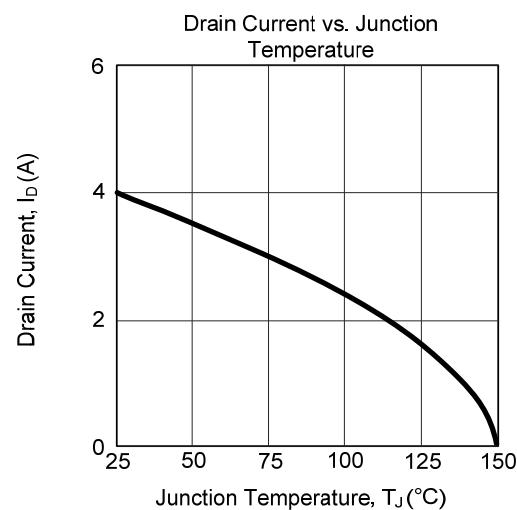
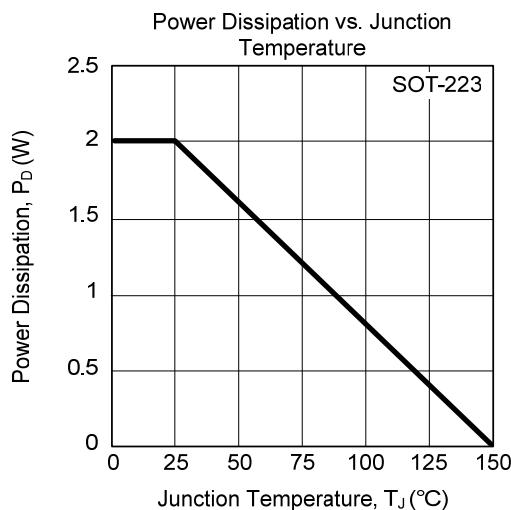
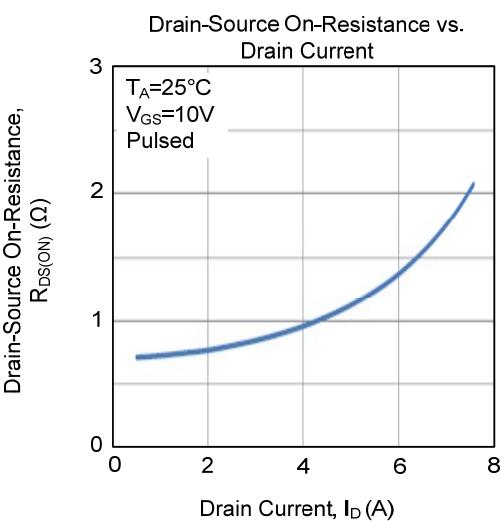
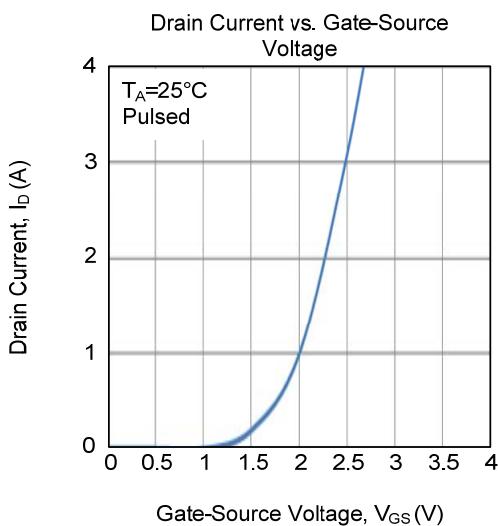
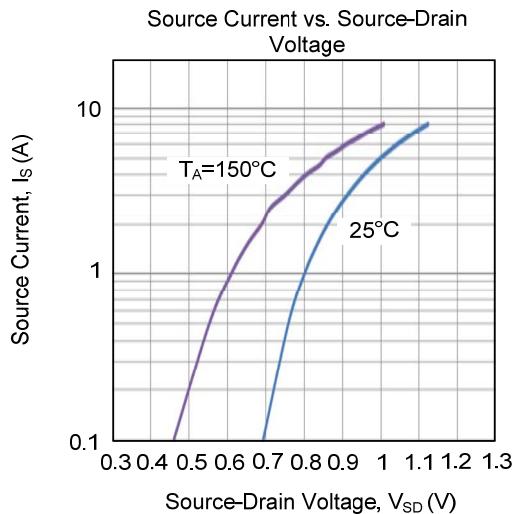
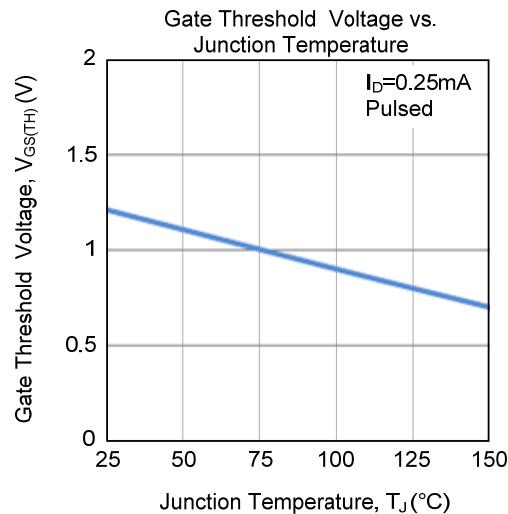
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

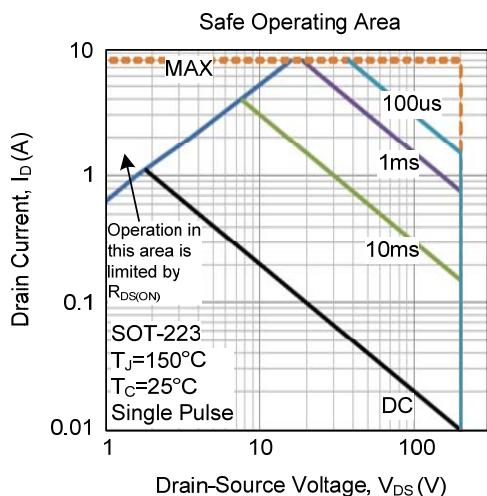
2. Essentially independent of operating temperature.

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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