



UF53N20

Preliminary

Power MOSFET

53A, 200V N-CHANNEL POWER MOSFET

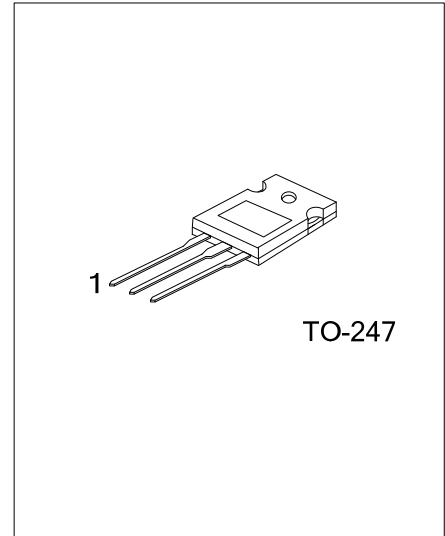
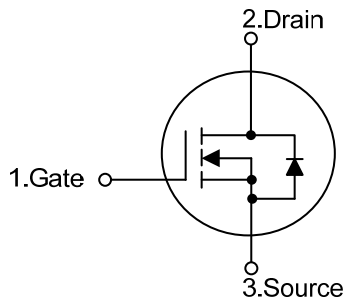
DESCRIPTION

The UTC **UF53N20** provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)} \leq 50 \text{ m}\Omega$ @ $V_{GS}=10\text{V}$, $I_D=26\text{A}$
- * Low Reverse Transfer Capacitance
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL



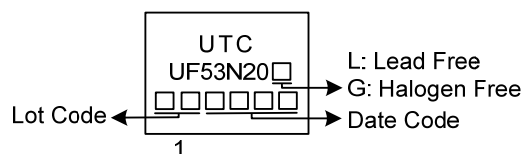
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF53N20L-T47-T	UF53N20G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UF53N20G-T47-T (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) T47: TO-247 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	200	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	53	A
	Pulsed (Note 2)	I_{DM}	106	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	812	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	5.3	V/ns
Power Dissipation		P_D	270	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress values only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 1\text{mH}$, $I_{AS} = 40.3\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

4. $I_{SD} \leq 30\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	50	$^{\circ}\text{C}/\text{W}$
Junction to Case	θ_{JC}	0.46	$^{\circ}\text{C}/\text{W}$

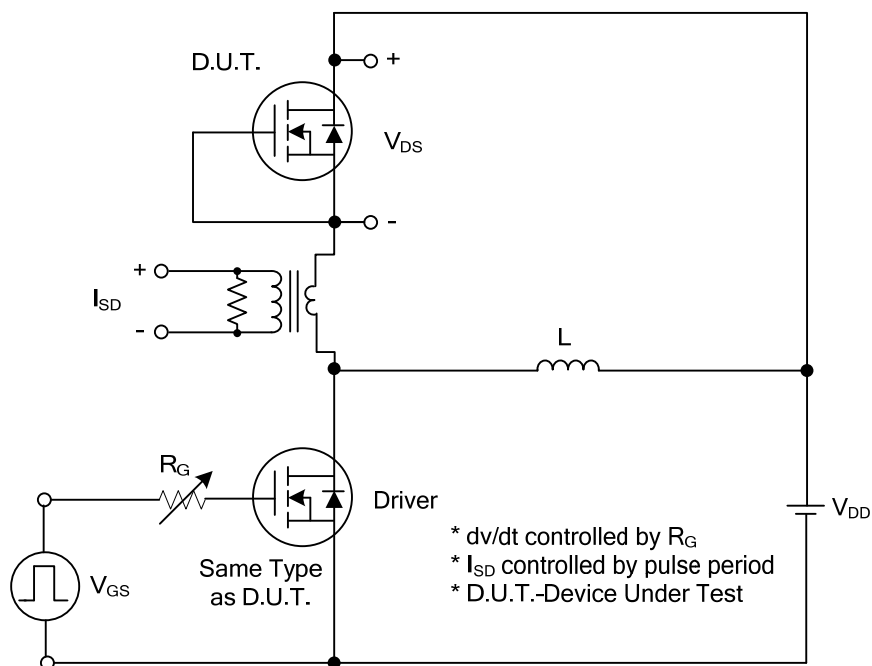
■ ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =200μA	200			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =200V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =200μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =26A			50	mΩ
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz		4393		pF
Output Capacitance	C _{OSS}			548		pF
Reverse Transfer Capacitance	C _{RSS}			20		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge (Note 1)	Q _G	V _{DS} =160V, V _{GS} =10V, I _D =53A (Note 1, 2)		93		nC
Gate-Source Charge	Q _{GS}			27		nC
Gate-Drain Charge	Q _{GD}			33		nC
Turn-On Delay Time (Note 1)	t _{D(ON)}	V _{DD} =100V, V _{GS} =10V, I _D =53A, R _G =3Ω (Note 1, 2)		21		ns
Turn-On Rise Time	t _R			19		ns
Turn-Off Delay Time	t _{D(OFF)}			45		ns
Turn-Off Fall Time	t _F			18		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				53	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				106	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =53A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time (Note 1)	t _{rr}	I _S =30A, V _{GS} =0V,		204		nS
Body Diode Reverse Recovery Charge	Q _{rr}	di _F /dt=100A/μs		1014		nC

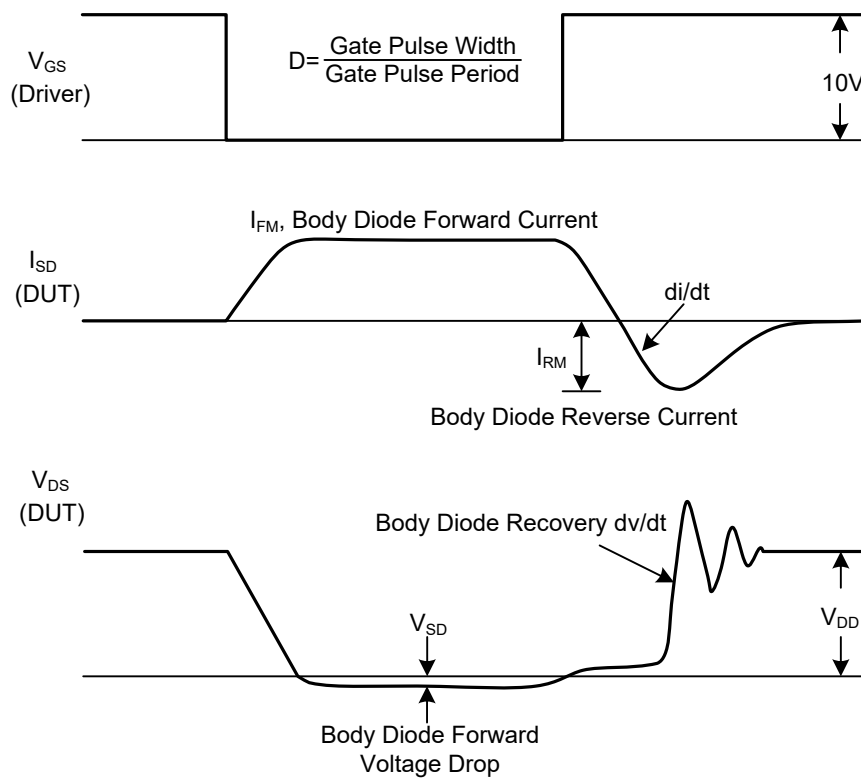
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

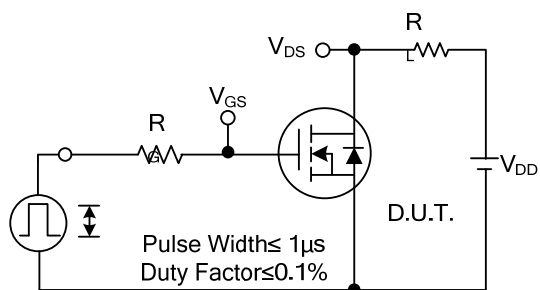


Peak Diode Recovery dv/dt Test Circuit

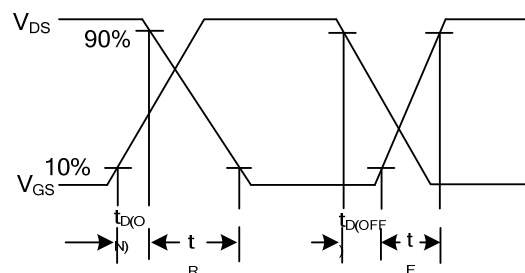


Peak Diode Recovery dv/dt Waveforms

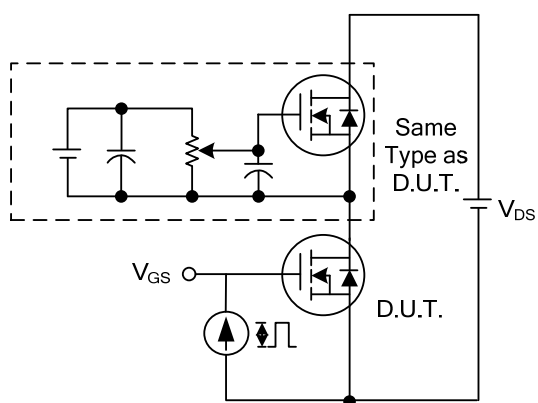
■ TEST CIRCUITS AND WAVEFORMS



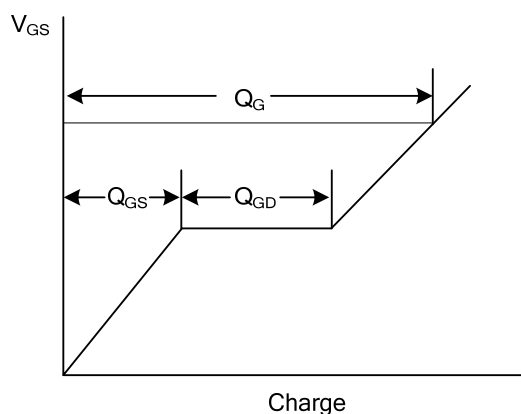
Switching Test Circuit



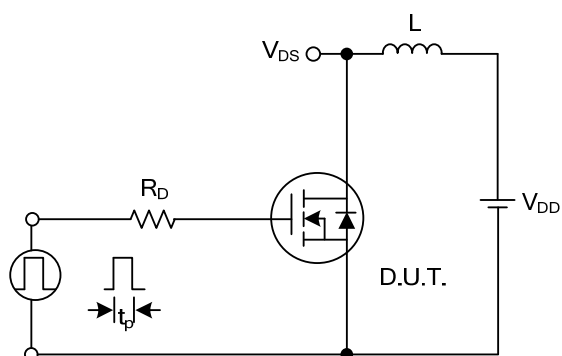
Switching Waveforms



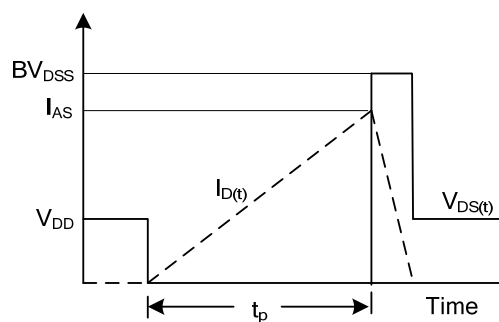
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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