

# UTC UNISONIC TECHNOLOGIES CO., LTD

# **UT06N03M**

# **Preliminary**

## **POWER MOSFET**

# 0.6A, 30V N-CHANNEL POWER MOSFET

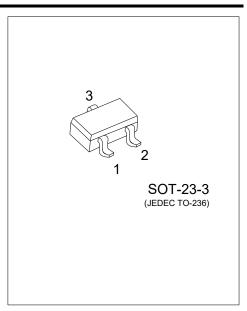
#### **DESCRIPTION**

The UTC UT06N03M is a N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with perfect R<sub>DS(ON)</sub> and high switching speed.

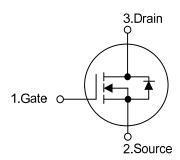
The UTC UT06N03M is suitable for all commercial-industrial applications at power dissipation levels to approximately 50 watts, etc.

#### **FEATURES**

- \*  $R_{DS(ON)} \le 580 \text{ m}\Omega$  @  $V_{GS}=10V$ ,  $I_D=300\text{m}A$
- \*  $R_{DS(ON)} \le 900 \text{ m}\Omega$  @  $V_{GS}=4.5V$ ,  $I_{D}=300\text{m}A$
- \* High Switching Speed



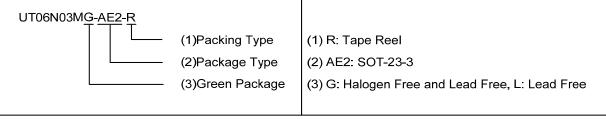
#### **SYMBOL**



#### ORDERING INFORMATION

Ordering Number		Daakana	Pin Assignment			Da alsima
Lead Free	Halogen Free	Package	1	2	3	Packing
UT06N03ML-AE2-R	UT06N03MG-AE2-R	SOT-23-3	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain



### **MARKING**



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#### ■ ABSOLUTE MAXIMUM RATINGS (Tc=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current	Continuous	I <sub>D</sub>	0.6	Α
	Pulsed	I <sub>DM</sub>	1.2	Α
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2	V/ns
Power Dissipation		P <sub>D</sub>	0.3	W
Junction Temperature		TJ	+150	°C
Storage Temperature Range		T <sub>STG</sub>	55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. I<sub>SD</sub>  $\leq$  0.6A, di/dt  $\leq$  200A/ $\mu$ s, V<sub>DD</sub>  $\leq$  BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C

#### THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θЈА	416	°C/W	
Junction to Case	θις	250	°C/W	

Note: Device mounted on FR4 substrate Pc board, 2oz copper, with 1inch square copper plate.

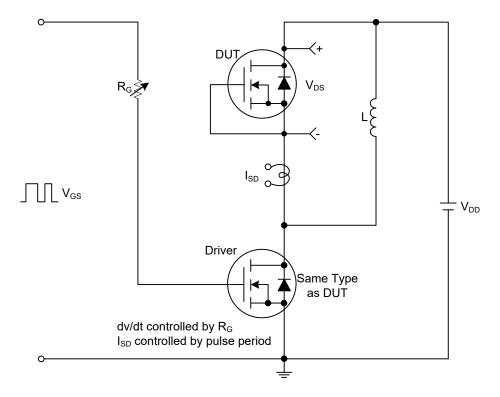
#### ELECTRICAL CHARACTER ISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS					•		
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250µA, V <sub>GS</sub> =0V	30			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V			1	μΑ
Gate-Source Leakage Current	Forward		V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
	Reverse	Igss	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1.0		3.0	V
Static Drain-Source On-State Resistance		-	V <sub>GS</sub> =10V, I <sub>D</sub> =300mA			580	mΩ
		R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =300mA			900	mΩ
DYNAMIC PARAMETERS					=	ā.	_
Input Capacitance		Ciss			23.5		pF
Output Capacitance		Coss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz		12.4		pF
Reverse Transfer Capacitance		Crss			6.3		pF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_{\mathrm{G}}$	\ -24\\ \ \ -10\\   -0.6A		8.4		nC
Gate to Source Charge		Q <sub>G</sub> s	V <sub>DS</sub> =24V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.6A (Note 1, 2)		1.3		nC
Gate to Drain Charge		$Q_{GD}$	(Note 1, 2)		1.1		nC
Turn-ON Delay Time		$t_{D(ON)}$			1		ns
Rise Time		$t_{R}$	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.6A,		16.8		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =3Ω (Note 1, 2)		2.6		ns
Fall Time		t⊦			27.2		ns
SOURCE DRAIN DIODE RATIN	IGS AND C	HARACTER	ISTICS				
Maximum Body Diode Continuous Current		Is				0.6	Α
Maximum Body Diode Pulsed Current		Ism				1.2	Α
Drain-Source Diode Forward Voltage		VsD	Is=0.6A, V <sub>GS</sub> =0V			1.4	V
Reverse Recovery Time		t <sub>rr</sub>	I <sub>S</sub> =0.6A, V <sub>GS</sub> =0V		107		ns
Reverse Recovery Charge (Note 1)		$Q_{rr}$	dI <sub>S</sub> /d <sub>t</sub> =100A/μs		70		nC

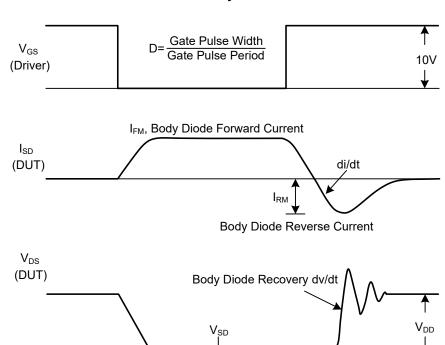
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.

# ■ TEST CIRCUITS AND WAVEFORMS



#### Peak Diode Recovery dv/dt Test Circuit

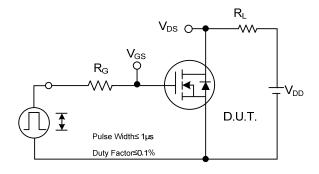


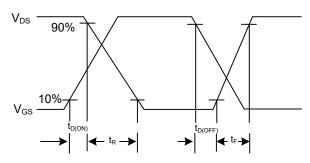
Peak Diode Recovery dv/dt Test Circuit and Waveforms

#### Peak Diode Recovery dv/dt Waveforms

Body Diode Forward Voltage Drop

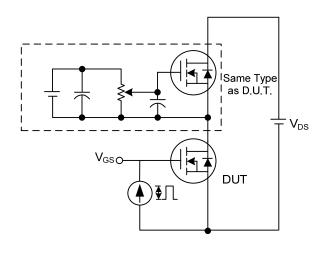
# TEST CIRCUITS AND WAVEFORMS

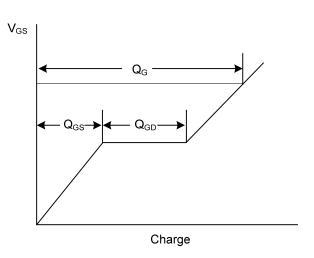




**Switching Test Circuit** 

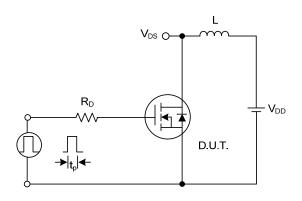
**Switching Waveforms** 

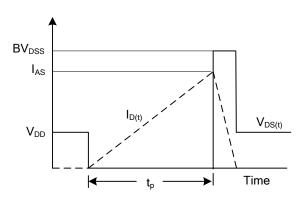




**Gate Charge Test Circuit** 

**Gate Charge Waveform** 





**Unclamped Inductive Switching Test Circuit** 

**Unclamped Inductive Switching Waveforms** 

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