



UT1D2N02LZ

Preliminary

Power MOSFET

1.2A, 20V N-CHANNEL POWER MOSFET

DESCRIPTION

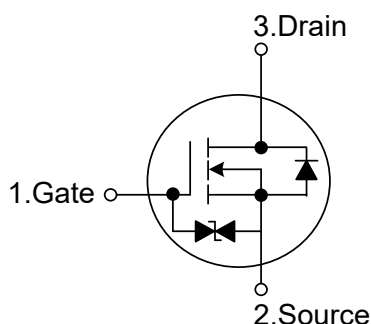
The UTC **UT1D2N02LZ** employs advanced MOSFET technology and features low gate charge while maintaining low on-resistance.

Optimized for switching applications, this device improves the overall efficiency of DC/DC converters and allows operation to higher switching frequencies.

FEATURES

- * $R_{DS(on)} \leq 210 \text{ m}\Omega$ @ $V_{GS}=4.5\text{V}$, $I_D=0.6\text{A}$
- $R_{DS(on)} \leq 275 \text{ m}\Omega$ @ $V_{GS}=2.5\text{V}$, $I_D=0.5\text{A}$
- $R_{DS(on)} \leq 400 \text{ m}\Omega$ @ $V_{GS}=1.8\text{V}$, $I_D=0.35\text{A}$

SYMBOL



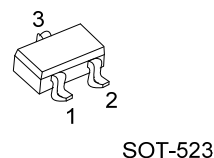
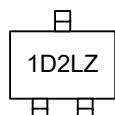
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT1D2N02LZL-AN3-R	UT1D2N02LZG-AN3-R	SOT-523	G	S	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UT1D2N02LZG-AN3-R	(1) Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel (2) AN3: SOT-523 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



SOT-523

■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 8	V
Continuous Drain Current	Continuous	I_D	1.2	A
	Pulsed (Note 2)	I_{DM}	4	A
Avalanche Energy	Repetitive (Note 3)	E_{AS}	1	mJ
Power Dissipation ($T_A=25^{\circ}\text{C}$)		P_D	0.17	W
Junction Temperature		T_J	+150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 0.1\text{mH}$, $I_{AS} = 4.5\text{A}$, $V_{DD} = 15\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	833 (Note)	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate P_c board, 2oz copper, with 1inch square copper plate.

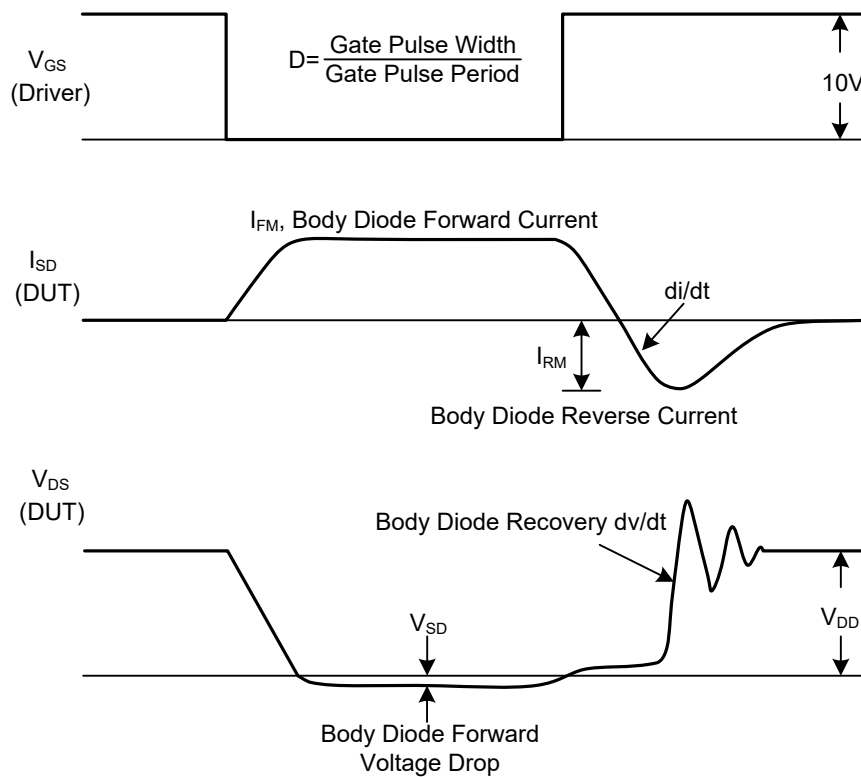
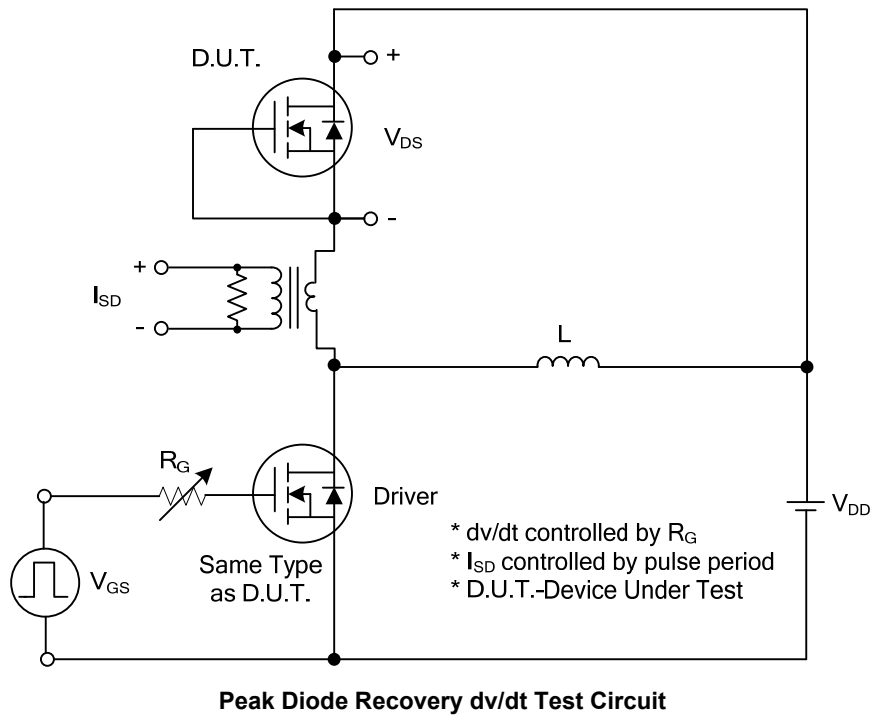
■ ELECTRICAL CHARACTERISTICS ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{DS} =0V	20			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =20V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{DS} =0V, V _{GS} =+8V			10	μA
	Reverse		V _{DS} =0V, V _{GS} =-8V			-10	μA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D =250μA	0.3		1.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =4.5V, I _D =0.6A			210	mΩ
			V _{GS} =2.5V, I _D =0.5A			275	mΩ
			V _{GS} =1.8V, I _D =0.35A			400	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =10V, f=1MHz		44		pF
Output Capacitance		C _{OSS}			25		pF
Reverse Transfer Capacitance		C _{RSS}			17		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DD} =16V, V _{GS} =10V, I _D =2.0A (Note 1, 2)		8		nC
Gate to Source Charge		Q _{GS}			1		nC
Gate to Drain Charge		Q _{GD}			1		nC
Turn-ON Delay Time (Note 1)		t _{D(ON)}	V _{DD} =10V, V _{GS} =10V, I _D =2.0A, R _G =3Ω (Note 1, 2)		6		ns
Rise Time		t _R			17		ns
Turn-OFF Delay Time		t _{D(OFF)}			78		ns
Fall-Time		t _F			73		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Continuous Drain-Source Diode Forward Current		I _S				1.2	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				4	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =2.0A, V _{GS} =0V			1.4	V

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

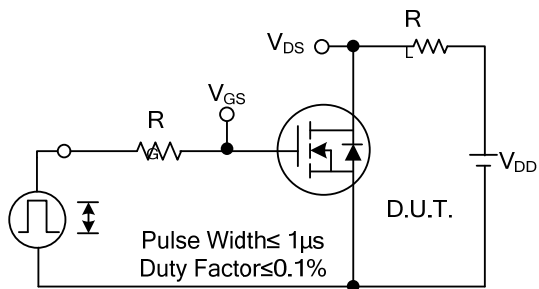
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

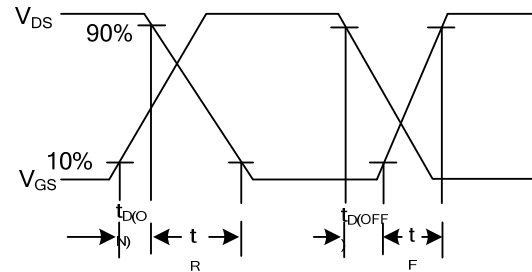


Peak Diode Recovery dv/dt Waveforms

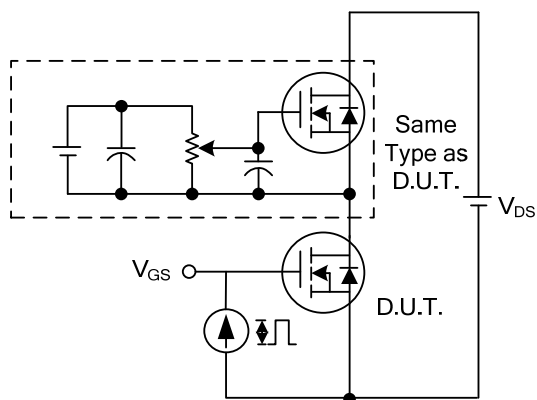
■ TEST CIRCUITS AND WAVEFORMS



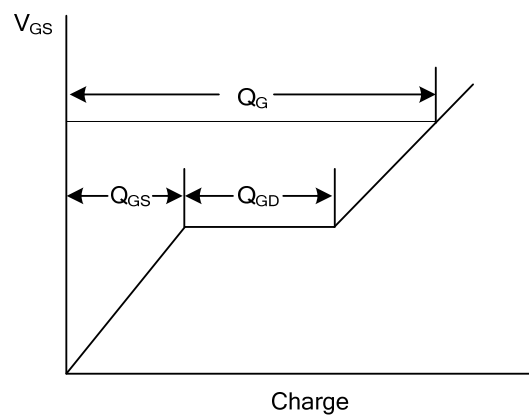
Switching Test Circuit



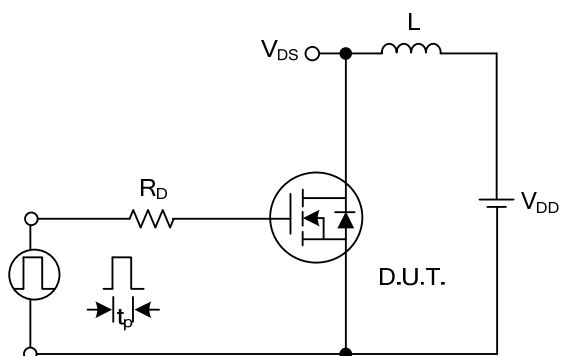
Switching Waveforms



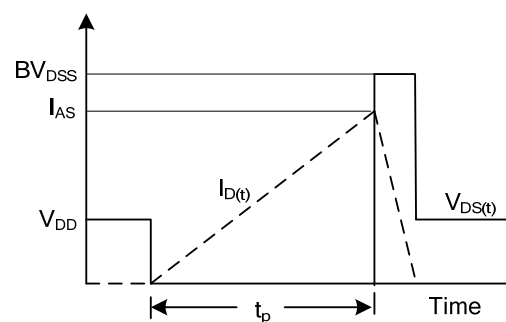
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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