

UTC UNISONIC TECHNOLOGIES CO., LTD

UT2302D **Preliminary Power MOSFET**

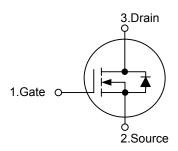
N-CHANNEL ENHANCEMENT MODE

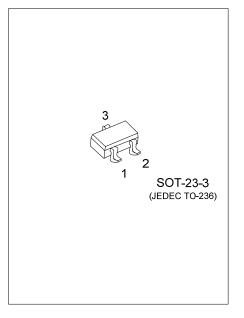
DESCRIPTION

The UTC UT2302D is N-channel Power MOSFET, designed with high density cell, with fast switching speed, ultra low on-resistance, and excellent thermal and electrical capabilities.

Used in commercial and industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

SYMBOL

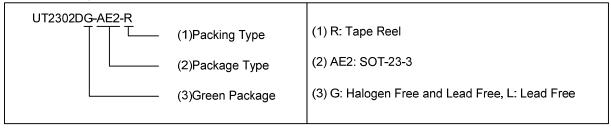




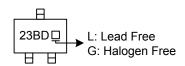
ORDERING INFORMATION

Ordering Number		Daakana	Pin Assignment			Dankina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT2302DL-AE2-R	UT2302DG-AE2-R	SOT-23-3	G	S	D	Tape Reel	

Note: Pin Assignment: G: Gate S: Source D: Drain



MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{ extsf{DSS}}$	20	V
Gate-Source Voltage		V_{GSS}	±8	V
Drain Current	Continuous	I _D	2.4	Α
	Pulsed (Note 2)	I _{DM}	10	Α
Power Dissipation		P _D	0.4	W
Junction Temperature		TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction-to-Ambient	θ_{JA}	312	°C/W	

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

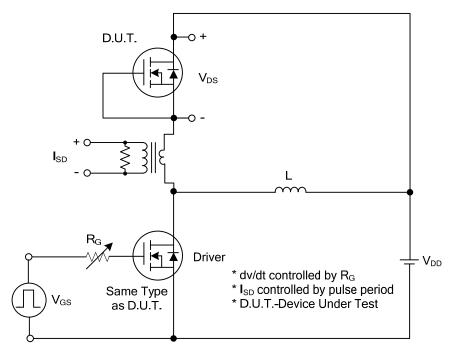
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250μA	20			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =20V, V _{GS} =0V			10	μΑ
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =8V, V _{DS} =0V			100	nA
	Reverse		V_{GS} =-8V, V_{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	0.45		1.2	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =4.5V, I _D =7.2A			90	mΩ
			V _{GS} =2.5V, I _D =3.1A			120	mΩ
			V _{GS} =1.8V, I _D =3.0A			182	mΩ
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{ISS}			69		pF
Output Capacitance		Coss	V _{DS} =10V, V _{GS} =0V, f=1MHz		31		pF
Reverse Transfer Capacitance		C _{RSS}			22		pF
SWITCHING CHARACTERISTIC	cs						
Total Gate Charge (Note 1)		Q_G	\\ -16\\ \\ -10\\ \ \ -2.0\\		7		nC
Gate to Source Charge		Q_GS	V_{DS} =16V, V_{GS} =10V, I_{D} =2.0A,		2		nC
Gate to Drain Charge		Q_GD	I _G =1mA (Note 1, 2)		0.6		nC
Turn-ON Delay Time (Note 1)		t _{D (ON)}			2		ns
Rise Time		t _R	V_{DD} =10V, V_{GS} =10V, I_{D} =2.0A,		14		ns
Turn-OFF Delay Time		t _{D (OFF)}	R _G =6Ω (Note 1, 2)		9		ns
Fall-Time		t _F			15		ns
DRAIN-SOURCE DIODE CHAR	ACTERISTICS	AND MAXI	MUM RATINGS				
Maximum Body-Diode Continuous Current		Is				2.4	Α
Maximum Body-Diode Pulsed Current		I _{SM}				10	Α
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I _S =2.0A, V _{GS} =0V			1.4	V

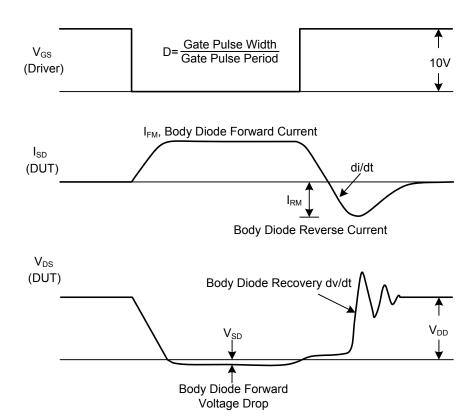
Notes: 1. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

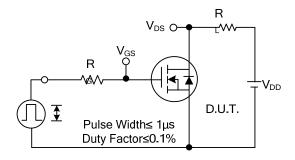


Peak Diode Recovery dv/dt Test Circuit

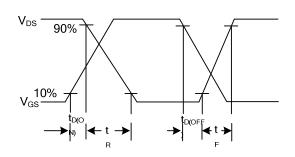


Peak Diode Recovery dv/dt Waveforms

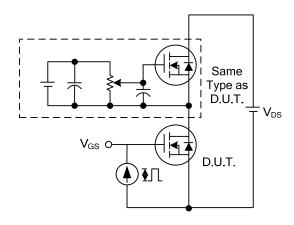
TEST CIRCUITS AND WAVEFORMS



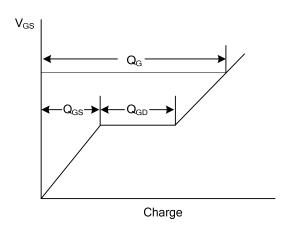
Switching Test Circuit



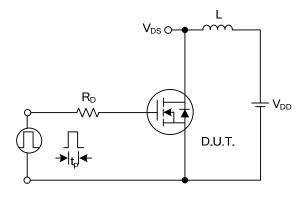
Switching Waveforms



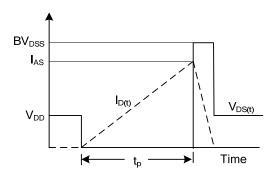
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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