

UNISONIC TECHNOLOGIES CO., LTD

UT2N03VZ

Preliminary

Power MOSFET

2.0A, 30V N-CHANNEL POWER MOSFET

■ DESCRIPTION

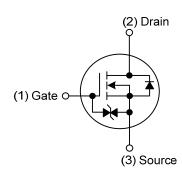
The UTC ${\bf UT2N03VZ}$ employs advanced MOSFET technology and features low gate charge while maintaining low on-resistance.

Optimized for switching applications, this device improves the overall efficiency of DC/DC converters and allows operation to higher switching frequencies.

■ FEATURES

- * $R_{DS(on)} \le 250 \text{ m}\Omega$ @ V_{GS} =4.5V, I_{D} =2.0A $R_{DS(on)} \le 350 \text{ m}\Omega$ @ V_{GS} =2.5V, I_{D} =1.0A
- * Low Capacitance
- * Low Gate Charge
- * Fast Switching Capability
- * Avalanche Energy Specified

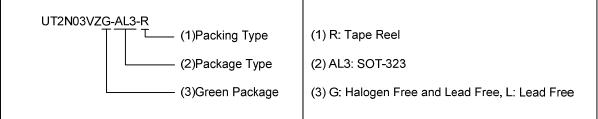
■ SYMBOL



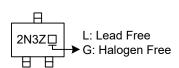
ORDERING INFORMATION

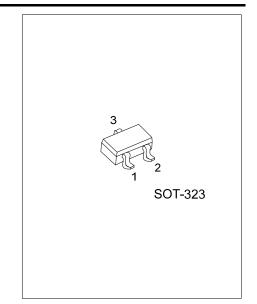
Ordering Number		Daakasa	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UT2N03VZL-AL3-R	UT2N03VZG-AL3-R	SOT-323	G	S	D	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING





www.unisonic.com.tw 1 of 5

ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT		
Drain-Source Voltage		V _{DSS}	30	V		
Gate-Source Voltage		V _{GSS}	±10	V		
Continuous Drain Current	Continuous	I _D	2	Α		
	Pulsed (Note 2)	Ірм	4	Α		
Power Dissipation (T _A =25°C)		P _D	0.2	W		
Junction Temperature		TJ	+150	°C		
Storage Temperature		T _{STG}	-55 ~ +150	°C		

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	625	°C/W	

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C ,unless otherwise specified)

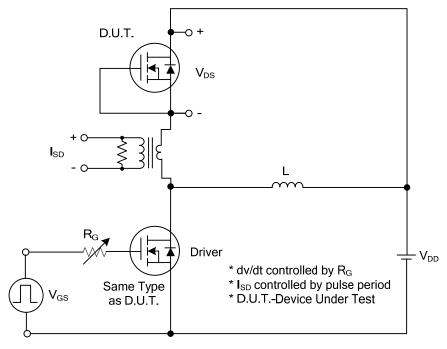
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{DS} =0V				V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	μΑ
Gate-Source Leakage Current	Forward	lgss	V _{DS} =0V, V _{GS} =+10V			10	μΑ
	Reverse		V _{DS} =0V, V _{GS} =-10V			-10	μΑ
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{D}=250\mu A$			1.5	V
Static Drain-Source On-State Resista	oiotopoo	Dagger	V _{GS} =4.5V, I _D =2.0A			250	mΩ
	sistance	R _{DS(ON)}	V _{GS} =2.5V, I _D =1.0A			350	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		Ciss			74.8		рF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =15V, f=1MHz		17.2		рF
Reverse Transfer Capacitance		Crss			11.1		рF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q _G	V _{DD} =24V, V _{GS} =10V, I _D =2.0A (Note 1, 2)		14.3		nC
Gate to Source Charge		Q _{GS}			1.66		nC
Gate to Drain Charge		Q_{GD}	(Note 1, 2)		2.74		nC
Turn-ON Delay Time (Note 1)		$t_{D(ON)}$			5.6		ns
Rise Time		t_{R}	V _{DD} =15V, V _{GS} =10V, I _D =2.0A, R _G =25Ω (Note 1, 2)		15		ns
Turn-OFF Delay Time		t _{D(OFF)}			163		ns
Fall-Time		t⊦			67.6		ns
SOURCE- DRAIN DIODE RATIF	NGS AND	CHARACTERI	STICS				
Maximum Continuous Drain-Source Diode		Is				2	Α
Forward Current							A
Maximum Pulsed Drain-Source Diode		I _{SM}				4	Α
Forward Current		ISIVI				7	^
Drain-Source Diode Forward Voltage		V _{SD}	Is=2.0A, V _{GS} =0V			1.4	V
(Note 1)			15-2.0A, VGS-UV			1.4	v

Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%.

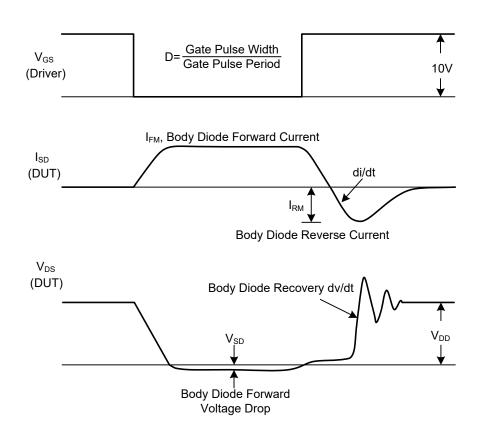
2. Essentially independent of operating temperature.



■ TEST CIRCUITS AND WAVEFORMS

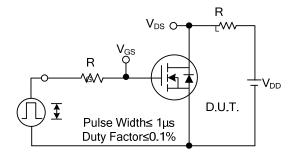


Peak Diode Recovery dv/dt Test Circuit

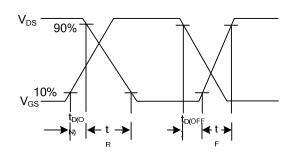


Peak Diode Recovery dv/dt Waveforms

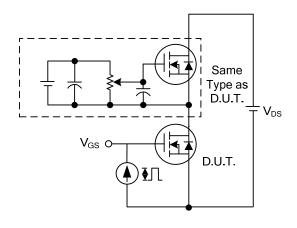
TEST CIRCUITS AND WAVEFORMS



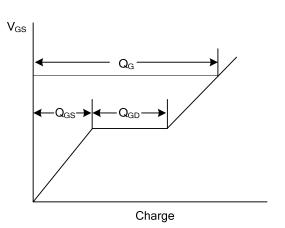
Switching Test Circuit



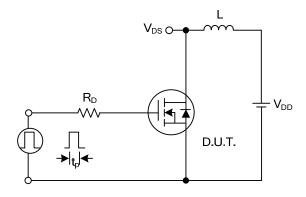
Switching Waveforms



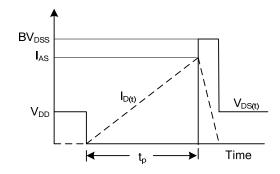
Gate Charge Test Circuit



Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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