



UT2P06

Power MOSFET

-2.0A, -60V (D-S) P-CHANNEL POWER MOSFET

DESCRIPTION

The UTC **UT2P06** is a P-channel enhancement power MOSFET using UTC's advanced technology to provide the customers with perfect $R_{DS(ON)}$ and low gate charge.

This UTC **UT2P06** can be operated with -4.5V low gate voltage.

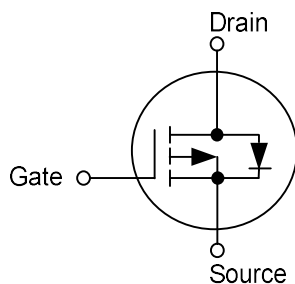
FEATURES

* $R_{DS(ON)} \leq 175 \text{ m}\Omega$ @ $V_{GS} = -10\text{V}$, $I_D = -0.9\text{A}$

$R_{DS(ON)} \leq 230 \text{ m}\Omega$ @ $V_{GS} = -4.5\text{V}$, $I_D = -0.8\text{A}$

* High switching speed

SYMBOL

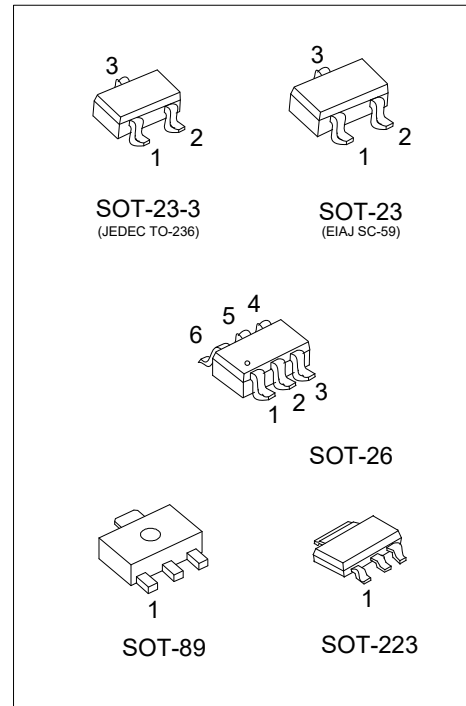


ORDERING INFORMATION

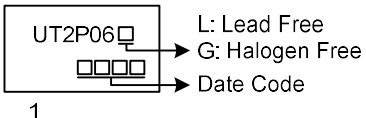
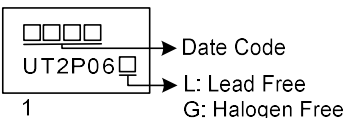
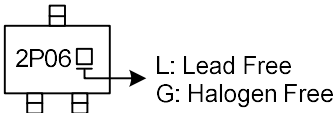
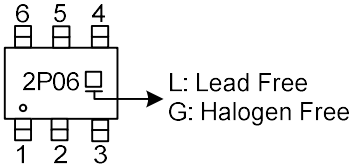
Ordering Number		Package	Pin Assignment						Packing
Lead Free	Halogen Free		1	2	3	4	5	6	
UT2P06L-AB3-R	UT2P06G-AB3-R	SOT-89	G	D	S	-	-	-	Tape Reel
UT2P06L-AA3-R	UT2P06G-AA3-R	SOT-223	G	D	S	-	-	-	Tape Reel
UT2P06L-AE2-R	UT2P06G-AE2-R	SOT-23-3	G	S	D	-	-	-	Tape Reel
UT2P06L-AE3-R	UT2P06G-AE3-R	SOT-23	G	S	D	-	-	-	Tape Reel
UT3N06L-AG6-R	UT3N06G-AG6-R	SOT-26	D	D	G	S	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

	(1) Packing Type
	(2) Package Type
	(3) Green Package
(1) R: Tape Reel	
(2) AA3: SOT-223, AB3: SOT-89, AE2: SOT-23-3, AE3: SOT-23, AG6: SOT-26	
(3) G: Halogen Free and Lead Free, L: Lead Free	



MARKING

SOT-223	SOT-89
 UT2P06 1 □□□□ L: Lead Free G: Halogen Free Date Code	 UT2P06 1 □□□□ Date Code L: Lead Free G: Halogen Free
SOT-23-3 / SOT-23	SOT-26
 2P06 1 L: Lead Free G: Halogen Free	 2P06 1 □□□□ L: Lead Free G: Halogen Free

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			V _{DSS}	-60	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current	Continuous		I _D	-2	A
	Pulsed		I _{DM}	-6.03	A
Avalanche Current (L=0.1mH)			I _{AR}	-7	A
Power Dissipation (Note 1, 2)	(Note 4a)	SOT-23-3 SOT-23	P _D	0.5	W
	(Note 4b)	SOT-23-3 SOT-23		0.46	W
Power Dissipation (Note 1, 2)		SOT-223		3.0	W
		SOT-26		0.5	W
		SOT-89		0.7	W
Junction Temperature			T _J	+150	°C
Storage Temperature			T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Surface Mounted on FR4 Board.

3. $t \leq 5$ sec.

4. θ_{JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. θ_{JC} is guaranteed by design while θ_{CA} is determined by the user's board design.



a. 250°C/W when mounted on a
0.02 in² pad of 2 oz. copper.



b. 270°C/W when mounted on a
minimum pad.

Scale 1 : 1 on letter size paper

■ THERMAL DATA (NOTE.)

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	41.6	$^{\circ}\text{C/W}$
	SOT-23-3		320	$^{\circ}\text{C/W}$
	SOT-23/SOT-26			
	SOT-89		178	$^{\circ}\text{C/W}$

Note: Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$. The pulse current is limited by the maximum junction temperature.

■ **ELECTRICAL CHARACTERISTICS** ($T_J=25^\circ\text{C}$, unless otherwise specified)

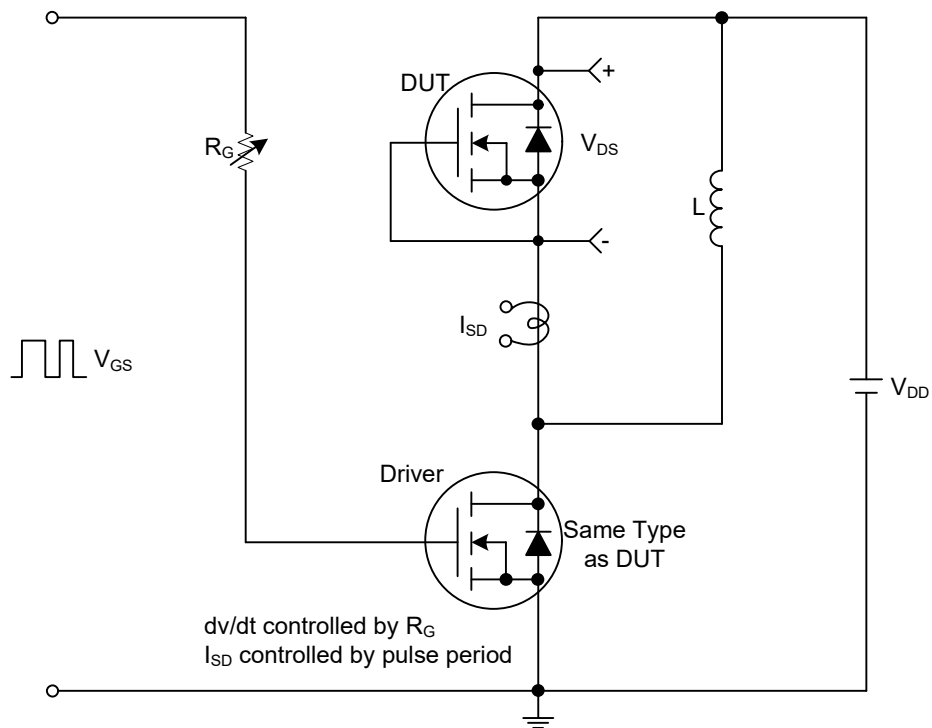
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =-250μA, V _{DS} =0V	-60			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-0.5	μA
Gate- Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance (Note 1)		R _{DS(ON)}	V _{GS} =-10V, I _D =-0.9A			175	mΩ
			V _{GS} =-4.5V, I _D =-0.8A			230	mΩ
DYNAMIC PARAMETERS							
Input Capacitance (Note 3)		C _{ISS}	V _{GS} =0V, V _{DS} =-25V, f=1.0MHz		545		pF
Output Capacitance (Note 3)		C _{OSS}			43		pF
Reverse Transfer Capacitance (Note 3)		C _{RSS}			31		pF
SWITCHING PARAMETERS (Note 2)							
Total Gate Charge (Note 3)		Q _G	V _{GS} =-10V, V _{DS} =-48V, I _D =-2A I _G =-1mA		16		nC
Gate to Source Charge (Note 3)		Q _{GS}			3.6		nC
Gate to Drain Charge (Note 3)		Q _{GD}			3		nC
Turn-ON Delay Time (Note 2, 3)		t _{D(ON)}	V _{DD} =-30V, V _{GS} =-10V, I _D =-2A, R _G ≈6Ω		4		ns
Rise Time (Note 2, 3)		t _R			16		ns
Turn-OFF Delay Time (Note 2, 3)		t _{D(OFF)}			19		ns
Fall-Time (Note 2, 3)		t _F			18		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS (Note 2)							
Maximum Body-Diode Continuous Current		I _S	T _A =25°C (Note 2)			-1.42	A
Maximum Body-Diode Pulsed Current		I _{SM}	T _A =25°C (Note 3)			-6.03	A
Drain-Source Diode Forward Voltage (Note 1)		V _{SD}	I _S =-0.8A, V _{GS} =0V		-0.85	-0.95	V
Reverse Recovery Time		t _{rr}	I _F =-2.0A, di/dt=100A/μs		66		ns
Reverse Recovery Charge		Q _{rr}			63		nC

Notes: 1. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

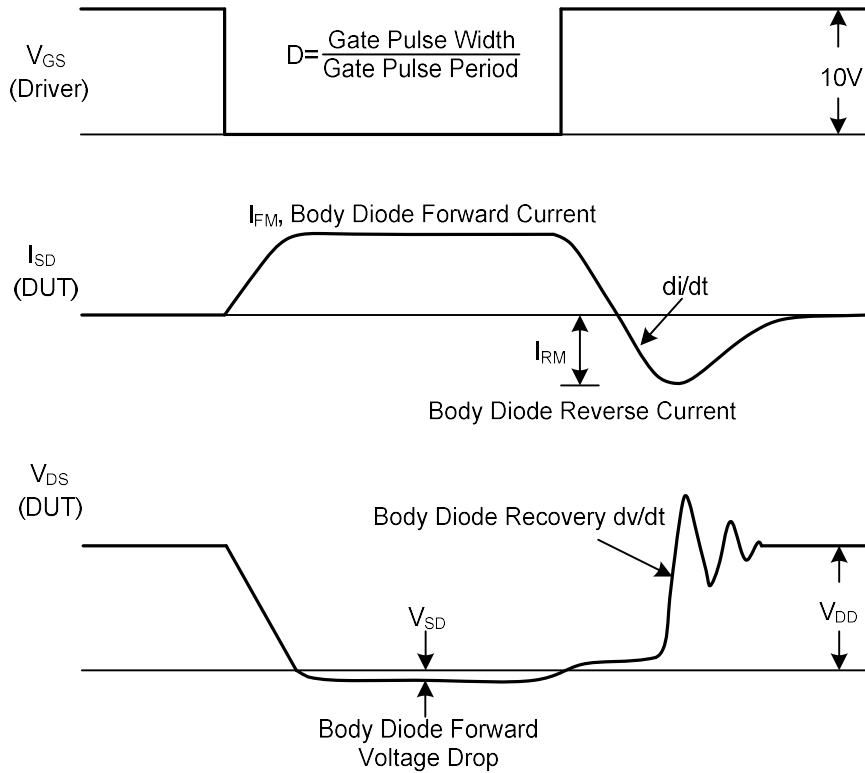
2. Switching characteristics are independent of operating junction temperature.

3. For design aid only, not subject to production testing.

■ TEST CIRCUITS AND WAVEFORMS



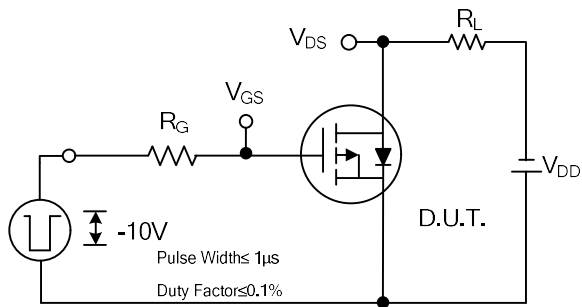
Peak Diode Recovery dv/dt Test Circuit



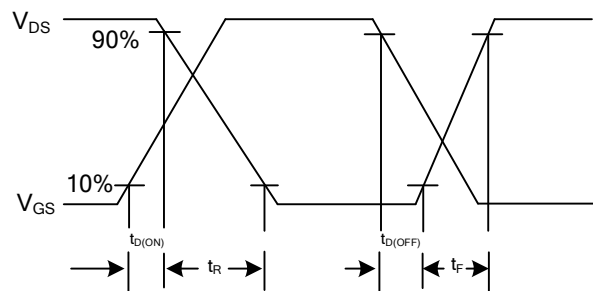
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

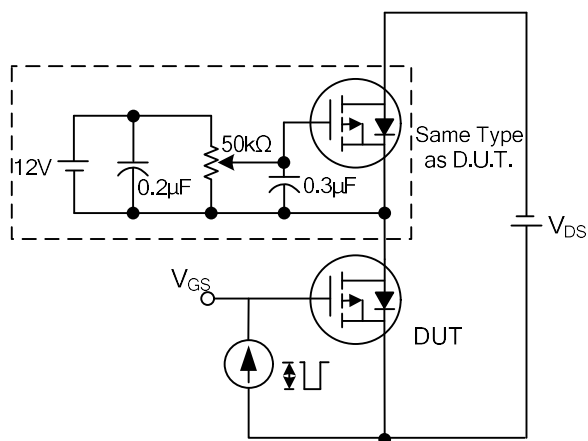
■ TEST CIRCUITS AND WAVEFORMS



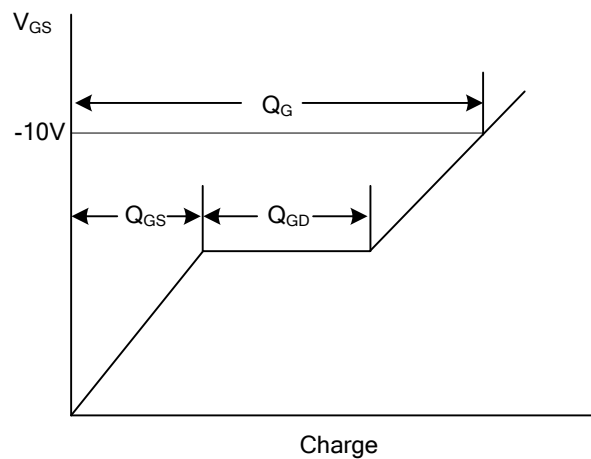
Switching Test Circuit



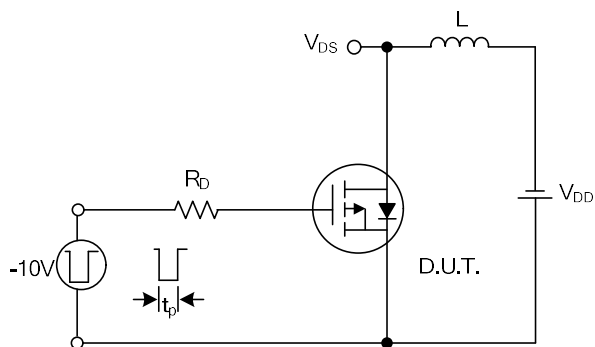
Switching Waveforms



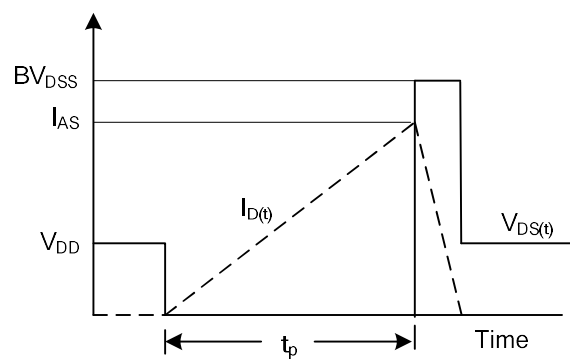
Gate Charge Test Circuit



Gate Charge Waveform

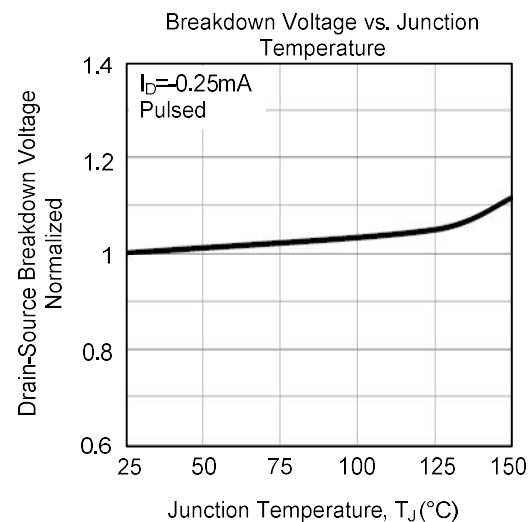
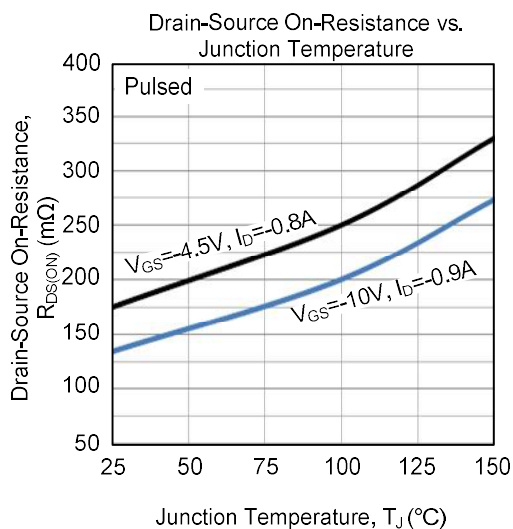
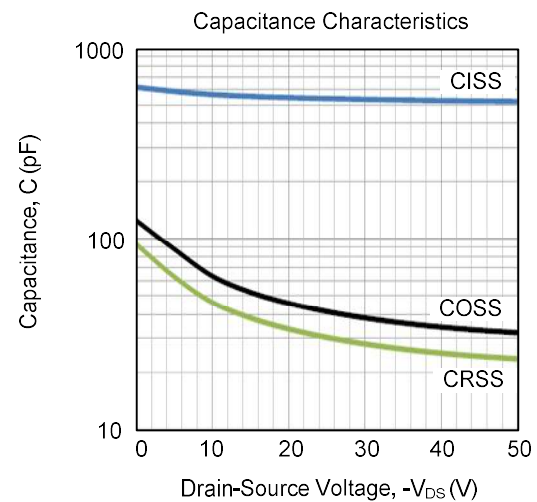
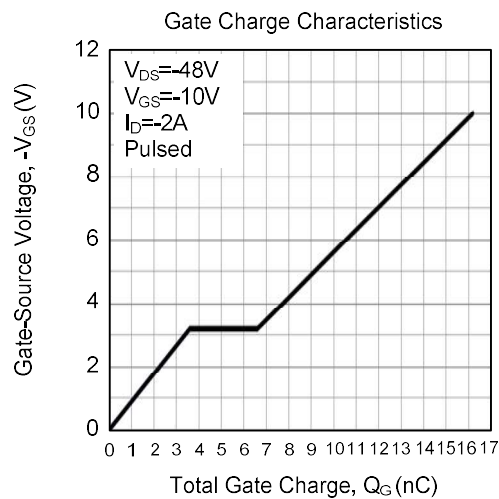
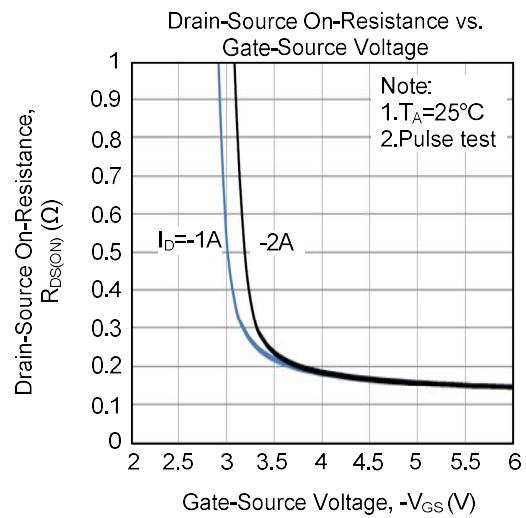
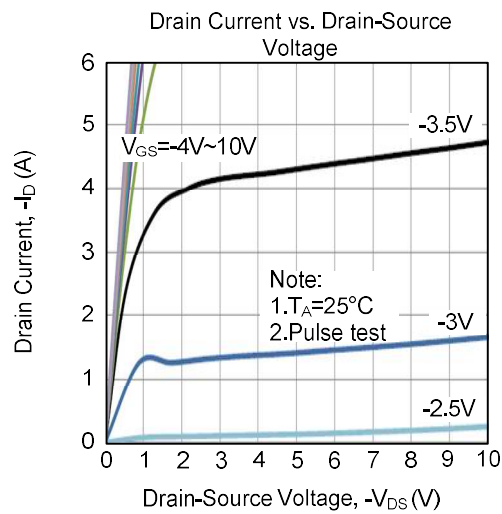


Unclamped Inductive Switching Test Circuit

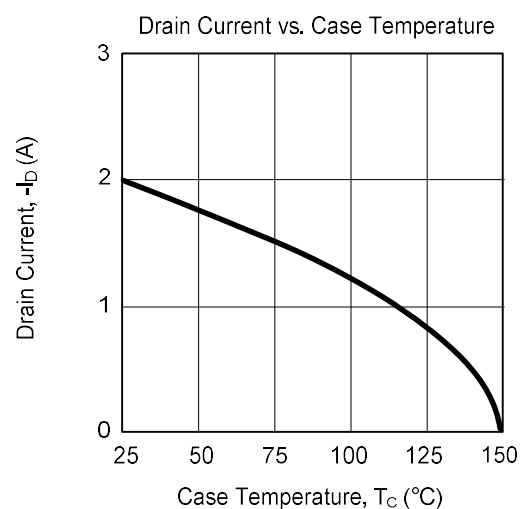
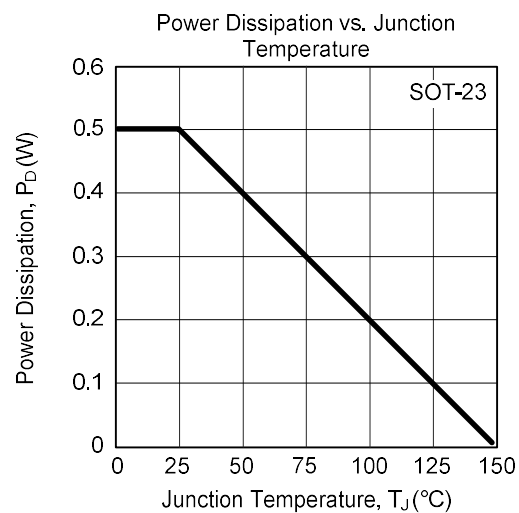
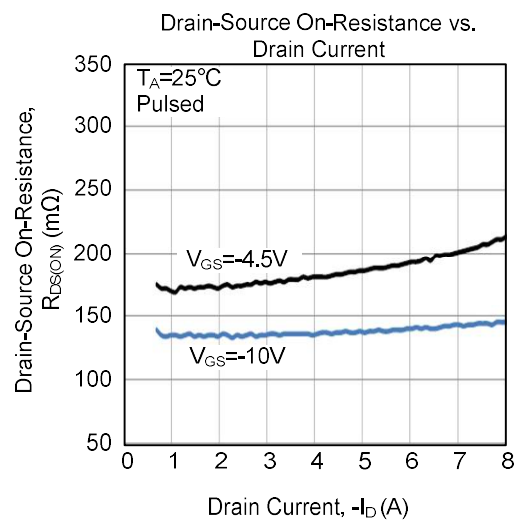
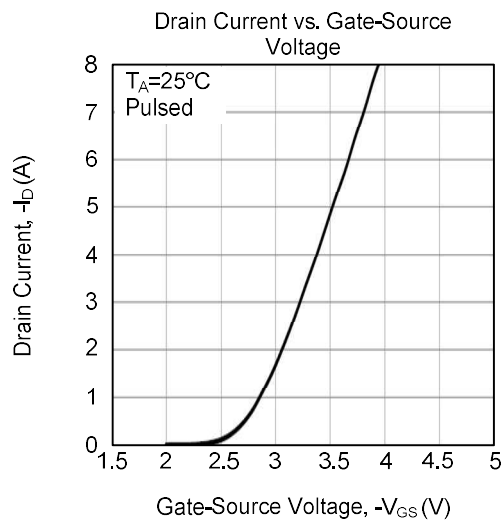
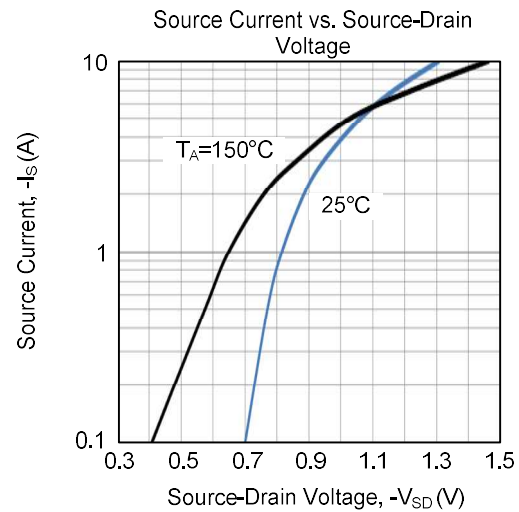
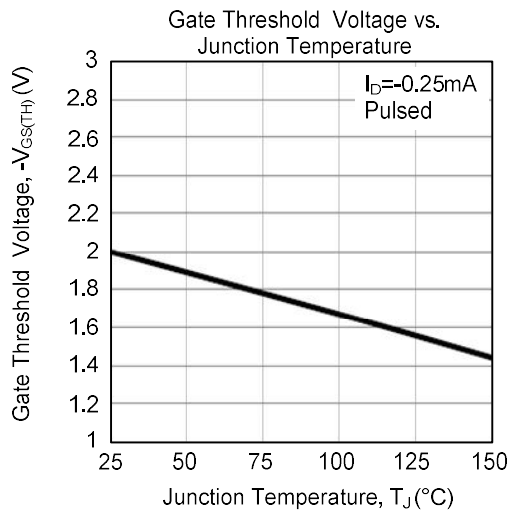


Unclamped Inductive Switching Waveforms

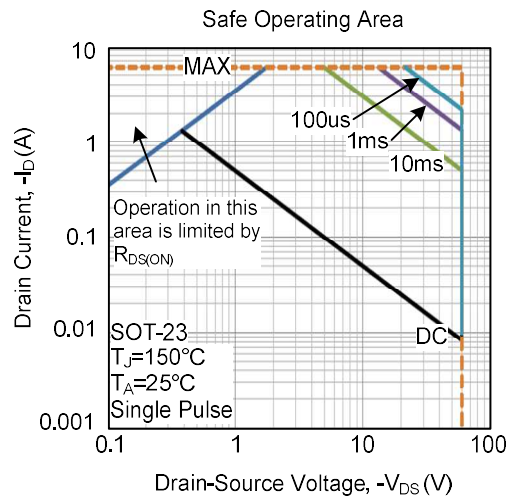
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



Note: 250°C/W when mounted on a 0.02 in² pad of 2 oz. copper.

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