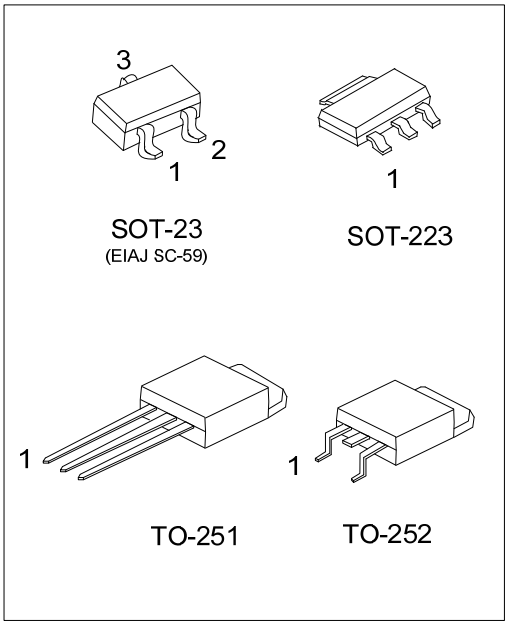
UT4N06

POWER MOSFET

4.0A, 60V SHIELDED GATE N-CHANNEL POWER MOSFET



■ DESCRIPTION

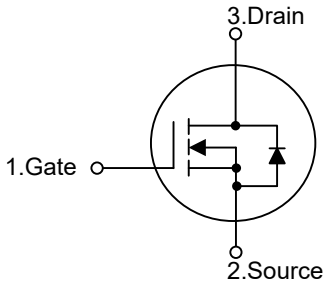
The UTC UT4N06 is N-Channel enhancement mode silicon gate power MOSFET, In addition to a significant reduction in onresistance, this device is designed to ensure a high level of dv/dt capability for the most demanding applications.

Is designed for high voltage, high speed power switching applications such as switching regulators, switching converters, solenoid, motor drivers, relay drivers.

■ FEATURES

* $R_{DS(ON)} \leq 75\ m\Omega$ @ V_{GS}=10V, I_D=2.0A
  $R_{DS(ON)} \leq 105\ m\Omega$ @ V_{GS}=4.5V, I_D=2.0A
* Simple drive requirement
* Single Pulse Avalanche Energy Rated
* Fast Switching Speeds
* Linear Transfer Characteristics
* High Input Impedance

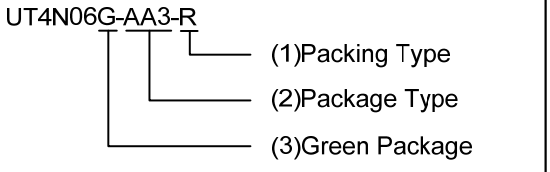
■ SYMBOL



■ ORDERING INFORMATION

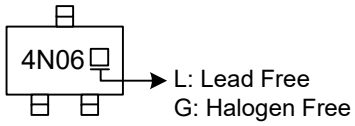
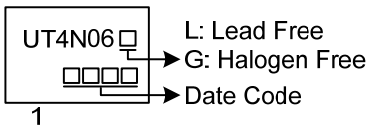
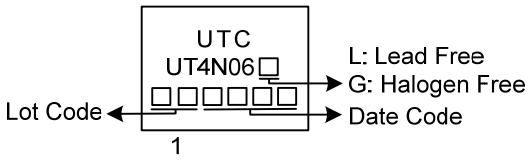
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UT4N06L-AA3-R	UT4N06G-AA3-R	SOT-223	G	D	S	Tape Reel
UT4N06L-AE3-R	UT4N06G-AE3-R	SOT-23	G	S	D	Tape Reel
UT4N06L-TM3-T	UT4N06G-TM3-T	TO-251	G	D	S	Tube
UT4N06L-TN3-R	UT4N06G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate S: Source D: Drain



(1) R: Tape Reel, T: Tube
(2) AA3: SOT-223, AE3: SOT-23, TM3: TO-251
TN3: TO-252
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING

SOT-23	SOT-223
	
TO-251 / TO-252	-
	-

■ ABSOLUTE MAXIMUM RATING (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I_D	4	A
	Pulsed	I_{DM}	16	A
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.8	V/ns
Power Dissipation(T_A=25°C)	SOT-223	P_D	1.5	W
	SOT-23		0.5	W
	TO-251 TO-252		2.5	W
Junction Temperature		T_J	+150	°C
Storage Temperature Range		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3. I_{SD} ≤ 4.0A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	83.3	°C/W
	SOT-23		250	°C/W
	TO-251 TO-252		50	°C/W
Junction to Case	SOT-223	θ_{JC}	62.5	°C/W
	SOT-23		100	°C/W
	TO-251 TO-252		4.62	°C/W

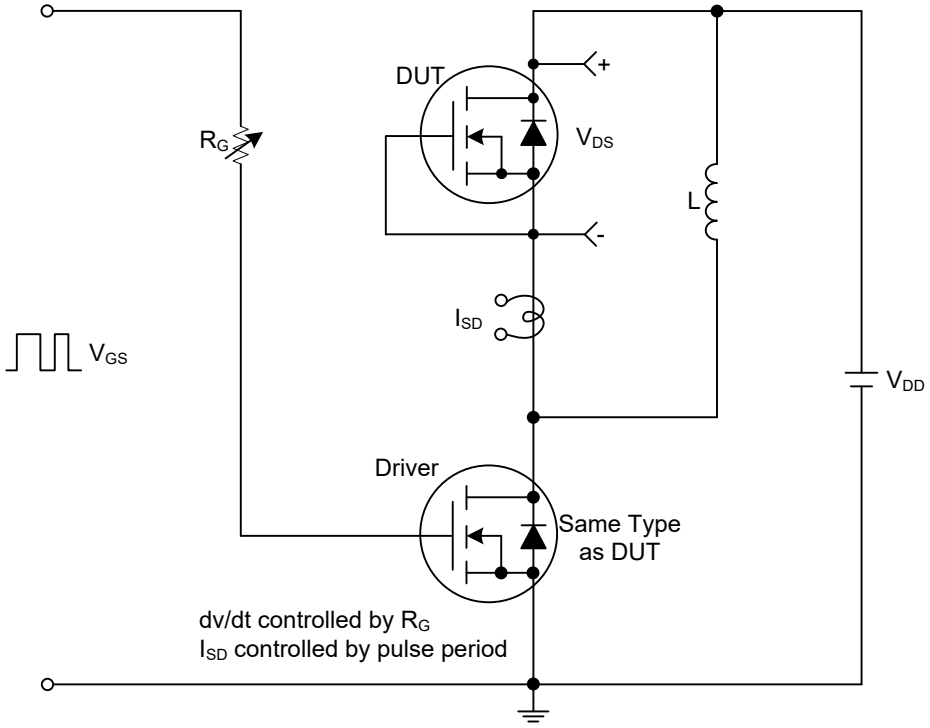
Note: Surface mounted on 1 in² copper pad of FR-4 board. 270°C/W when mounted on minimum copper pad.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

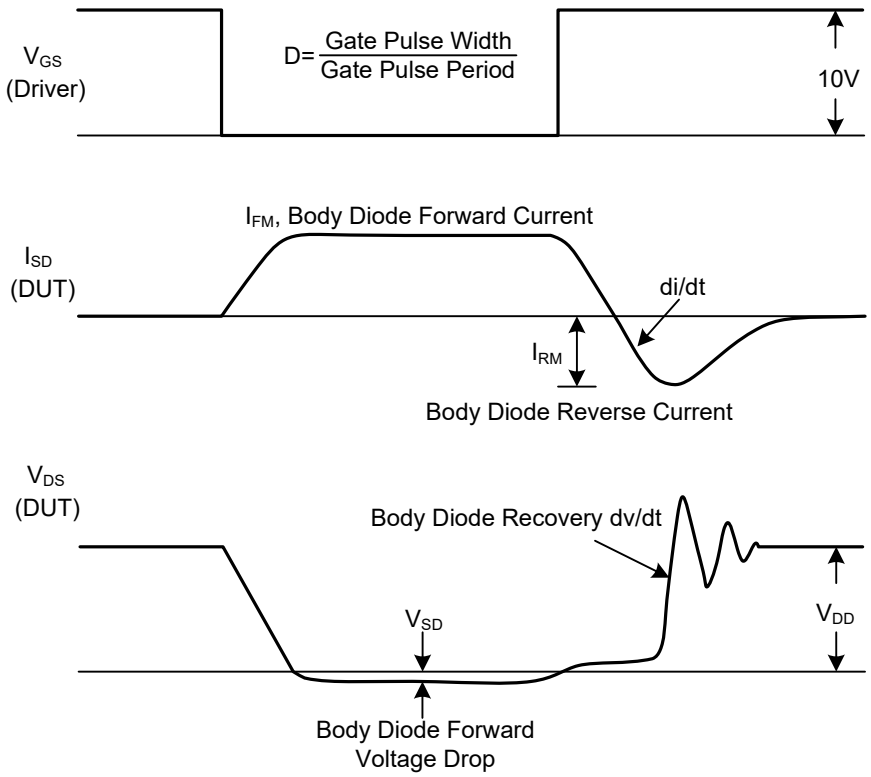
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=250μA, V_{GS}=0V	60			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=60V, V_{GS}=0V			1	μA
Gate-Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{DS}=0V			+100	nA
	Reverse		V_{GS}=-20V, V_{DS}=0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250μA	1.0		2.5	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=2.0A			75	mΩ
			V_{GS}=4.5V, I_D=2.0A			105	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{GS}=0V, V_{DS}=25V, f=1.0MHz		238		pF
Output Capacitance		C_{OSS}			40		pF
Reverse Transfer Capacitance		C_{RSS}			30		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q_G	V_{DS}=48V, V_{GS}=10V, I_D=4.0A (Note 1, 2)		12		nC
Gate to Source Charge		Q_{GS}			2		nC
Gate to Drain Charge		Q_{GD}			2.5		nC
Turn-on Delay Time (Note 1)		$t_{D(ON)}$	V_{DD}=30V, V_{GS}=10V, I_D =4.0A, R_G =3Ω (Note 1, 2)		5		ns
Rise Time		t_R			18		ns
Turn-off Delay Time		$t_{D(OFF)}$			10		ns
Fall-Time		t_F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				4	A
Maximum Body-Diode Pulsed Current		I_{SM}				16	A
Drain-Source Diode Forward Voltage (Note 1)		V_{SD}	I_S=4.0A, V_{GS}=0V			1.2	V
Reverse Recovery Time		t_{rr}	I_S=4.0A, V_{GS}=0V, d_{IF}/d_t=100A/μs		30		ns
Reverse Recovery Charge		Q_{rr}			25		nC

Notes: 1. Pulse Test: Pulse width ≤ 300μs, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS



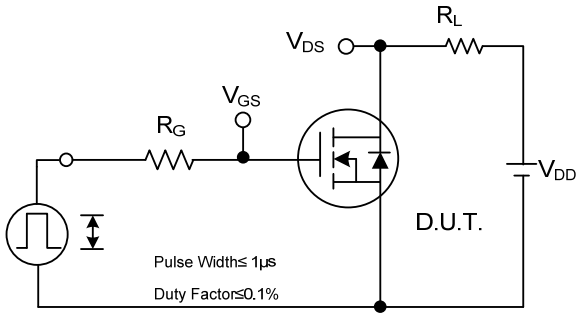
Peak Diode Recovery dv/dt Test Circuit



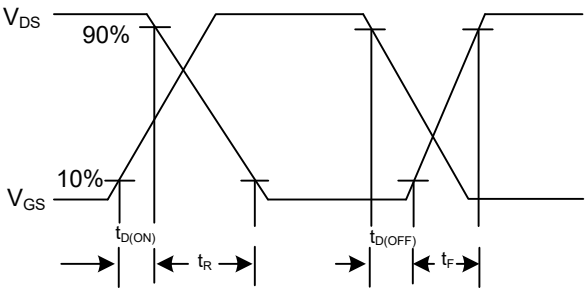
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

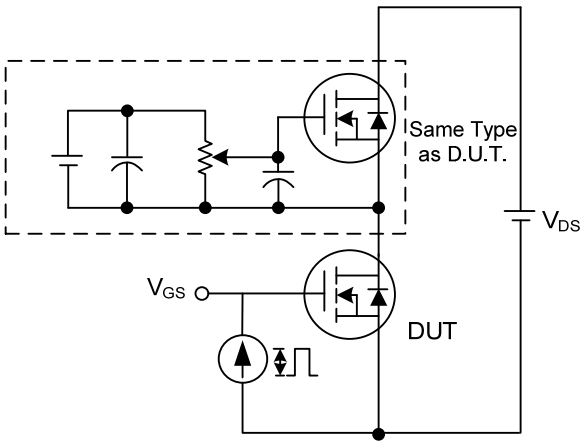
■ TEST CIRCUITS AND WAVEFORMS



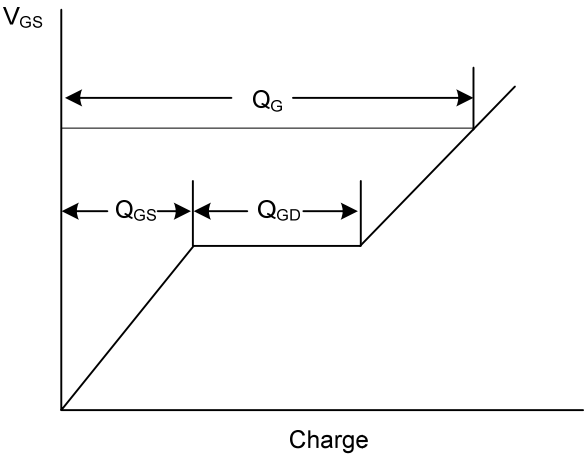
Switching Test Circuit



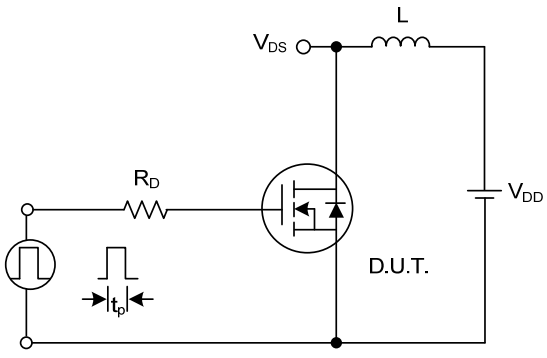
Switching Waveforms



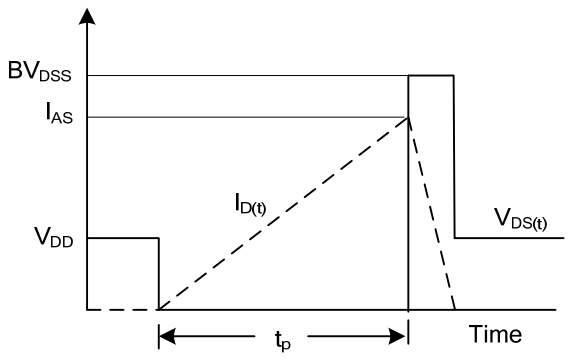
Gate Charge Test Circuit



Gate Charge Waveform

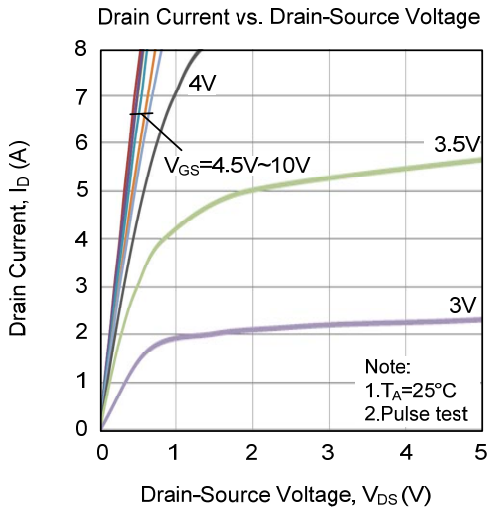


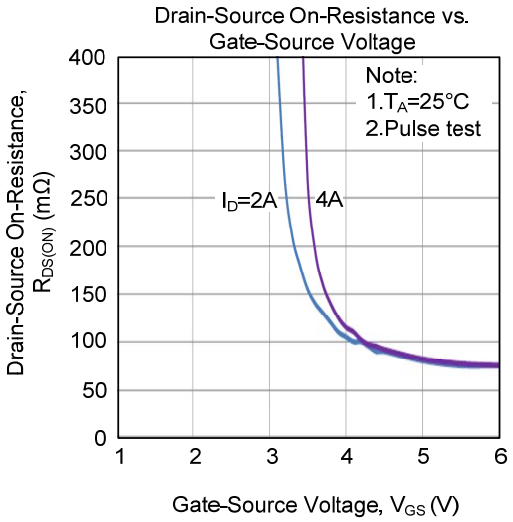
Unclamped Inductive Switching Test Circuit

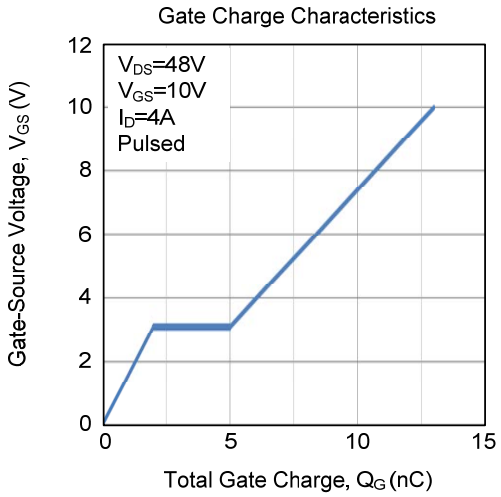


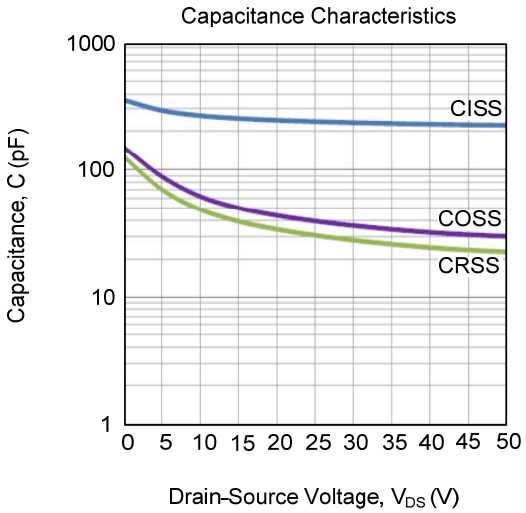
Unclamped Inductive Switching Waveforms

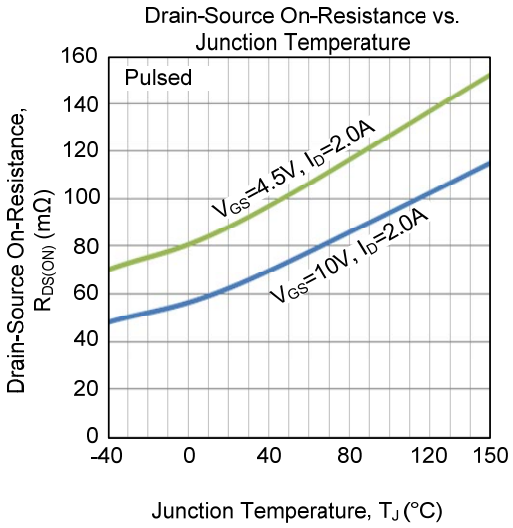
■ TYPICAL CHARACTERISTICS

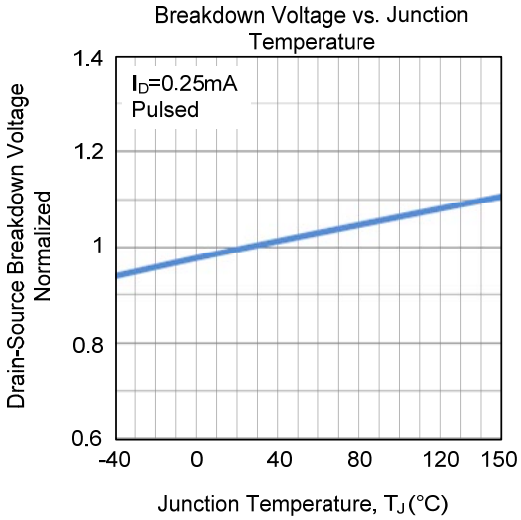






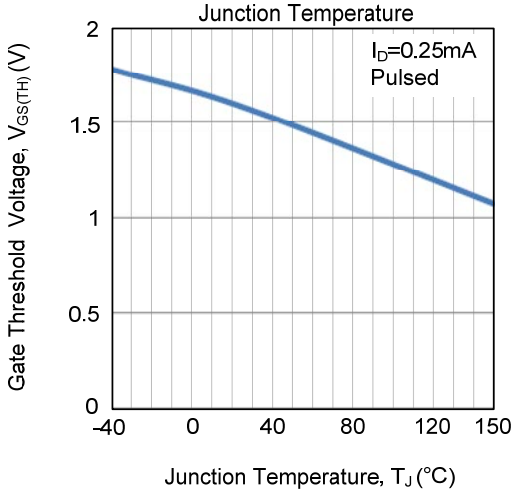




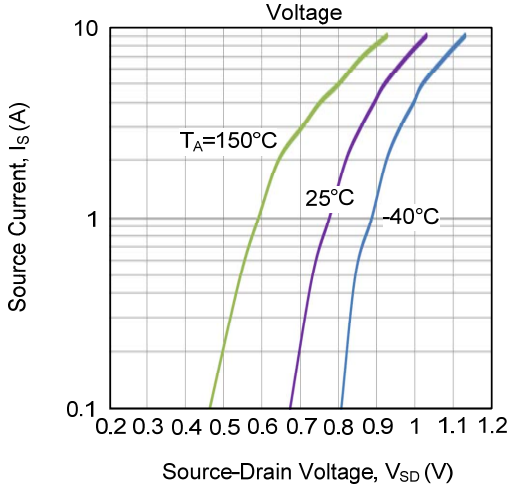


■ TYPICAL CHARACTERISTICS (Cont.)

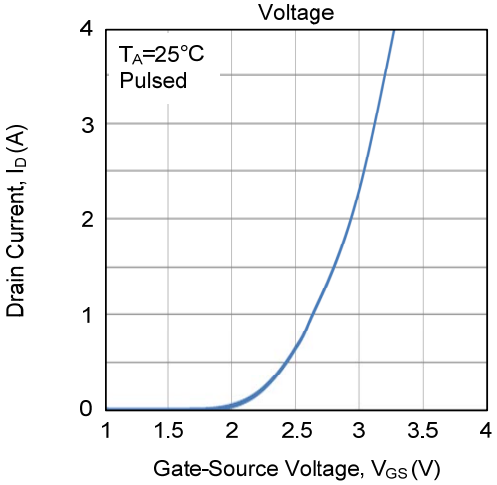
Gate Threshold Voltage vs. Junction Temperature



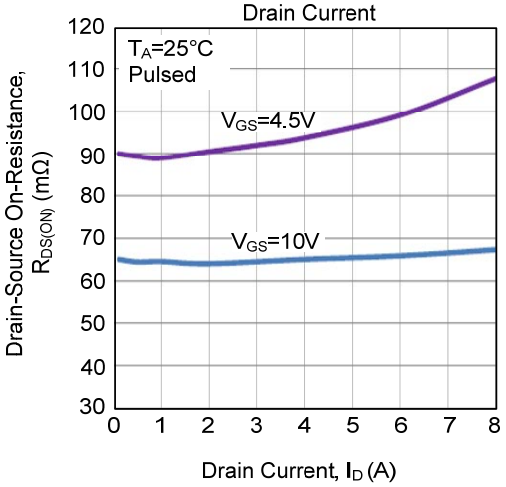
Source Current vs. Source-Drain Voltage



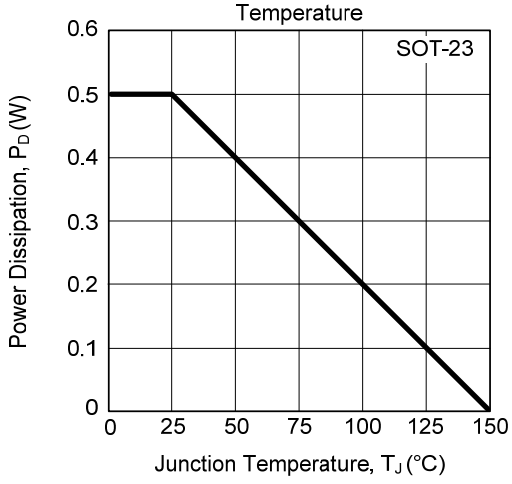
Drain Current vs. Gate-Source Voltage



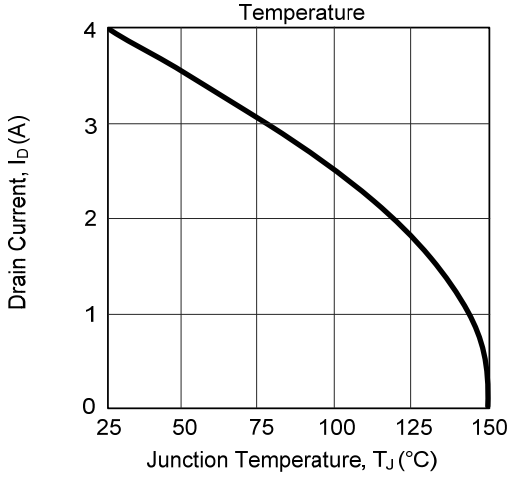
Drain-Source On-Resistance vs. Drain Current



Power Dissipation vs. Junction Temperature



Drain Current vs. Junction Temperature



■ TYPICAL CHARACTERISTICS (Cont.)