UNISONIC TECHNOLOGIES CO., LTD

UTG12N120-S

Insulated Gate Bipolar Transistor

1200V TRENCH GATE FIELD-STOP IGBT

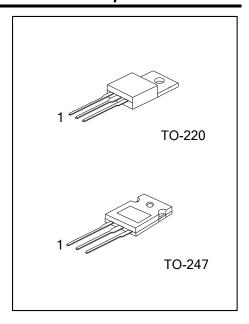
DESCRIPTION

The UTC **UTG12N120-S** is an Trench Field-Stop Insulated Gate Bipolar Transistor. it uses UTC's advanced technology to provide customers with high switching speed, low saturation voltage and low switching loss, etc.

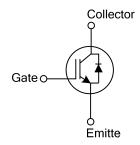
The UTC **UTG12N120-S** is suitable for the resonant or soft switching applications.

■ FEATURES

- * High switching speed
- * High avalanche ruggedness
- * Low saturation voltage: $V_{CE(SAT).Typ.}$ =1.6V @ Ic=12A, V_{GE} =15V (T_C =25°C)



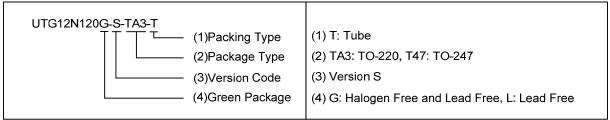
■ SYMBOL



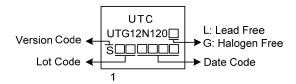
■ ORDERING INFORMATION

Ordering Number		Daalsana	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTG12N120L-S-TA3-T	UTG12N120G-S-TA3-T	TO-220	G	С	Е	Tube	
UTG12N120L-S-T47-T	UTG12N120G-S-T47-T	TO-247	G	С	Е	Tube	

Note: Pin Assignment: G: Gate C: Collector E: Emitter



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage		V_{CES}	1200	V
Gate-Emitter Voltage			±20	V
Transient Gate-emitter voltage (tp < 5 ms)		V_{GES}	±25	V
Continuous Collector Current	=25°C	Ic	24	Α
T _C	=90°C		12	Α
Collector Current Pulsed (Note 1)		I _{CM}	48	Α
T _C	=25°C	l _F	24	Α
Diode Forward Current	=90°C		12	Α
Short Circuit Withstand Time V_{GE} = 15V, $V_{\text{CC}} \le 200\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\ge 1.0\text{s}$ T_{VJ} = 25°C		tsc	8	μs
Power Dissipation (Tc=25°C)	D-220 D-247	P _D	100 240	W
Operating Junction Temperature		TJ	-40 ~ +150	°C
Storage Temperature Range		T_{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are stress ratings only and functional device operation is not implied. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT	
l ti t O	TO-220	0	1.25	°C/W	
Junction to Case	TO-247	Өлс	0.56	°C/W	

^{2.} Pulse width limited by maximum junction temperature.

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS			TYP	MAX	UNIT	
Off Characteristics								
Collector-Emitter Breakdown Voltage	BVces			1200			V	
Collector Cut-Off Current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				5	μA	
G-E Leakage Current	I _{GES}	V _{CE} =0V, V _{GE} =±20V				±100	nA	
On Characteristics								
Gate to Emitter Threshold Voltage	$V_{GE(TH)}$	Ic=250µA, Vce=Vge		2.5		5.0	V	
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	L12A \/15\/	T _C =25°C		1.6	2.1	V	
		I _C =12A, V _{GE} =15V	T _C =125°C		2.0		V	
Dynamic Characteristics								
Input Capacitance	CIES			1340		pF		
Output Capacitance	Coes	V _{CE} =25V, V _{GE} =0V, f=1		65.7		pF		
Reverse Transfer Capacitance	Cres				32.3		pF	
Switching Characteristics								
Total Gate Charge	Q_{G}				99		nC	
Gate-Emitter Charge	Q _{GE}	V _{CE} =600V, I _C =12A, V _{GE} =15V			16.2		nC	
Gate-Collector Charge	Q_{GC}	7			53.7		nC	
Turn-On Delay Time	t _{DON)}	V _{CC} =600V, I _C =12A, R _G =5Ω, V _{GE} =0~15V, L=1000μH			7.8		ns	
Rise Time	t _R				20		ns	
Turn-Off Delay Time	t _{DOFF)}				102		ns	
Fall Time	t⊧				226		ns	
Turn-On Switching Loss	Eon				0.877		mJ	
Turn-Off Switching Loss	Eoff				0.85		mJ	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Forward Voltage Drop	V _F	I _F =12A				2.0	V	
Reverse Recovery Time	t _{rr}	I _F =12A, dI/dt=100A/μS, V _{CC} =600V			55		ns	
Reverse Recovery Charge	Qrr				1.15		μC	

■ TEST CIRCUIT AND WAVEFORMS

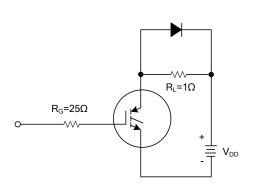


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

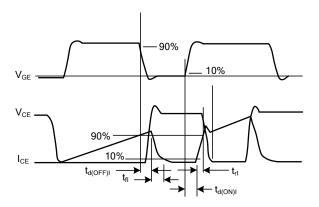
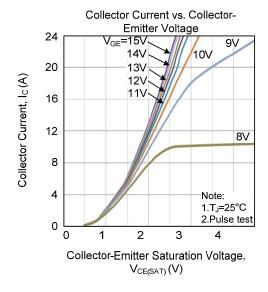
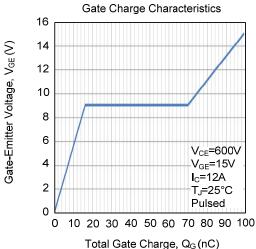
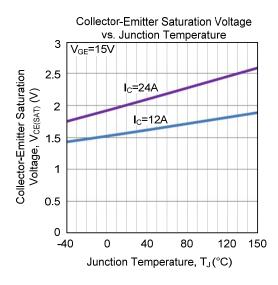


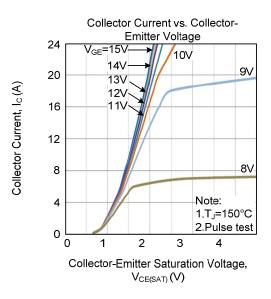
Fig 2. SWITCHING TEST WAVEFORMS

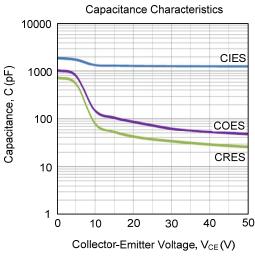
■ TYPICAL CHARACTERISTICS

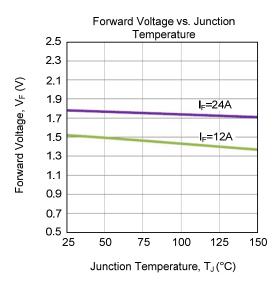




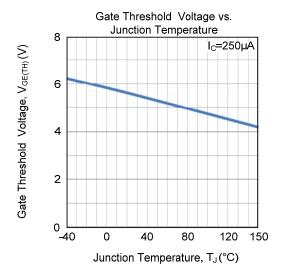


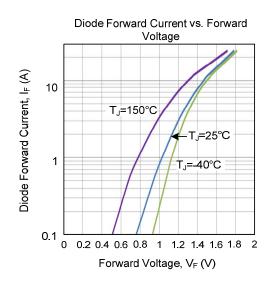


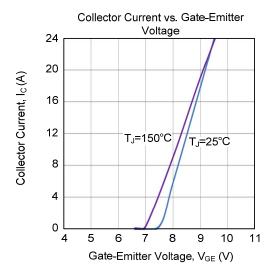


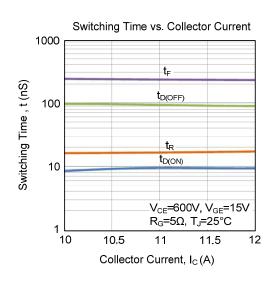


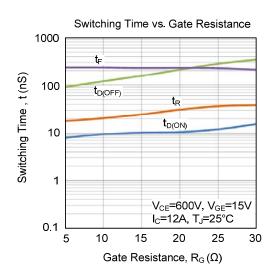
■ TYPICAL CHARACTERISTICS (Cont.)

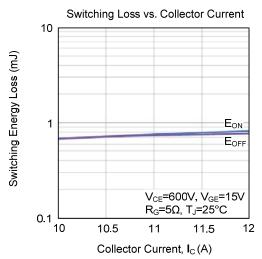




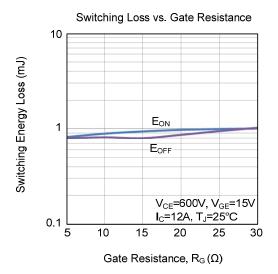


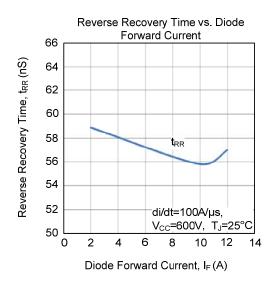


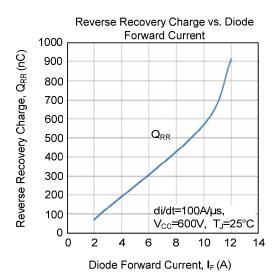


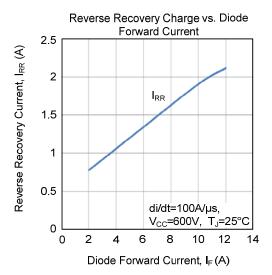


■ TYPICAL CHARACTERISTICS (Cont.)









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