



UTT18P06

Power MOSFET

-18.3A, -60V P-CHANNEL POWER MOSFET

DESCRIPTION

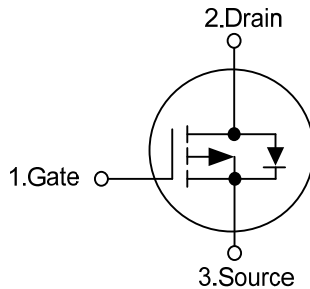
The UTC **UTT18P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and minimum on-state resistance. It can also withstand high energy in the avalanche.

FEATURES

* $R_{DS(ON)} \leq 70 \text{ m}\Omega$ @ $V_{GS} = -10\text{V}$, $I_D = -18.3\text{A}$

* High Switching Speed

SYMBOL



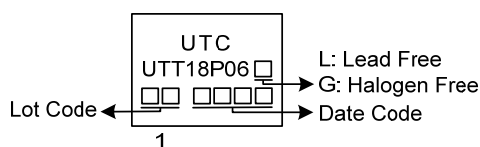
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT18P06L-TA3-T	UTT18P06G-TA3-T	TO-220	G	D	S	Tube
UTT18P06L-TF1-T	UTT18P06G-TF1-T	TO-220F1	G	D	S	Tube
UTT18P06L-TF2-T	UTT18P06G-TF2-T	TO-220F2	G	D	S	Tube
UTT18P06L-TF3-T	UTT18P06G-TF3-T	TO-220F	G	D	S	Tube
UTT18P06L-TM3-T	UTT18P06G-TM3-T	TO-251	G	D	S	Tube
UTT18P06L-TN3-R	UTT18P06G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT18P06G-TA3-T (1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2 TF3: TO-220F, TM3: TO-251, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			V _{DSS}	-60	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current	Continuous	T _C =25°C	I _D	-18.3	A
	Pulsed		I _{DM}	-36	A
Single Pulsed Avalanche Current (L=0.1mH)			I _{AS}	-18.3	A
Single Pulsed Avalanche Energy (L=0.1mH) (Note 3)			E _{AS}	24.2	mJ
Peak Diode Recovery dv/dt (Note 4)			dv/dt	2.4	V/nS
Power Dissipation (Note 4)	T _C =25°C	TO-220	P _D	80	W
		TO-220F		32	W
		TO-220F1			
		TO-220F2			
		TO-251 TO-252		41	W
Junction Temperature			T _J	+150	°C
Storage Temperature			T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. Duty cycle $\leq 1\%$.

4. $I_{SD} \leq -18.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	θ_{JA}	62.5	$^{\circ}\text{C}/\text{W}$
	TO-220F1/TO-220F2		110	$^{\circ}\text{C}/\text{W}$
	TO-251/TO-252			
Junction to Case	TO-220	θ_{JC}	1.56	$^{\circ}\text{C}/\text{W}$
	TO-220F/TO-220F1		3.9	$^{\circ}\text{C}/\text{W}$
	TO-220F2			
	TO-251/TO-252		3.05 (Note 3)	$^{\circ}\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

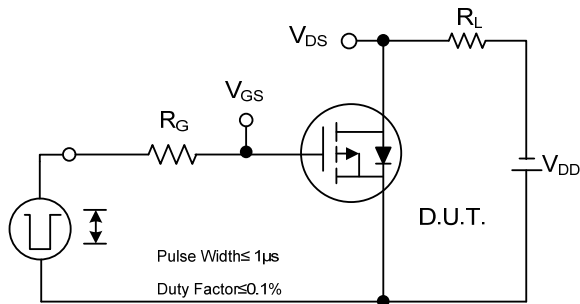
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =-250μA, V _{GS} =0V	-60			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =-10V, I _D =-18.3A		55	70	mΩ
On State Drain Current (Note 1)		I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5.0V	-30			A
DYNAMIC PARAMETERS (Note 2)							
Input Capacitance		C _{ISS}	V _{GS} =0V, V _{DS} =-25V, f=1.0MHz		1415		pF
Output Capacitance		C _{OSS}			100		pF
Reverse Transfer Capacitance		C _{RSS}			81		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q _G	V _{GS} =-10V, V _{DS} =-48V, I _D =-18.3A(Note 1, 2)		33		nC
Gate to Source Charge		Q _{GS}			5		nC
Gate to Drain Charge		Q _{GD}			9		nC
Turn-ON Delay Time		t _{D(ON)}	V _{DD} =-30V, I _D =-18.3A, R _G =3Ω (Note 1, 2)		6		ns
Rise Time		t _R			18		ns
Turn-OFF Delay Time		t _{D(OFF)}			32		ns
Fall-Time		t _F			20		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS (T _C =25°C) (Note 2)							
Maximum Body-Diode Continuous Current		I _S				-18.3	A
Maximum Body-Diode Pulsed Current		I _{SM}				-36	A
Drain-Source Diode Forward Voltage		V _{SD}	I _F =-18.3A, V _{GS} =0V (Note 1)		-1.0	-1.5	V
Body Diode Reverse Recovery Time		t _{rr}	I _F =-18.3A, dI _F /dt=100A/μs		57		ns
Reverse Recovery Charge		Q _{rr}			49		nC

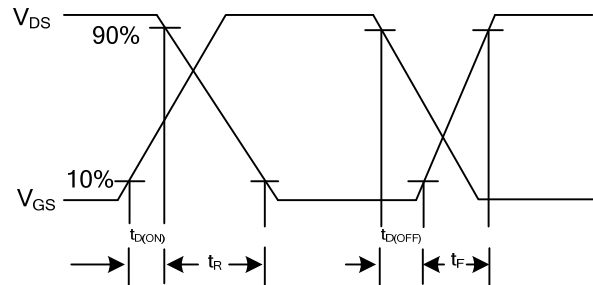
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

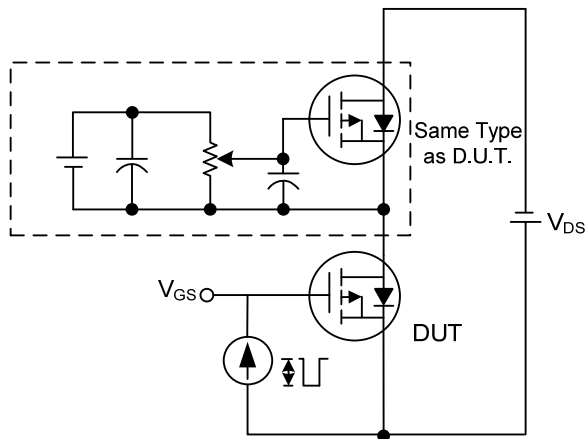
■ TEST CIRCUITS AND WAVEFORMS



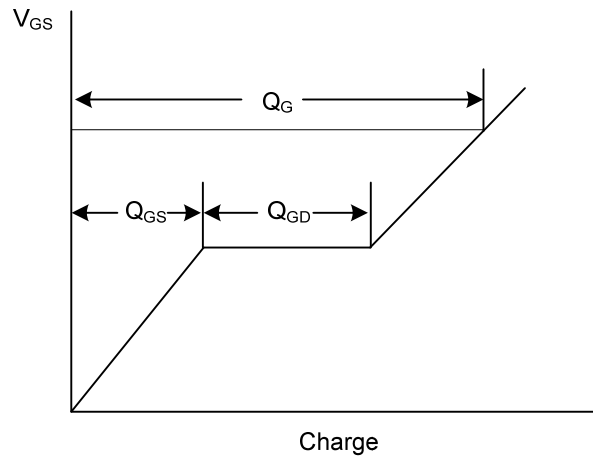
Switching Test Circuit



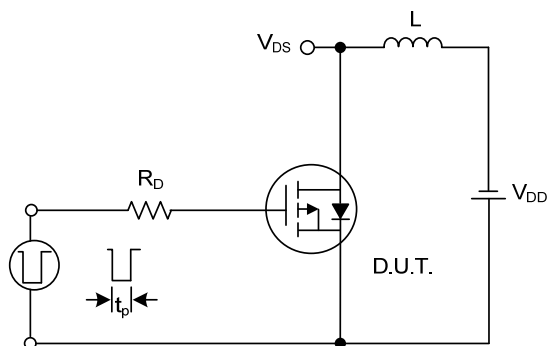
Switching Waveforms



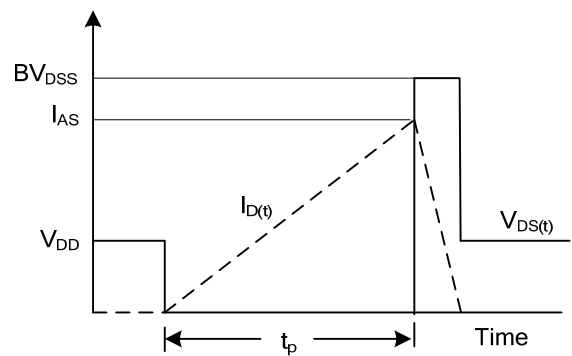
Gate Charge Test Circuit



Gate Charge Waveform

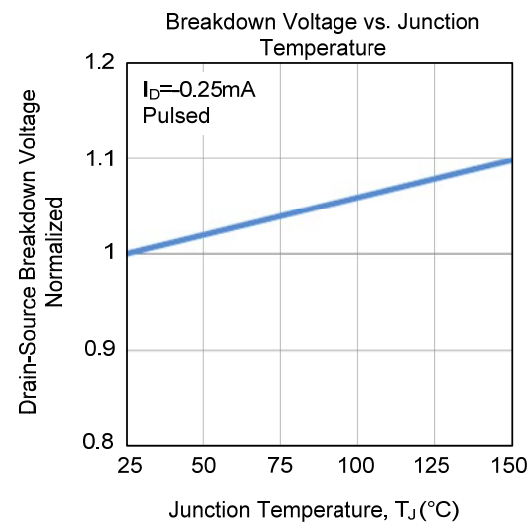
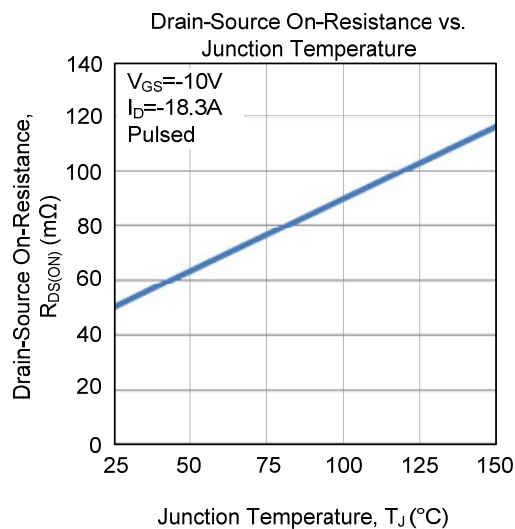
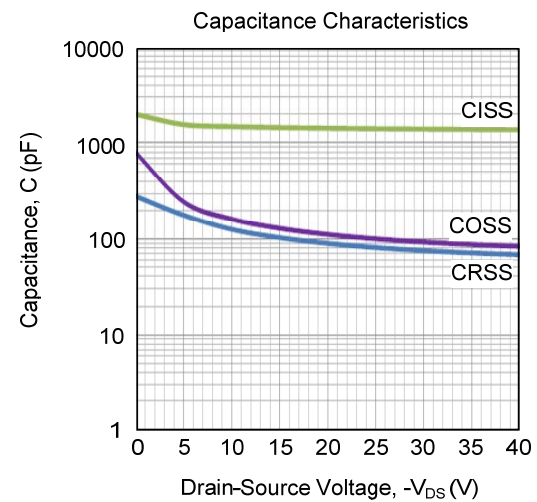
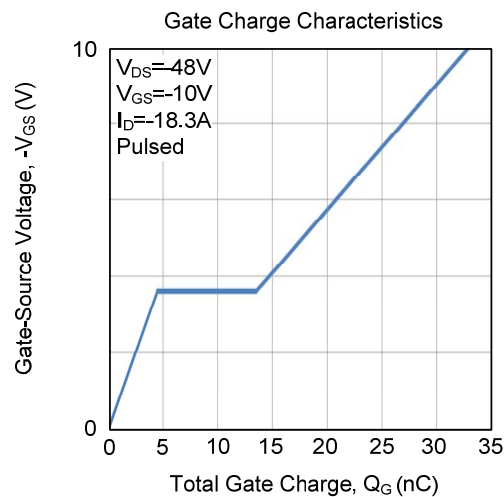
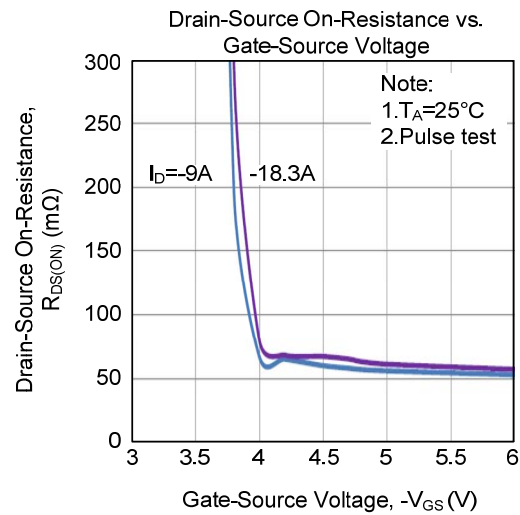
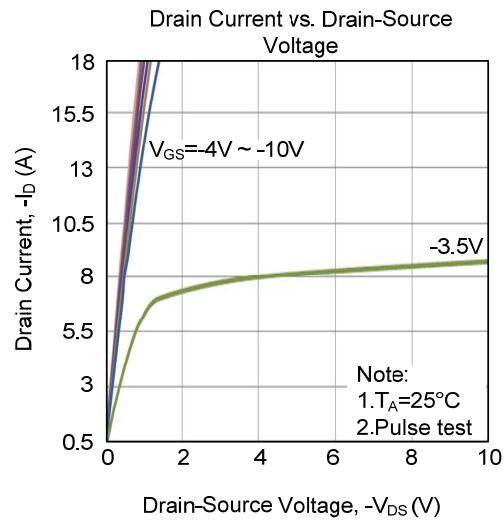


Unclamped Inductive Switching Test Circuit

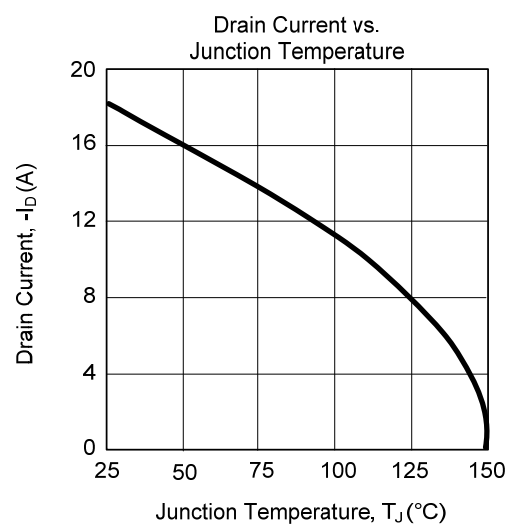
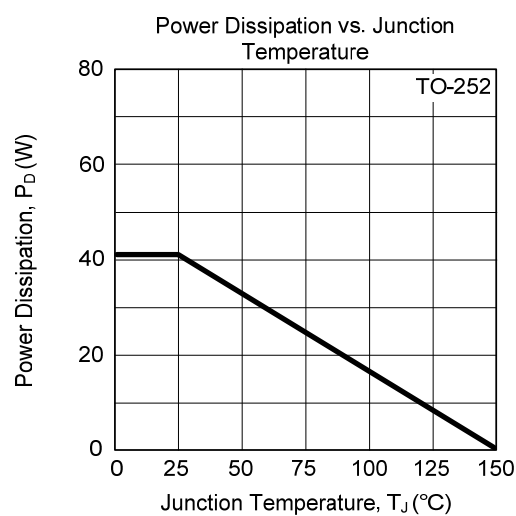
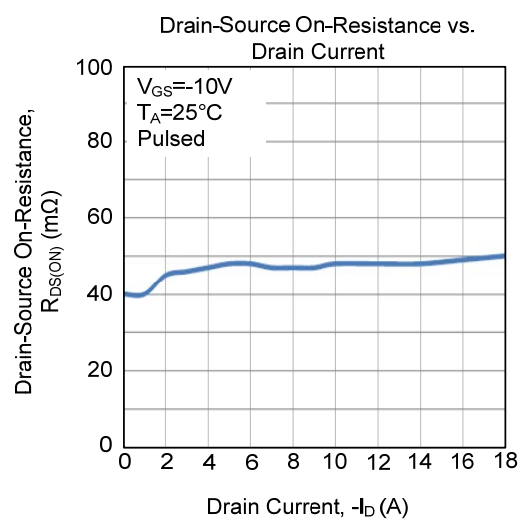
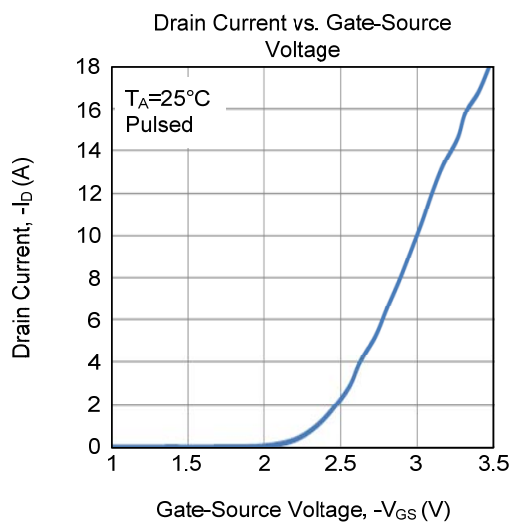
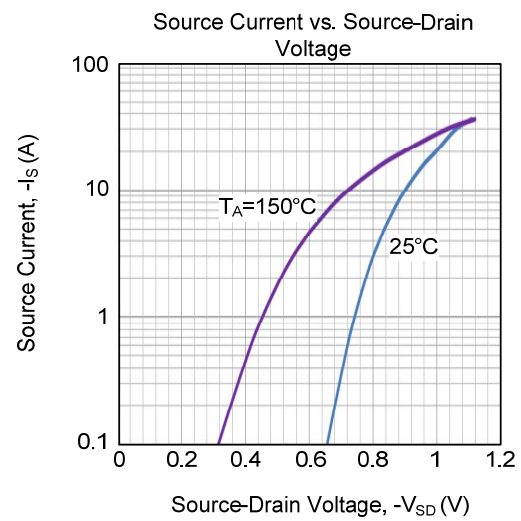
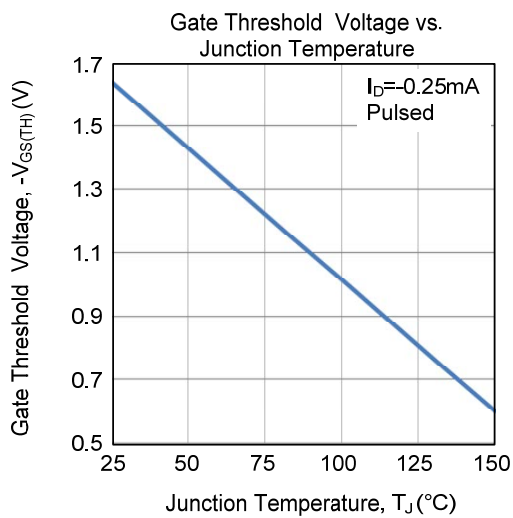


Unclamped Inductive Switching Waveforms

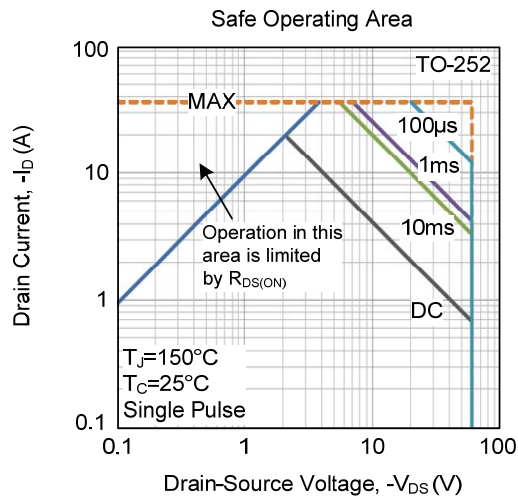
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



TYPICAL CHARACTERISTICS (Cont.)



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