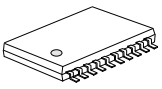
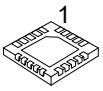
UTXS0108

CMOS IC

8-BIT BIDIRECTIONAL, LEVEL-SHIFTING, VOLTAGE TRANSLATOR FOR OPEN-DRAIN AND PUSH-PULL APPLICATION



TSSOP-20



QFN-20(3×3)

■ DESCRIPTION

This 8-bit non-inverting translator is a bidirectional voltage-level translator and can be used to establish digital switching compatibility between mixed-voltage systems. It uses two separate configurable power-supply rails. The A port is designed to track V_{CCA}. V_{CCA} accepts any supply voltage from 1.4V to 3.6V.The B port is designed to track V_{CCB}. V_{CCA} must be less than or equal to V_{CCB}. V_{CCB} accepts any supply voltage from 1.65V to 5.5V. This allows for low voltage bidirectional translation between any of the 1.5V, 1.8V, 2.5V, 3.3V, and 5V voltage nodes.

When the output-enable(OE) input is low, all outputs are placed in the high-impedance state.

To ensure the high-impedance state during power up or power down, OE should be tied to GND through a pull-down resistor; the minimum value of the resistor is determined by the current-sourcing capability of the driver.

■ FEATURES

* 1.4V to 3.6V on A port and 1.65V to 5.5V on B Port ($V_{CCA} \leq V_{CCB}$)
* No power-supply sequencing required – either V_{CCA} or V_{CCB} can be ramped first
* No direction-control signal needed

■ APPLICATION

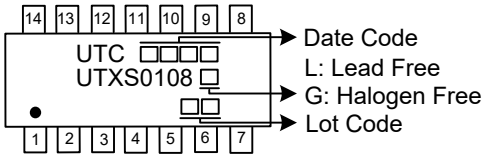
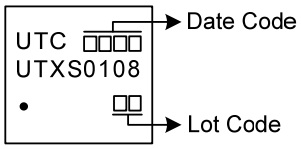
* Handset
* Smartphone
* Tablet
* Desktop PC

■ ORDERING INFORMATION

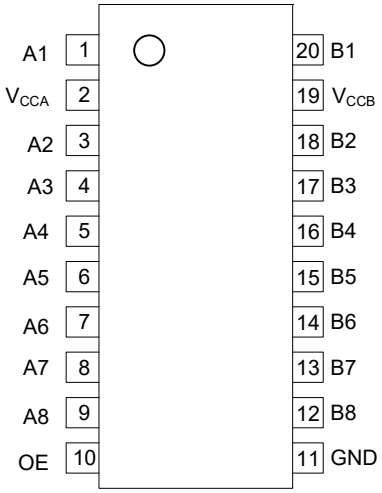
Ordering Number		Package	Packing
Lead Free	Halogen Free		
UTXS0108L-P20-R	UTXS0108G-P20-R	TSSOP-20	Tape Reel
UTXS0108L-QAM-R	UTXS0108G-QAM-R	QFN-20(3×3)	Tape Reel

UTXS0108G-P20-R (1)Packing Type (2) Package Type (3) Green Package	(1) R: Tape Reel (2) P20: TSSOP-20, QAM: QFN-20(3×3) (3) G: Halogen Free and Lead Free, L: Lead Free

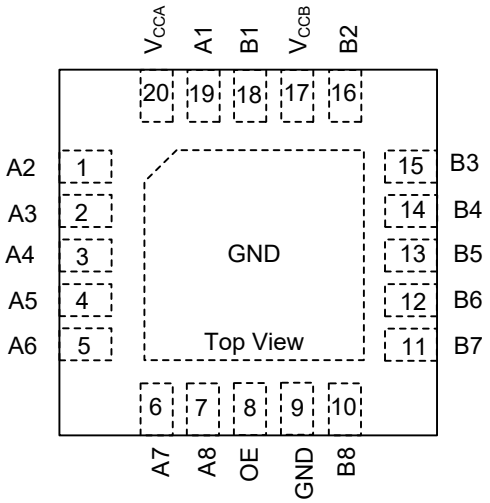
■ MARKING

TSSOP-20	QFN-20(3×3)
	

■ PIN CONFIGURATION



TSSOP-20



QFN3030-20

■ PIN DESCRIPTION

PIN NO.		PIN NAME	I/O	DESCRIPTION
TSSOP-20	QFN-20(3x3)			
1	19	A1	I/O	Input/output A1. Referenced to V_{CCA}
2	20	V_{CCA}	P	A-Port supply voltage.1.4V ≤ V_{CCA} ≤ 3.6V and V_{CCA} ≤ V_{CCB}
3	1	A2	I/O	Input/output A2. Referenced to V_{CCA}
4	2	A3	I/O	Input/output A3. Referenced to V_{CCA}
5	3	A4	I/O	Input/output A4. Referenced to V_{CCA}
6	4	A5	I/O	Input/output A5. Referenced to V_{CCA}
7	5	A6	I/O	Input/output A6. Referenced to V_{CCA}
8	6	A7	I/O	Input/output A7. Referenced to V_{CCA}
9	7	A8	I/O	Input/output A8. Referenced to V_{CCA}
10	8	OE	I	Output enable. Pull OE low to place all outputs in 3-state mode. Referenced to V_{CCA}.
11	9	GND		Ground
12	10	B8	I/O	Input/output B8. Referenced to V_{CCB}
13	11	B7	I/O	Input/output B7. Referenced to V_{CCB}
14	12	B6	I/O	Input/output B6. Referenced to V_{CCB}
15	13	B5	I/O	Input/output B5. Referenced to V_{CCB}
16	14	B4	I/O	Input/output B4. Referenced to V_{CCB}
17	15	B3	I/O	Input/output B3. Referenced to V_{CCB}
18	16	B2	I/O	Input/output B2. Referenced to V_{CCB}
19	17	V_{CCB}	P	B-Port supply voltage.1.65V ≤ V_{CCB} ≤ 5.5V
20	18	B1	I/O	Input/output B1. Referenced to V_{CCB}

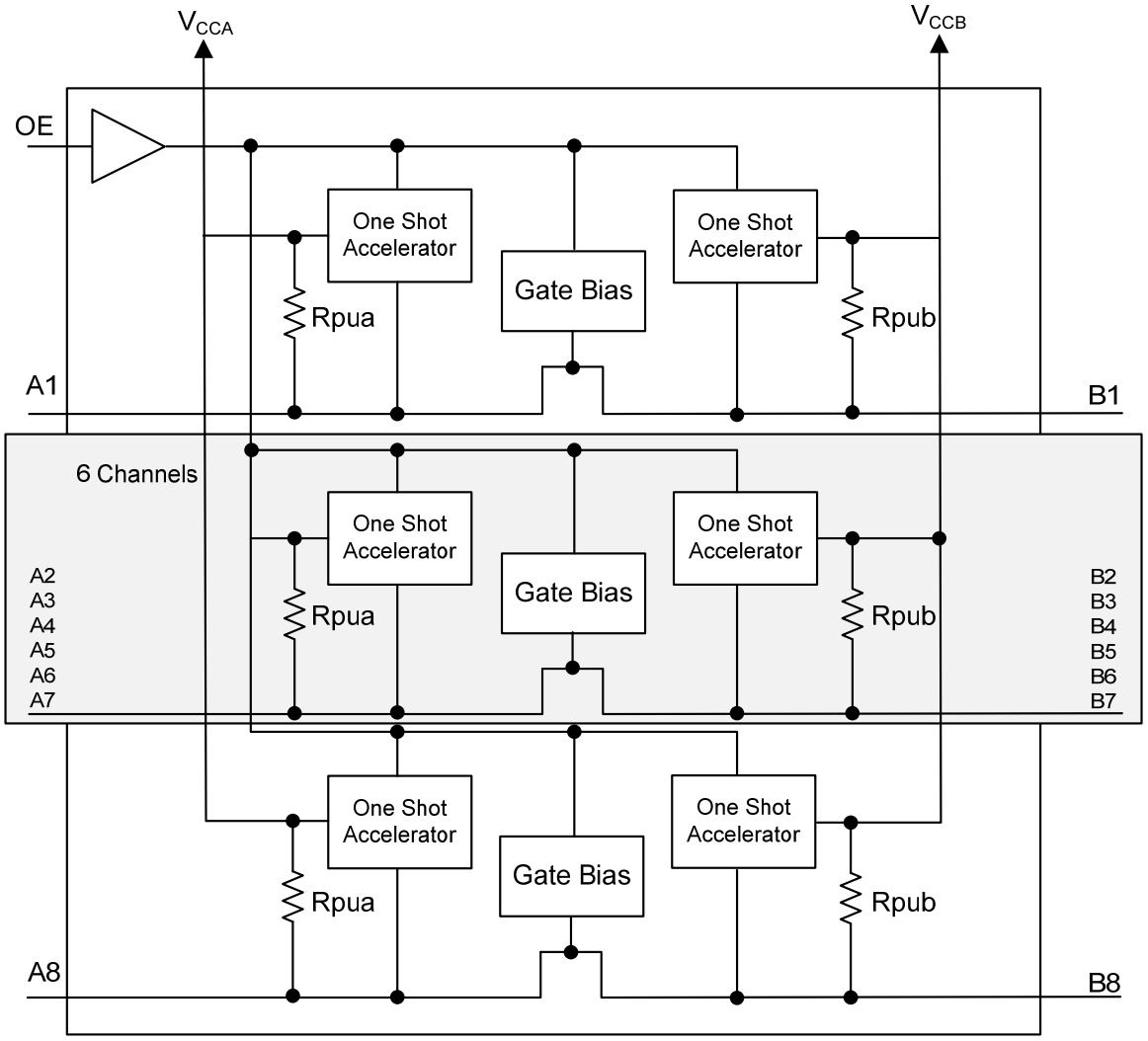
Note: I=Input, I/O=Input and Output, P=Power

■ FUNCTION TABLE

SUPPLY VOLTAGE		INPUTS	INPUTS/OUTPUT	
V_{CCA}	V_{CCB}	OE	An	Bn
1.4V ~ V_{CCB}	1.65V ~ 5.5V	L	Z	Z
1.4V ~ V_{CCB}	1.65V ~ 5.5V	H	Input or Output	Output or Input
GND (Note 2)	GND (Note 2)	X	Z	Z

Notes: 1. H = High voltage level；L = Low voltage level；Z : High impedance OFF-state；X = Don't care.
2. When either V_{CCA} or V_{CCB} is at GND level, the device goes into Power-down mode.

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (Unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Supply voltage		V_{CCA}	-0.5 ~ 4.6	V
Supply voltage		V_{CCB}	-0.5 ~ 6.5	V
Input voltage	A Port	V_{IN}	-0.5 ~ 4.6	V
	B Port		-0.5 ~ 6.5	V
Voltage range applied to any output in the high-impedance or power-off state	A Port	V_{OUT}	-0.5 ~ 4.6	V
	B Port		-0.5 ~ 6.5	V
Voltage range applied to any output in the high or low state	A Port	V_{OUT}	-0.5 ~ V_{CCA}+0.5	V
	B Port		-0.5 ~ V_{CCB}+0.5	V
Input clamp current	V_I<0	I_{IK}	-50	mA
Output clamp current	V_O<0	I_{OK}	-50	mA
Continuous output current		I_O	±50	mA
Continuous current through V_{CCA}, V_{CCB}, or GND		I_{CC} / I_{GND}	±100	mA
Storage Temperature		T_{STG}	-65 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS (Unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage (Note 3)		V_{CCA}		1.4		3.6	V
Supply Voltage (Note 3)		V_{CCB}		1.65		5.5	V
Input Voltage		V_{IN}		0		V_{CCI}	V
Output Voltage	A Port I/Os	V_{OUT}	V_{CCA}=1.4V~3.6V, V_{CCB}=1.65V~5.5V	0		3.6	V
	B Port I/Os			0		5.5	V
High-Level Input Voltage	A Port I/Os	V_{IH}	V_{CCA}=1.4V~1.95V, V_{CCB}=1.65V~5.5V	V_{CCI}-0.2		V_{CCI}	V
			V_{CCA}=1.95V~3.6V, V_{CCB}=1.65V~5.5V	V_{CCI}-0.4		V_{CCI}	V
	B Port I/Os		V_{CCA}=1.4V~3.6V, V_{CCB}=1.65V~5.5V	V_{CCI}-0.4		V_{CCI}	V
	OE			V_{CCA} ×0.65		5.5	V
Low-Level Input Voltage	A Port I/Os	V_{IL}	V_{CCA}=1.4V~1.95V, V_{CCB}=1.65V~5.5V	0		0.15	V
			V_{CCA}=1.95V~3.6V, V_{CCB}=1.65V~5.5V	0		0.15	V
	B Port I/Os		V_{CCA}=1.4V~3.6V, V_{CCB}=1.65V~5.5V	0		0.15	V
	OE			0		V_{CCA} ×0.35	V
Input Transition Rise or Fall Rate	A Port I/Os	Δt/Δv	V_{CCA}=1.4V~3.6V, V_{CCB}=1.65V~5.5V			10	ns/V
	B Port I/Os					10	ns/V
	OE					10	ns/V
Operating Temperature		T_A		-40		+125	°C

Notes: 1. V_{CCI} is the supply voltage associated with the input port.
2. V_{CCO} is the supply voltage associated with the output port.
3. V_{CCA} must be less than or equal to V_{CCB}, and V_{CCA} must not exceed 3.6V.

■ ELECTRICAL CHARACTERISTICS (Unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Port A Output High Voltage		V_{OHA}	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V I_{OH}=-20μA, V_{IB}≥V_{CCB}-0.4V		V_{CCA} ×0.67			V
Port A Output Low Voltage		V_{OLA}	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V, I_{OL}=180μA, V_{IB}≤0.15V				0.4	V
			V_{CCA}=1.65V, V_{CCB}=1.65V~5.5V, I_{OL}=220μA, V_{IB}≤0.15V				0.4	V
			V_{CCA}=2.3V, V_{CCB}=1.65V~5.5V, I_{OL}=300μA, V_{IB}≤0.15V				0.4	V
			V_{CCA}=3.0V, V_{CCB}=1.65V~5.5V, I_{OL}=400μA, V_{IB}≤0.15V				0.55	V
Port B Output High Voltage		V_{OHB}	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V I_{OH}=-20μA, V_{IA}≥V_{CCA}-0.2V		V_{CCB} ×0.67			V
Port B Output Low Voltage		V_{OLB}	V_{CCA}=1.4V~3.6V, V_{CCB}=1.65V, I_{OL}=220μA, V_{IA}≤0.15V				0.4	V
			V_{CCA}=1.4V~3.6V, V_{CCB}=2.3V, I_{OL}=300μA, V_{IA}≤0.15V				0.4	V
			V_{CCA}=1.4V~3.6V, V_{CCB}=3.0V, I_{OL}=400μA, V_{IA}≤0.15V				0.55	V
			V_{CCA}=1.4V~3.6V, V_{CCB}=4.5V, I_{OL}=620μA, V_{IA}≤0.15V				0.55	V
Input Leakage Current	OE	$I_{I(LEAK)}$	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V		-1		1	μA
High-Impedance State Output Current	A or B Port	I_{OZ}	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V		-1		1	μA
Quiescent Supply Current		I_{CCA}	V_{IN}=V_{OUT}=Open I_O=0A	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V		1.5		μA
				V_{CCA}=1.5V~3.6V, V_{CCB}=2.3V~5.5V			2	μA
				V_{CCA}=3.6V, V_{CCB}=0V			2	μA
				V_{CCA}=0V, V_{CCB}=5.5V			-1	μA
		I_{CCB}		V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V		1.5		μA
				V_{CCA}=1.5V~3.6V, V_{CCB}=2.3V~5.5V			6	μA
				V_{CCA}=3.6V, V_{CCB}=0V			-1	μA
				V_{CCA}=0V, V_{CCB}=5.5V			2	μA
		I_{CCA}+I_{CCB}	V_{IN}=V_{CCI} or GND I_O=0A	V_{CCA}=1.4V, V_{CCB}=2.3V~5.5V		3		μA
				V_{CCA}=1.5V~3.6V, V_{CCB}=2.3V~5.5V			8	μA
		I_{CCZA}	V_{IN}=V_{OUT}=Open I_O=0A	V_{CCA}=1.4V, V_{CCB}=1.65V~5.5V OE=GND		0.05		μA
		I_{CCZB}				4		μA
Input Capacitance	OE	C_{IN}	V_{CCA}=3.3V, V_{CCB}=3.3V			4.5		pF
Output Capacitance	A Port	C_{IO}	V_{CCA}=3.3V, V_{CCB}=3.3V			6		pF
	B Port		V_{CCA}=3.3V, V_{CCB}=3.3V			5.5		pF

Notes: 1. V_{CCI} is the supply voltage associated with the input port.
2. V_{CCO} is the supply voltage associated with the output port.
3. V_{CCA} must be less than or equal to V_{CCB}, and V_{CCA} must not exceed 3.6V.

■ SWITCHING CHARACTERISTICS (Unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation Delay From Input (A) to Output (B)	Push-Pull Driving	t_{PHL}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			12.5	ns
				V_{CCB}=2.5V±0.2V			10.7	ns
				V_{CCB}=3.3V±0.3V			10.1	ns
				V_{CCB}=5V±0.5V			10.1	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			15.9	ns
				V_{CCB}=2.5V±0.2V			14.3	ns
				V_{CCB}=3.3V±0.3V			13.7	ns
				V_{CCB}=5V±0.5V			13.5	ns
	Push-Pull Driving		V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			11.2	ns
				V_{CCB}=2.5V±0.2V			8.8	ns
				V_{CCB}=3.3V±0.3V			8	ns
				V_{CCB}=5V±0.5V			7.4	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			12.9	ns
				V_{CCB}=2.5V±0.2V			11.4	ns
				V_{CCB}=3.3V±0.3V			10.8	ns
				V_{CCB}=5V±0.5V			10.4	ns
	Push-Pull Driving		V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			7.7	ns
				V_{CCB}=3.3V±0.3V			6.8	ns
				V_{CCB}=5V±0.5V			6.2	ns
	Open-Drain Driving			V_{CCB}=2.5V±0.2V			8.4	ns
				V_{CCB}=3.3V±0.3V			7.8	ns
				V_{CCB}=5V±0.5V			7.3	ns
	Push-Pull Driving		V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			6.4	ns
				V_{CCB}=5V±0.5V			5.7	ns
	Open-Drain Driving			V_{CCB}=3.3V±0.3V			6.8	ns
				V_{CCB}=5V±0.5V			6.3	ns

■ SWITCHING CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation Delay From Input (B) to Output (A)	Push-Pull Driving	t_{PHL}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			14.2	ns
				V_{CCB}=2.5V±0.2V			12.6	ns
				V_{CCB}=3.3V±0.3V			12.5	ns
				V_{CCB}=5V±0.5V			13.5	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			14.7	ns
				V_{CCB}=2.5V±0.2V			11.1	ns
				V_{CCB}=3.3V±0.3V			10	ns
				V_{CCB}=5V±0.5V			9	ns
	Push-Pull Driving		V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			11.3	ns
				V_{CCB}=2.5V±0.2V			9.5	ns
				V_{CCB}=3.3V±0.3V			8.9	ns
				V_{CCB}=5V±0.5V			8.5	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			13.6	ns
				V_{CCB}=2.5V±0.2V			10	ns
				V_{CCB}=3.3V±0.3V			8.7	ns
				V_{CCB}=5V±0.5V			7.6	ns
	Push-Pull Driving		V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			7.4	ns
				V_{CCB}=3.3V±0.3V			6.3	ns
				V_{CCB}=5V±0.5V			5.7	ns
	Open-Drain Driving			V_{CCB}=2.5V±0.2V			8.8	ns
				V_{CCB}=3.3V±0.3V			7.5	ns
				V_{CCB}=5V±0.5V			6.4	ns
	Push-Pull Driving		V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			6.5	ns
				V_{CCB}=5V±0.5V			5.3	ns
	Open-Drain Driving			V_{CCB}=3.3V±0.3V			7	ns
				V_{CCB}=5V±0.5V			6	ns

■ SWITCHING CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation Delay From Input (A) to Output (B)	Push-Pull Driving	t_{PLH}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			14.1	ns
				V_{CCB}=2.5V±0.2V			11.5	ns
				V_{CCB}=3.3V±0.3V			11.3	ns
				V_{CCB}=5V±0.5V			11.2	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			800	ns
				V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			12.8	ns
				V_{CCB}=2.5V±0.2V			9.9	ns
				V_{CCB}=3.3V±0.3V			8.9	ns
				V_{CCB}=5V±0.5V			8	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			800	ns
				V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			8.3	ns
				V_{CCB}=3.3V±0.3V			7.4	ns
				V_{CCB}=5V±0.5V			6.7	ns
	Open-Drain Driving			V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			6.7	ns
				V_{CCB}=5V±0.5V			6.1	ns
	Open-Drain Driving			V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns

■ SWITCHING CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Propagation Delay From Input (B) to Output (A)	Push-Pull Driving	t_{PLH}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			12	ns
				V_{CCB}=2.5V±0.2V			8.4	ns
				V_{CCB}=3.3V±0.3V			7.1	ns
				V_{CCB}=5V±0.5V			6.1	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			800	ns
				V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			11.7	ns
				V_{CCB}=2.5V±0.2V			8.5	ns
				V_{CCB}=3.3V±0.3V			7.3	ns
				V_{CCB}=5V±0.5V			6.5	ns
	Open-Drain Driving			V_{CCB}=1.8V±0.15V			800	ns
				V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			7.7	ns
				V_{CCB}=3.3V±0.3V			6.1	ns
				V_{CCB}=5V±0.5V			5.1	ns
	Open-Drain Driving			V_{CCB}=2.5V±0.2V			600	ns
				V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns
	Push-Pull Driving		V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			6.2	ns
				V_{CCB}=5V±0.5V			5.8	ns
	Open-Drain Driving			V_{CCB}=3.3V±0.3V			500	ns
				V_{CCB}=5V±0.5V			400	ns

■ SWITCHING CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Enable Time From Input (OE) to Output (A or B)		t_{en}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			200	ns
				V_{CCB}=2.5V±0.2V			200	ns
				V_{CCB}=3.3V±0.3V			200	ns
				V_{CCB}=5V±0.5V			200	ns
			V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			200	ns
				V_{CCB}=2.5V±0.2V			200	ns
				V_{CCB}=3.3V±0.3V			200	ns
				V_{CCB}=5V±0.5V			200	ns
			V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			200	ns
				V_{CCB}=3.3V±0.3V			200	ns
				V_{CCB}=5V±0.5V			200	ns
			V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			200	ns
				V_{CCB}=5V±0.5V			200	ns
Disable Time From Input (OE) to Output (A or B)		t_{dis}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			400	ns
				V_{CCB}=2.5V±0.2V			400	ns
				V_{CCB}=3.3V±0.3V			400	ns
				V_{CCB}=5V±0.5V			400	ns
			V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			400	ns
				V_{CCB}=2.5V±0.2V			400	ns
				V_{CCB}=3.3V±0.3V			400	ns
				V_{CCB}=5V±0.5V			400	ns
			V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			400	ns
				V_{CCB}=3.3V±0.3V			400	ns
				V_{CCB}=5V±0.5V			400	ns
			V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			400	ns
				V_{CCB}=5V±0.5V			400	ns
Data Rate	Push-Pull Driving	f_{data}	V_{CCA}=1.5V±0.1V	V_{CCB}=1.8V±0.15V			40	Mbps
				V_{CCB}=2.5V±0.2V			50	Mbps
				V_{CCB}=3.3V±0.3V			50	Mbps
				V_{CCB}=5V±0.5V			50	Mbps
	Open-drain Driving			V_{CCB}=1.8V±0.15V			0.8	Mbps
				V_{CCB}=2.5V±0.2V			0.8	Mbps
				V_{CCB}=3.3V±0.3V			1	Mbps
				V_{CCB}=5V±0.5V			1	Mbps
Pulse Duration	Push-Pull Driving	t_W		V_{CCB}=1.8V±0.15V	25			ns
				V_{CCB}=2.5V±0.2V	20			ns
				V_{CCB}=3.3V±0.3V	20			ns
				V_{CCB}=5V±0.5V	20			ns
	Open-drain Driving			V_{CCB}=1.8V±0.15V	1250			ns
				V_{CCB}=2.5V±0.2V	1250			ns
				V_{CCB}=3.3V±0.3V	1000			ns
				V_{CCB}=5V±0.5V	1000			ns
Data Rate	Push-Pull Driving	f_{data}	V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V			40	Mbps
				V_{CCB}=2.5V±0.2V			60	Mbps
				V_{CCB}=3.3V±0.3V			60	Mbps
				V_{CCB}=5V±0.5V			60	Mbps
	Open-drain Driving			V_{CCB}=1.8V±0.15V			0.8	Mbps
				V_{CCB}=2.5V±0.2V			0.8	Mbps
				V_{CCB}=3.3V±0.3V			0.8	Mbps
				V_{CCB}=5V±0.5V			1	Mbps

■ SWITCHING CHARACTERISTICS (Cont.)

PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Pulse Duration	Push-Pull Driving	t_W	V_{CCA}=1.8V±0.15V	V_{CCB}=1.8V±0.15V	25			ns
				V_{CCB}=2.5V±0.2V	16.7			ns
				V_{CCB}=3.3V±0.3V	16.7			ns
				V_{CCB}=5V±0.5V	16.7			ns
	Open-drain Driving			V_{CCB}=1.8V±0.15V	1250			ns
				V_{CCB}=2.5V±0.2V	1250			ns
				V_{CCB}=3.3V±0.3V	1000			ns
				V_{CCB}=5V±0.5V	1000			ns
Data Rate	Push-Pull Driving	f_{data}	V_{CCA}=2.5V±0.2V	V_{CCB}=2.5V±0.2V			60	Mbps
				V_{CCB}=3.3V±0.3V			60	Mbps
				V_{CCB}=5V±0.5V			60	Mbps
	Open-drain Driving			V_{CCB}=2.5V±0.2V			0.8	Mbps
				V_{CCB}=3.3V±0.3V			0.8	Mbps
				V_{CCB}=5V±0.5V			1	Mbps
Pulse Duration	Push-Pull Driving	t_W		V_{CCB}=2.5V±0.2V	16.7			ns
				V_{CCB}=3.3V±0.3V	16.7			ns
				V_{CCB}=5V±0.5V	16.7			ns
	Open-drain Driving			V_{CCB}=2.5V±0.2V	1250			ns
				V_{CCB}=3.3V±0.3V	1250			ns
				V_{CCB}=5V±0.5V	1000			ns
Data Rate	Push-Pull Driving	f_{data}	V_{CCA}=3.3V±0.3V	V_{CCB}=3.3V±0.3V			60	Mbps
				V_{CCB}=5V±0.5V			60	Mbps
	Open-drain Driving			V_{CCB}=3.3V±0.3V			0.8	Mbps
				V_{CCB}=5V±0.5V			1.2	Mbps
Pulse Duration	Push-Pull Driving	t_W		V_{CCB}=3.3V±0.3V	16.7			ns
				V_{CCB}=5V±0.5V	16.7			ns
	Open-drain Driving			V_{CCB}=3.3V±0.3V	1250			ns
				V_{CCB}=5V±0.5V	1000			ns

■ OPERATING CHARACTERISTICS (Unless otherwise specified)

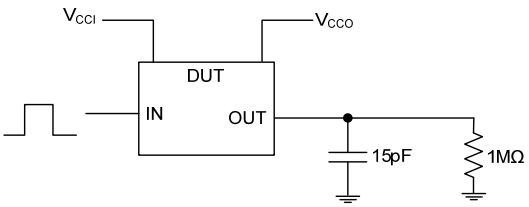
PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	A Port Input B Port Output	C_{PDA}	C_L=0, f=10MHz t_r=t_f=1nS OE=V_{CCA} (Output Enabled)	V_{CCA}=1.5V V_{CCB}=1.5V		6.1		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		6.2		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		7		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		8.2		pF
	B Port Input A Port Output			V_{CCA}=1.5V V_{CCB}=1.5V		10.1		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		10.2		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		10.4		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		10.5		pF
	A Port Input B Port Output		C_L=0, f=10MHz t_r=t_f=1nS OE=GND (Output Disabled)	V_{CCA}=1.5V V_{CCB}=1.5V		0.01		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		0.01		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		0.01		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		0.01		pF
	B Port Input A Port Output			V_{CCA}=1.5V V_{CCB}=1.5V		0.01		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		0.01		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		0.01		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		0.01		pF

■ OPERATING CHARACTERISTICS (Cont.)

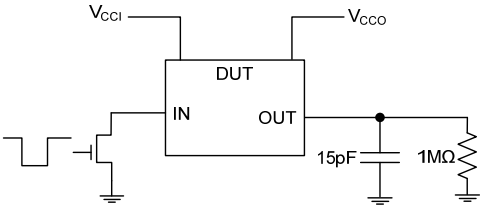
PARAMETER		SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Power Dissipation Capacitance	A Port Input B Port Output	C_{PDB}	C_L=0, f=10MHz t_r=t_f=1nS OE=V_{CCA} (Output Enabled)	V_{CCA}=1.5V V_{CCB}=1.5V		21.7		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		21.2		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		21.3		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		23.2		pF
	B Port Input A Port Output			V_{CCA}=1.5V V_{CCB}=1.5V		16.9		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		17		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		18		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		21.2		pF
	A Port Input B Port Output		C_L=0, f=10MHz t_r=t_f=1nS OE=GND (Output Disabled)	V_{CCA}=1.5V V_{CCB}=1.5V		0.01		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		0.01		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		0.01		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		0.02		pF
	B Port Input A Port Output			V_{CCA}=1.5V V_{CCB}=1.5V		0.01		pF
				V_{CCA}=1.8V V_{CCB}=1.8V		0.01		pF
				V_{CCA}=2.5V V_{CCB}=2.5V		0.01		pF
				V_{CCA}=3.3V V_{CCB}=3.3V		0.02		pF

■ TEST CIRCUIT AND WAVEFORMS

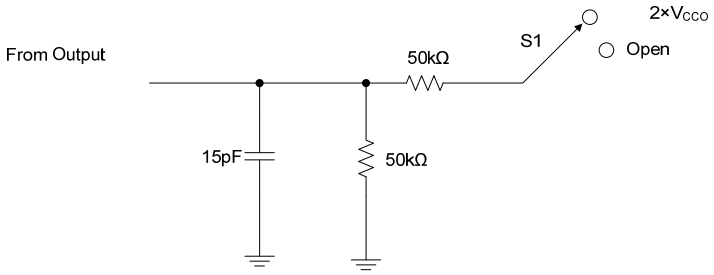
Load Circuits



Data Rate, Pulse Duration, Propagation Delay, Output Rise-Time and Fall-Time Measurement Using a Push-Pull Driver

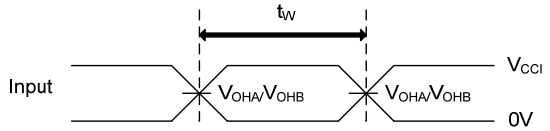


Data Rate, Pulse Duration, Propagation Delay, Output Rise-Time and Fall-Time Measurement Using an Open-Drain Driver

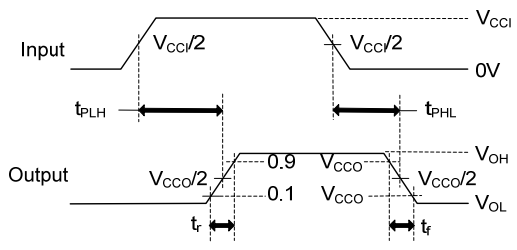


TEST	S1
t_{PZL}/t_{PLZ}	2×V_{CCO}
t_{PHZ}/t_{PZH}	Open

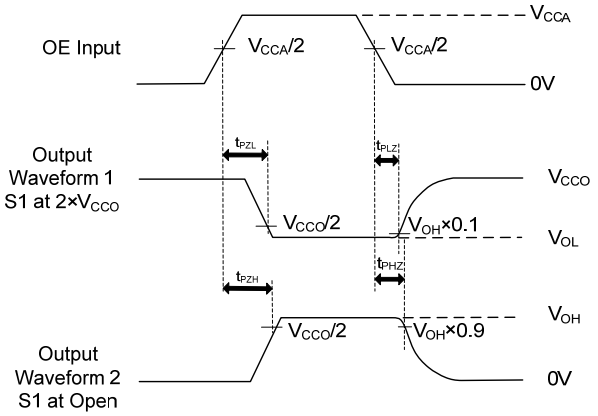
Notes: 1. V_{CCI} is the supply voltage associated with the input.
2. V_{CCO} is the supply voltage associated with the output.
3. t_{en} is the same as t_{PZL} and t_{PZH}.
t_{dis} is the same as t_{PLZ} and t_{PHZ}.



PULSE DURATION



PROPAGATION DELAY TIMES



ENABLE AND DISABLE TIMES

■ DETAILED DESCRIPTION

Overview

The **UTXS0108** can be used in level-translation applications for interfacing devices or systems operating at different interface voltages with one another. The **UTXS0108** is ideal for use in applications where an open-drain driver is connected to the data I/Os. The **UTXS0108** device is a semi-buffered auto-direction-sensing voltage translator design is optimized for translation applications (e.g. MMC Card Interfaces) that require the system to start out in a low-speed open-drain mode and then switch to a higher speed push-pull mode.

Architecture

To address these application requirements, a semi-buffered architecture design is used and is illustrated below (see Figure 1). Edge-rate accelerator circuitry (for both the high-to-low and low-to-high edges), a High-Ron n-channel pass-gate transistor (on the order of 300Ω to 500Ω) and pull-up resistors (to provide DC-bias and drive capabilities) are included to realize this solution. A direction-control signal (to control the direction of data flow from A to B or from B to A) is not needed. The resulting implementation supports both low-speed open-drain operation as well as high-speed push-pull operation.

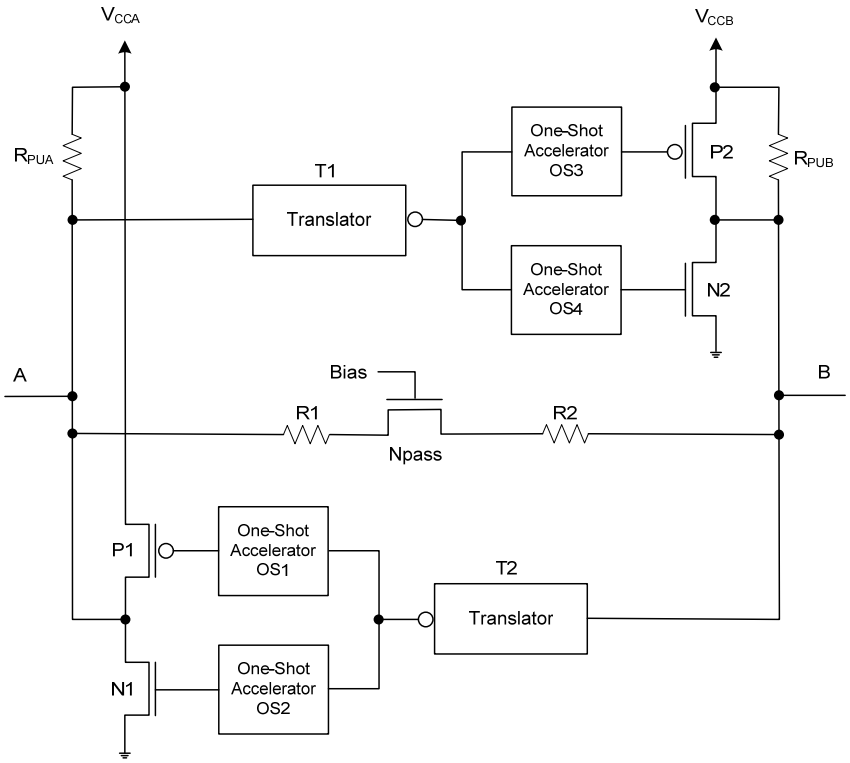


Figure 1. Architecture of UTXS0108 I/O Cell

When transmitting data from A to B ports, during a rising edge the One-Shot (OS3) turns on the PMOS transistor (P2) for a short-duration and this speeds up the low-to-high transition. Similarly, during a falling edge, when transmitting data from A to B, the One-Shot (OS4) turns on NMOS transistor (N2) for a short-duration and this speeds up the high-to-low transition. The B port edge-rate accelerator consists of one-shots OS3 and OS4, Transistors P2 and N2 and serves to rapidly force the B port high or low when a corresponding transition is detected on the A port.

When transmitting data from B to A ports, during a rising edge the One-Shot (OS1) turns on the PMOS transistor (P1) for a short-duration and this speeds up the low-to-high transition. Similarly, during a falling edge, when transmitting data from B to A, the One-Shot (OS2) turns on NMOS transistor (N1) for a short-duration and this speeds up the high-to-low transition. The A port edge-rate accelerator consists of one-shots OS1 and OS2, Transistors P1 and N1 and serves to rapidly force the A port high or low when a corresponding transition is detected on the B port.

■ DETAILED DESCRIPTION (Cont.)

Power-Up

During operation, ensure that $V_{CCA} \leq V_{CCB}$ at all times. During power-up sequencing, $V_{CCA} \geq V_{CCB}$ does not damage the device, so any power supply can be ramped up first.

Enable and Disable

The **UTXS0108** has an OE input that is used to disable the device by setting OE low, which places all I/Os in the Hi-Z state. The disable time (t_{dis}) indicates the delay between the time when OE goes low and when the outputs actually become disabled (Hi-Z). The enable time (t_{en}) indicates the amount of time the user must allow for the one-shot circuitry to become operational after OE is taken high.

Pull-up or Pull-down Resistors on I/O Lines

Each A port I/O has a pull-up resistor (R_{pua}) to V_{CCA} and each B port I/O has a pull-up resistor (R_{pub}) to V_{CCB}. When the output is driving low, R_{pua} and R_{pub} have a value of 40kΩ. When the output is driving high, R_{pua} and R_{pub} have a value of 4kΩ. If OE is taken low , R_{pua} and R_{pub} will be disable.

■ TYPICAL APPLICATION CIRCUIT